



## General Description

The IRFR120NTRPBF use advanced SGT MOSFET technology

to provide low  $R_{DS(ON)}$ , low gate charge, fast switching

and excellent avalanche characteristics.

This device is specially designed to get better ruggedness

and suitable to use in.

## General Features

$V_{DS} = 100V$   $I_D = 12A$

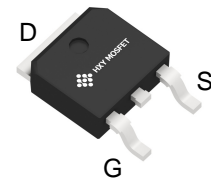
$R_{DS(ON)} < 120m\Omega$  @  $V_{GS}=10V$

## Applications

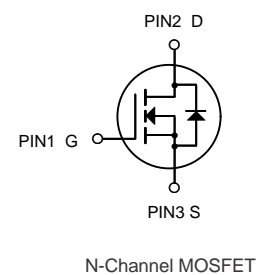
Consumer electronic power supply

Motor control

Synchronous-rectification



TO-252-2L  
(TO-252-2(DPAK))



## Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
IRFR120NTRPBF	TO-252-2L(TO-252-2(DPAK))	12N10 XXXXX	2500

## Absolute Maximum Ratings at $T_j=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain source voltage	100	V
$V_{GS}$	Gate source voltage	$\pm 20$	V
$I_D$	Continuous drain current <sup>1)</sup>	12	A
$I_D$ , pulse	Pulsed drain current <sup>2)</sup>	24	A
$P_D$	Power dissipation <sup>3)</sup>	17	W
EAS	Single pulsed avalanche energy <sup>4)</sup>	1.2	mJ
$T_{stg}$ , $T_j$	Operation and storage temperature	-55 to 150	$^{\circ}C$
$R_{\theta JC}$	Thermal resistance, junction-case	6.6	$^{\circ}C/W$
$R_{\theta JA}$	Thermal resistance, junction-ambient <sup>5)</sup>	62	$^{\circ}C/W$



## Electrical Characteristics $T_c=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	100	110	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	1	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
On Characteristics <small>note3</small>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.0	1.8	3.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance <small>note2</small>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	95	120	mΩ
Dynamic Characteristics <small>note4</small>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	196	-	pF
C <sub>oss</sub>	Output Capacitance		-	25.9	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	21.4	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 50V, I <sub>D</sub> = 3A, V <sub>GS</sub> = 10V	-	4.3	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	3.5	-	nC
Q <sub>gd</sub>	Gate-Drain(“Miller”) Charge		-	3.1	-	nC
Switching Characteristics <small>note4</small>						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 50V, I <sub>DS</sub> =3A  R <sub>G</sub> = 2Ω, V <sub>GEN</sub> = 10V	-	14.7	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	3.5	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	20.9	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	2.7	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current <small>note2</small>		-	-	4.5	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	12	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage <small>note3</small>	V <sub>GS</sub> = 0V, I <sub>S</sub> =3A	-	-	1.3	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>F</sub> = 3A,  di/dt =100A/μs	-	32.1	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Time Charge		-	39.4	-	nC
I <sub>rrm</sub>	Peak Reverse Recovery Current		-	2.1	-	A

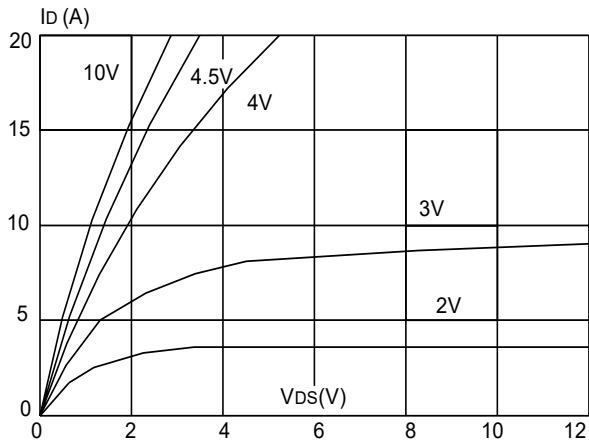
Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5.  $V_{DD}=50$  V,  $R_G=50$   $\Omega$ ,  $L=0.3$  mH, starting  $T_J=25$   $^{\circ}\text{C}$

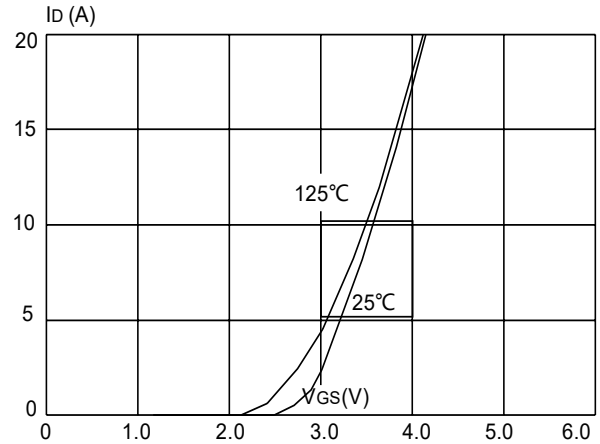


## Typical Performance Characteristics

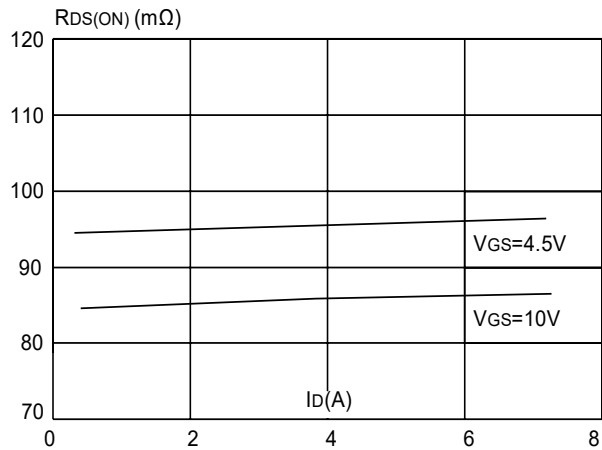
**Figure1: Output Characteristics**



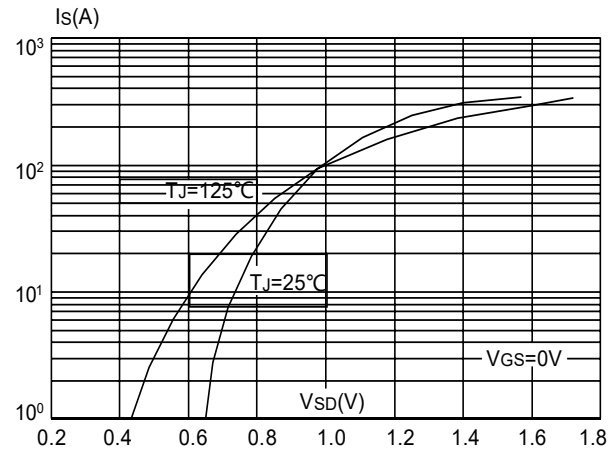
**Figure 2: Typical Transfer Characteristics**



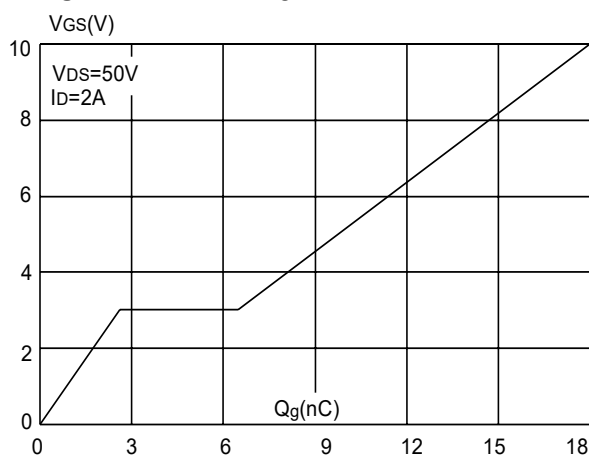
**Figure 3: On-resistance vs. Drain Current**



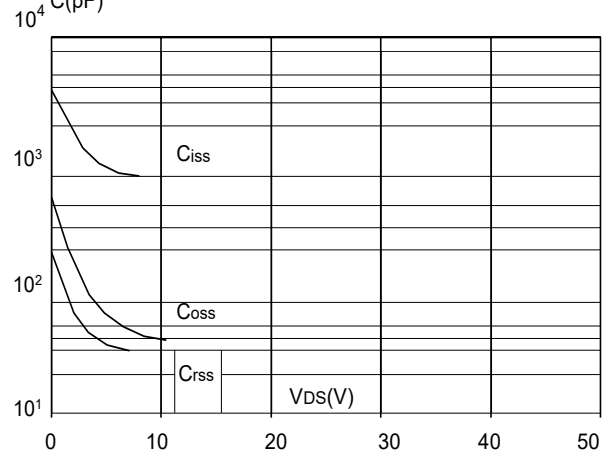
**Figure 4 : Body Diode Characteristics**



**Figure 5: Gate Charge Characteristics**

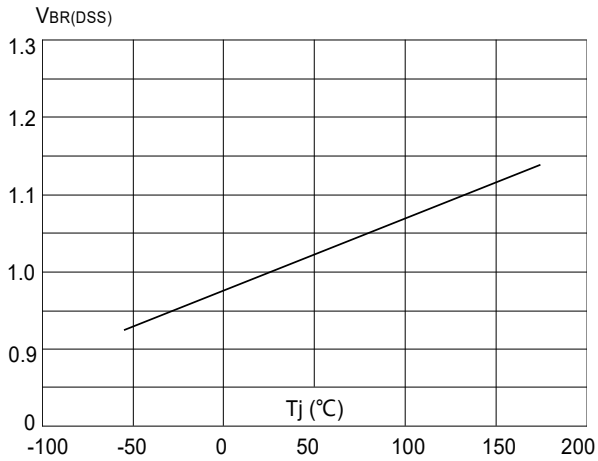


**Figure 6: Capacitance Characteristics**

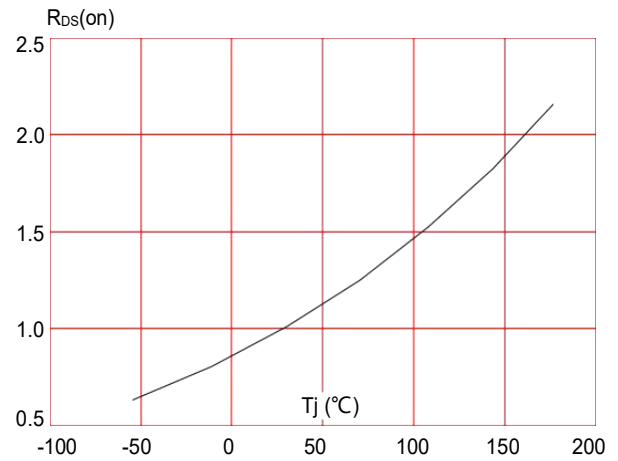




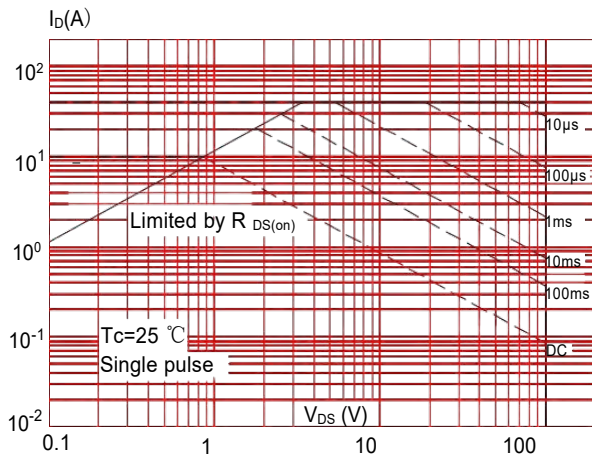
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



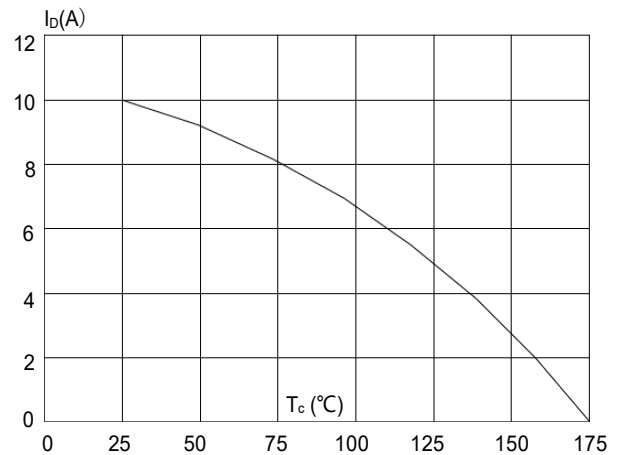
**Figure 8:** Normalized on Resistance vs. Junction Temperature



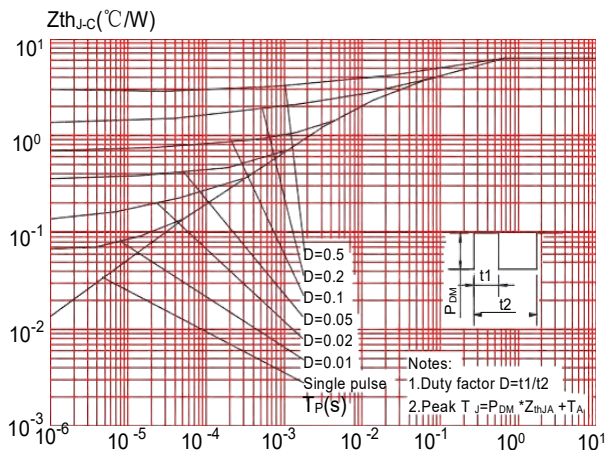
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

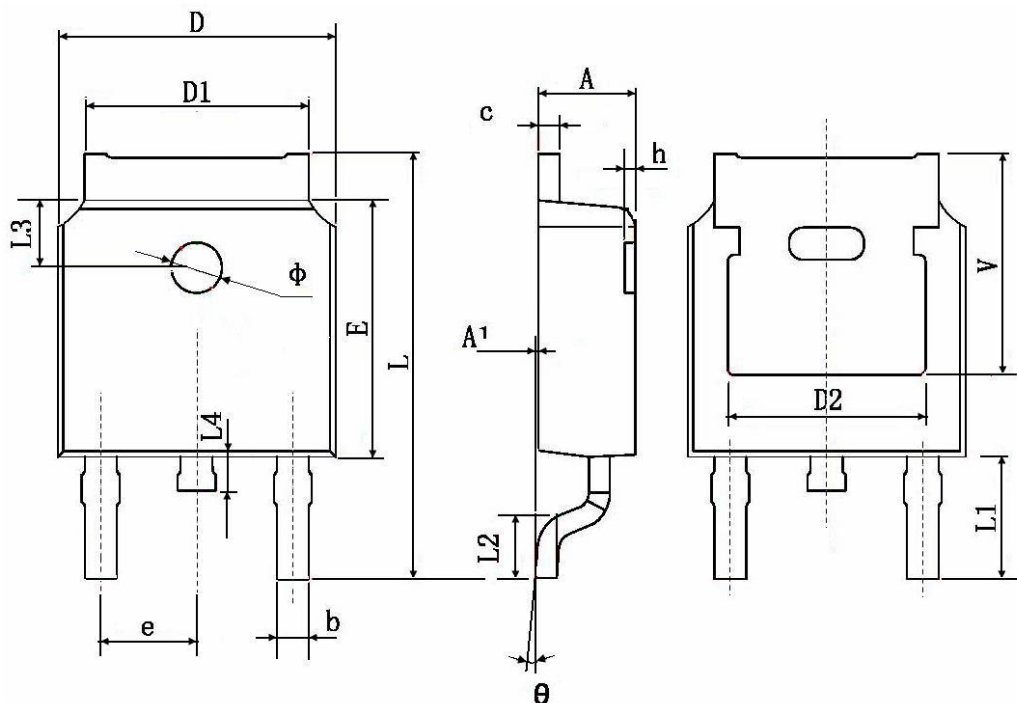


**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case





## TO-252-2L(TO-252-2(DPAK)) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	0.483 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	



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