



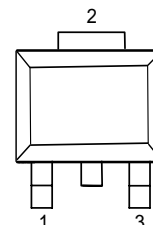
Descripon

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 1.5A of load current with an output voltage adjustable over a 1.2 to 37V.

It employs internal current limiting, thermal shut-down and safe area compensation.

Pin Configuration

TO-252-2L(DPAK)



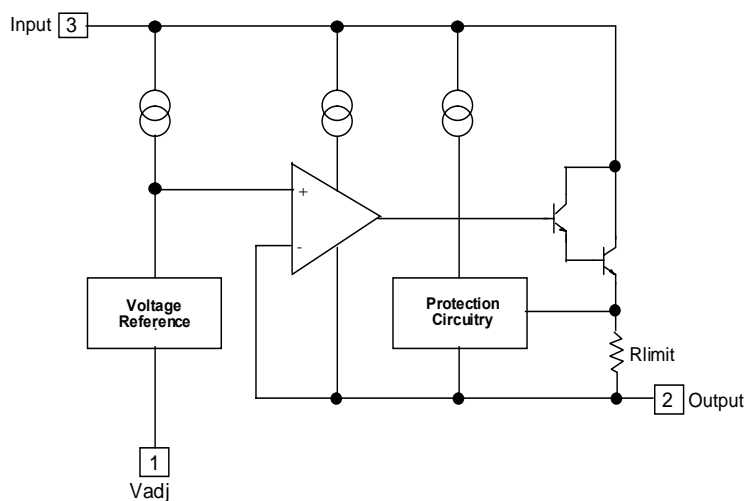
Features

- Internal thermal overload protection
- Internal short circuit current limiting
- Output transistor safe operating area compensation

Pin Cescription

PIN No.	Name	Functions Description
1	ADJ	Adjustable
2	V_{OUT}	Output Voltage
3	V_{IN}	Input Voltage

Internal Bock Diahram





Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_I-V_O	Input-Output Voltage Differential	40	V
T_{LEAD}	Lead Temperature	230	°C
P_D	Power Dissipation	Internally limited	W
T_J	Operating Junction Temperature Range	0~125	°C
T_{stg}	Storage Temperature Range	-55~125	
$\Delta V_O / \Delta T$	Temperature Coefficient of Output Voltage	±0.02	%/°C

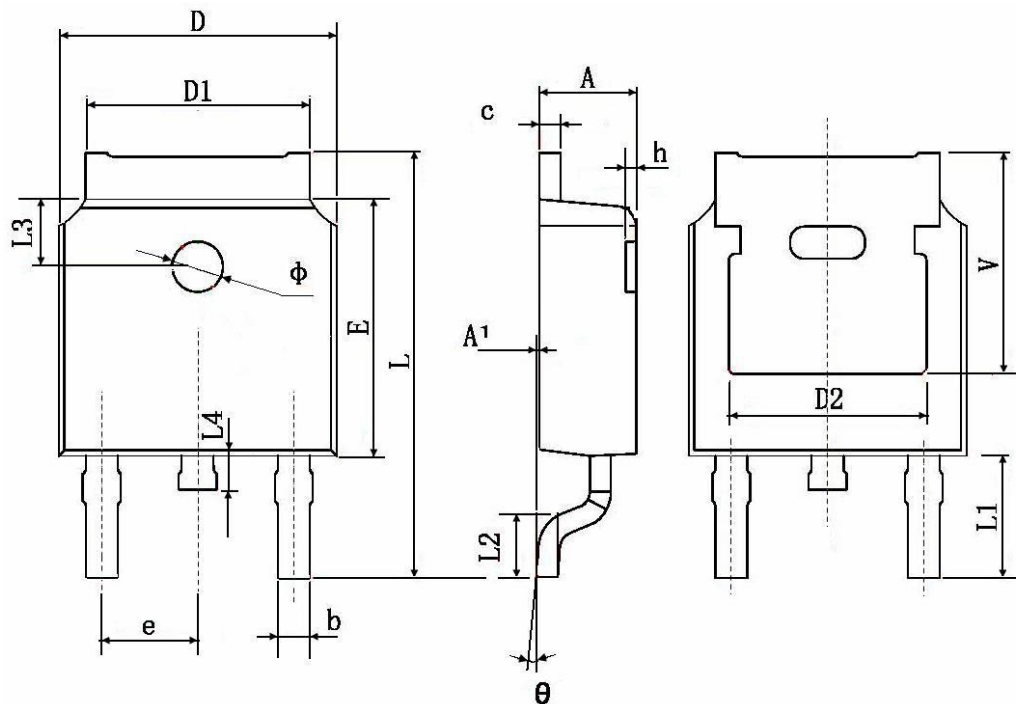
Electrical Characteristics

($V_O-V_I=5V$, $I_O=0.5A$, $0^\circ C \leq T_J \leq +125^\circ C$, $I_{MAX}=1.5A$, $P_{DMAX}=20W$, unless otherwise specified)

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Line Regulation(note1)	R_{line}	$T_A=25^\circ C$ $3V \leq V_I-V_O \leq 40V$		0.01	0.04	%V
		$3V \leq V_I-V_O \leq 40V$		0.02	0.07	
Load Regulation(note1)	R_{load}	$T_A=25^\circ C$, $10mA \leq I_O \leq I_{MAX}$ $V_O < 5V$ $V_O \geq 5V$		18 0.4	25 0.5	mV
		$10mA \leq I_O \leq I_{MAX}$ $V_O < 5V$ $V_O \geq 5V$		40 0.8	70 1.5	% V_O
Adjustable Pin Current	I_{ADJ}	-		46	100	μA
Adjustable Pin Current Change	ΔI_{ADJ}	$3V \leq V_I-V_O \leq 40V$ $10mA \leq I_O \leq I_{MAX}$, $P_D \leq P_{MAX}$		2.0	5	
Reference Voltage	V_{REF}	$3V \leq V_I-V_O \leq 40V$ $10mA \leq I_O \leq I_{MAX}$, $P_D \leq P_{MAX}$	1.20	1.25	1.30	V
Temperature Stability	ST_T	-		0.7		%/ V_O
Minimum Load Current to Maintain Regulation	$I_{L(MIN)}$	$V_I-V_O=40V$		3.5	12	mA
Maximum Output Current	$I_{O(MAX)}$	$V_I-V_O \leq 15V$, $P_D \leq P_{MAX}$ $V_I-V_O \leq 40V$, $P_D \leq P_{MAX}$ $T_A=25^\circ C$	1.0	2.2 0.3		A
RMS Noise,% of V_{OUT}	e_N	$T_A=25^\circ C$, $10Hz \leq f \leq 10KHz$		0.003	0.01	%/ V_O
Ripple Rejection	RR	$V_O=10V$, $f=120Hz$ without C_{ADJ} $C_{ADJ}=10\mu F$ (note2)	66	60 75		dB
Long-Term Stability, $T_J=T_{HIGH}$	ST	$T_A=25^\circ C$ for end point measurements, 1000HR		0.3	1	%
Thermal Resistance Junction to case	$R_{\theta JC}$	-		5		°C/W



TO-252-2L(DPAK) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	0.483 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	



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