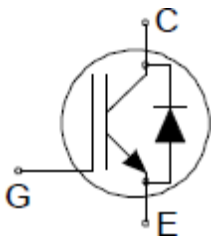
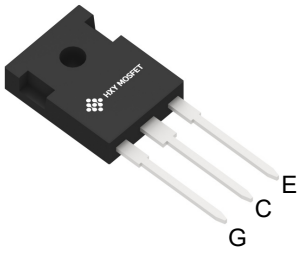
1. Description

RGS80TSX2HRC11 is obtained by advanced Trench and Field Stop (T-FS) technology which reduces the conduction loss,improves switching performance and enhances the avalanche energy.



Device Per Unit

Package Type	Unit	Quantity
TO-247	Tube	30



TO-247

Key Characteristics

Parameter	Value	Unit
V_{CES}	1200	V
I_C	40	A
$V_{CE(sat).typ}$	1.70	V

Features

- Positive temperature coefficient
- Fast Switching
- Low $V_{CE(sat)}$
- Reliable and Rugged
- AEC-Q101 Qualified
- 175℃ operating temperature
- Halogen Free and Green Devices Available
- (RoHS Compliant)

Applications

- PTC
- Motor drives
- OBC

2. Absolute ratings

Symbol	Parameter	Values	Units
V_{CES}	Collector-Emitter Voltage	1200	V
I_C	Collector Current @T_C=25°C	80	A
	Collector Current @T_C=100°C	40	A
I_{CM}	Pulsed Collector Current, tp limited by T_{Jmax}	160	A
I_F	Diode Continuous Forward Current @T_C=25°C	80	A
	Diode Continuous Forward Current @T_C=100°C	40	A
I_{FM}	Diode Maximum Forward Current, limited by T_{Jmax}	160	A
V_{GES}	Gate-Emitter Voltage	±30	V
t_{SC}	Short circuit withstand time V_{GE}=15V, V_{CC}≤400V, allowed number of short circuits<1000,times between short circuits≥1.0s,T_J≤175°C	13	μs
P_D	Power Dissipation @T_C=25°C	441	W
T_{Jmax}, T_{stg}	Operating Junction and Storage Temperature Range	–55 to 175	℃
T_L	Maximum Temperature for Soldering	260	℃

3. Thermal characteristics

Symbol	Parameter	Values	Units
$R_{\theta JC}$	Junction-to-Case (IGBT)	0.34	℃/W
$R_{\theta JC}$	Junction-to-Case (Diode)	0.80	℃/W
$R_{\theta JA}$	Junction-to-Ambient	40	℃/W

4. Electrical Characteristics （at T_c = 25°C, unless otherwise specified）

Static Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
V_{CES}	Collector-Emitter Breakdown Voltage	V_{GE} =0V I_C=1mA	1200	--	--	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V_{GE} =15V I_C =40A T_J=25℃ T_J=125℃ T_J=175℃	-- --	1.70 2.07 2.20	2.05 --	V

$V_{GE(TH)}$	Gate Threshold Voltage	V_{CE}=V_{GE},I_C=1mA	4.3	5.3	6.3	V
V_F	Diode Forward Voltage	I_F=40A T_J=25℃ T_J=125℃ T_J=175℃	-- -- --	1.85 1.70 1.61	2.25 -- --	V
I_{CES}	Collector-Emitter Leakage Current	V_{CE}=1200V V_{GE}=0V	--	--	10	μA
$I_{GES(F)}$	Gate-Emitter Forward Leakage Current	V_{GE}=+20V	--	--	200	nA
$I_{GES(R)}$	Gate-Emitter Reverse Leakage Current	V_{GE}=-20V	--	--	-200	nA
Pulse width tp≤300μs, δ≤2%						

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
C_{ies}	Input Capacitance	V_{GE}=0V V_{CE}=25V f=1.0MHz	--	3980	--	pF
C_{oes}	Output Capacitance		--	157	--	
C_{res}	Reverse Transfer Capacitance		--	93	--	
Q_g	Gate charge	V_{CC}=960V I_{CE}=40A V_{GE}=15V	--	346	--	nC
Q_{ge}	Gate-emitter charge		--	2.4	--	
Q_{gc}	Gate-collector charge		--	238	--	

IGBT Switching Characteristics, at T_J=25℃

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-on Delay Time	I_C=40A V_{CC}=600V V_{GE}=15V R_g=5Ω Inductive Load	--	25	--	ns
t_r	Rise Time		--	28	--	
$t_{d(off)}$	Turn-Off Delay Time		--	262	--	
t_f	Fall Time		--	149	--	
E_{on}	Turn-On Switching Loss		--	1.30	--	mJ
E_{off}	Turn-Off Switching Loss		--	2.30	--	
E_{ts}	Total Switching Loss		--	3.60	--	

IGBT Switching Characteristics, at T_J=175℃

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-on Delay Time	I_C=40A V_{CC}=600V V_{GE}=15V R_g=5Ω Inductive Load	--	26	--	ns
t_r	Rise Time		--	35	--	
$t_{d(off)}$	Turn-Off Delay Time		--	331	--	
t_f	Fall Time		--	224	--	
E_{on}	Turn-On Switching Loss		--	2.20	--	mJ
E_{off}	Turn-Off Switching Loss		--	3.70	--	
E_{ts}	Total Switching Loss		--	5.90	--	

Diode Characteristics, at T_J=25℃

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
T_{rr}	Reverse Recovery Time	I_F=40A V_{CC}=600V di/dt=200A/μs	--	94	-	ns
Q_{rr}	Reverse Recovery Charge		--	225	-	nC
I_{rrm}	Reverse Recovery Current		--	9.7	-	A

Diode Characteristics, at T_J=175℃

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
T_{rr}	Reverse Recovery Time	I_F=40A V_{CC}=600V di/dt=200A/μs	--	125	-	ns
Q_{rr}	Reverse Recovery Charge		--	277	-	nC
I_{rrm}	Reverse Recovery Current		--	11.2	-	A

5. Characteristics Curves

Figure 1: Power Dissipation

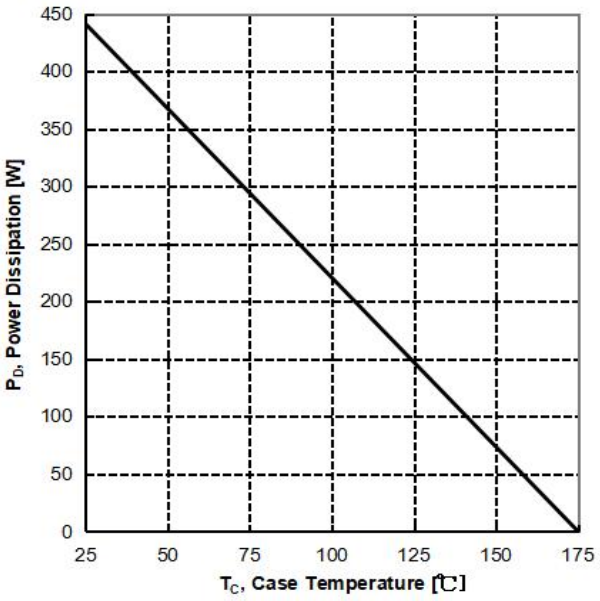


Figure 2: Collector Current vs. Case Temperature

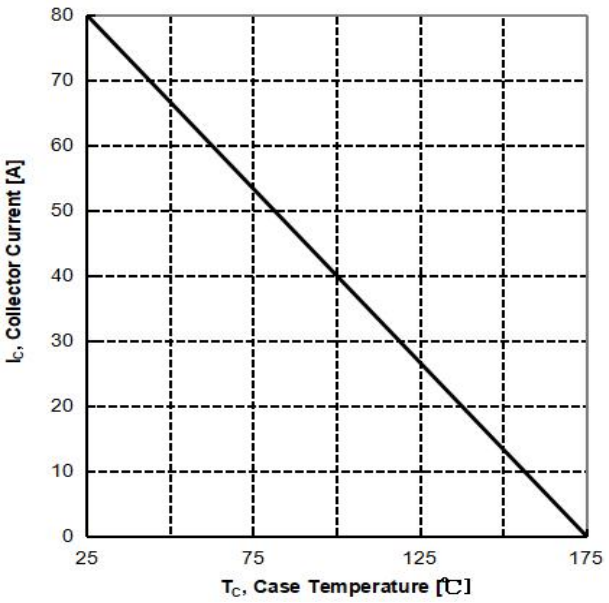


Figure 3: Safe Operation Area

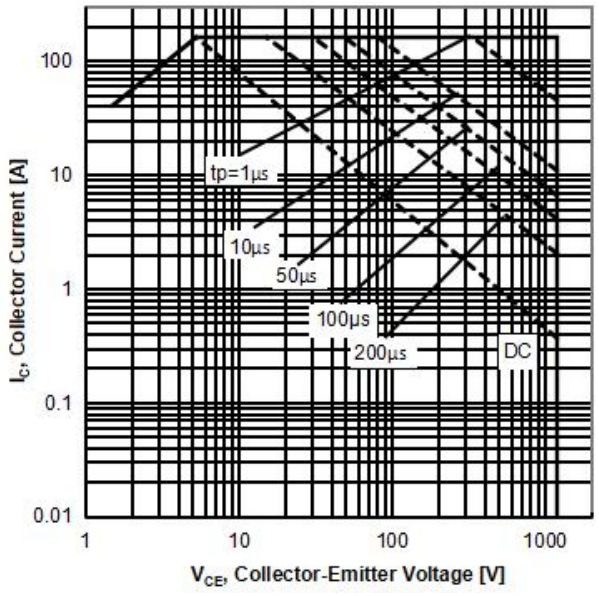


Figure 4: Typical Transfer Characteristics

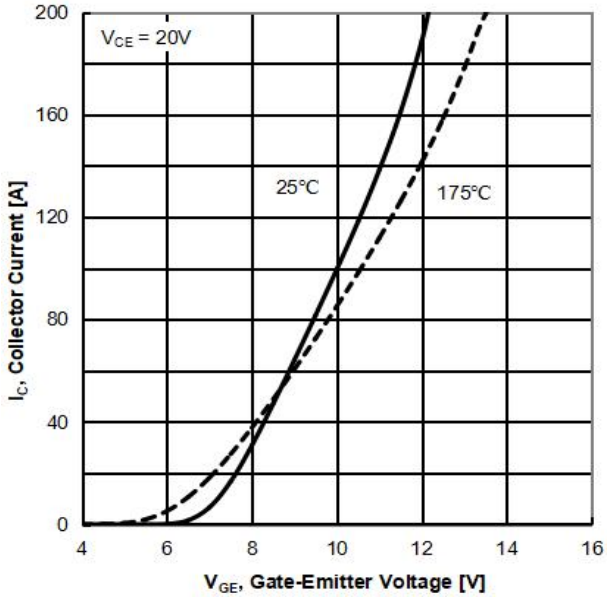


Figure 5: Typical Output Characteristics

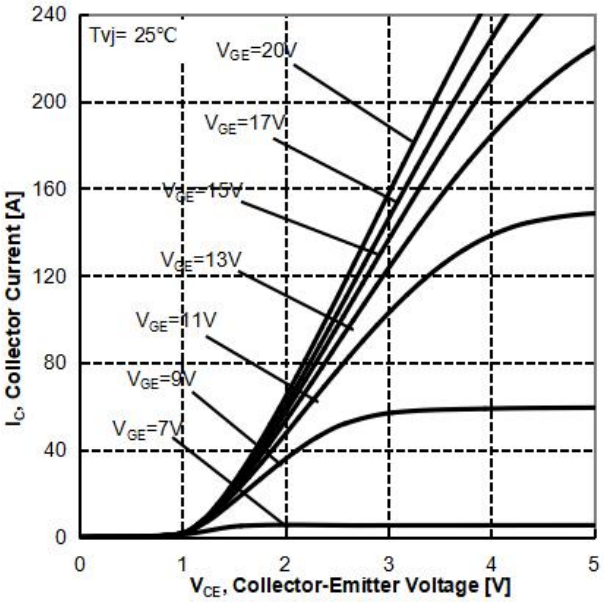


Figure 6: Typical Output Characteristics

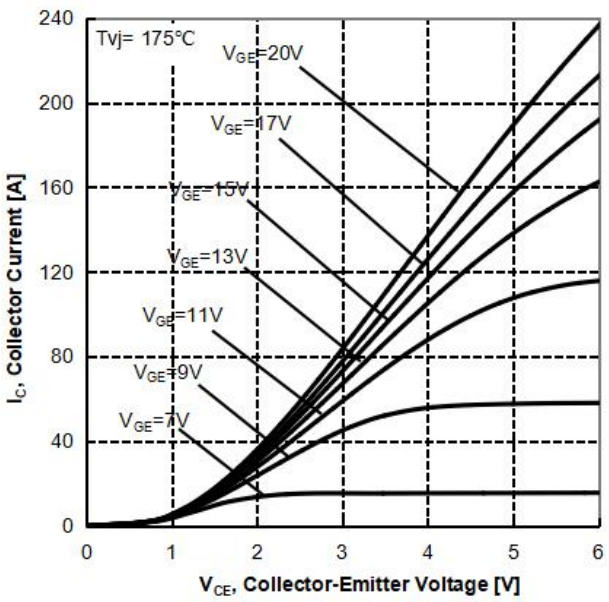


Figure 7: Typical Collector-Emitter Saturation Voltage vs. Junction Temperature

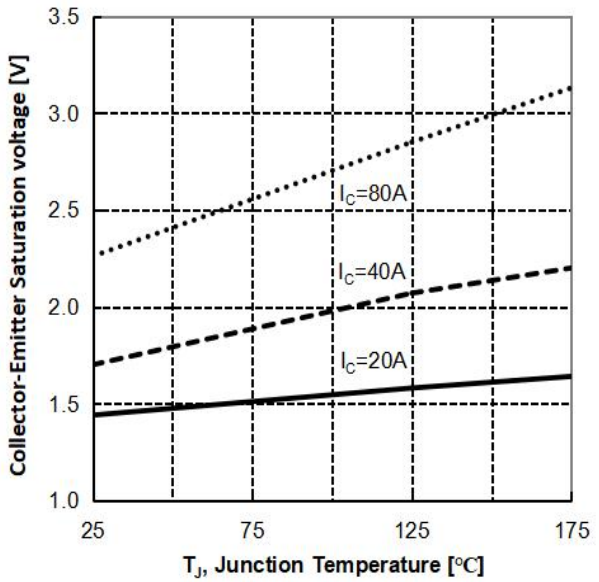


Figure 8: Typical Gate-Emitter Threshold Voltage vs. Junction Temperature

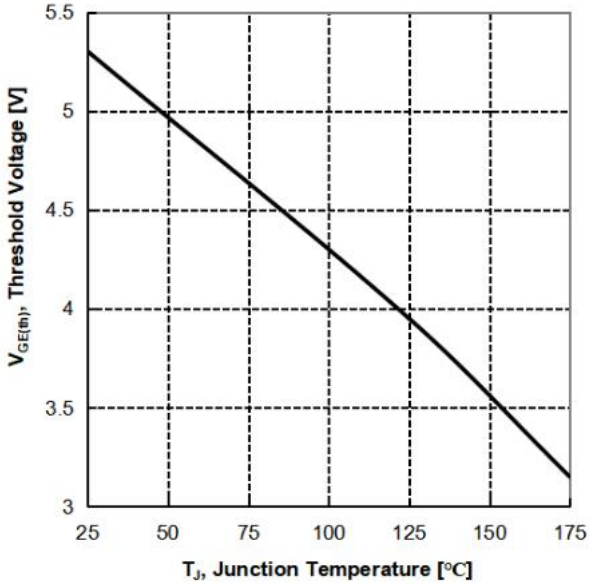


Figure 9: Typical Switching Times vs. Gate Resistor (T_J=25℃, V_{CE}=600V, V_{GE}=15V, I_C=40A)

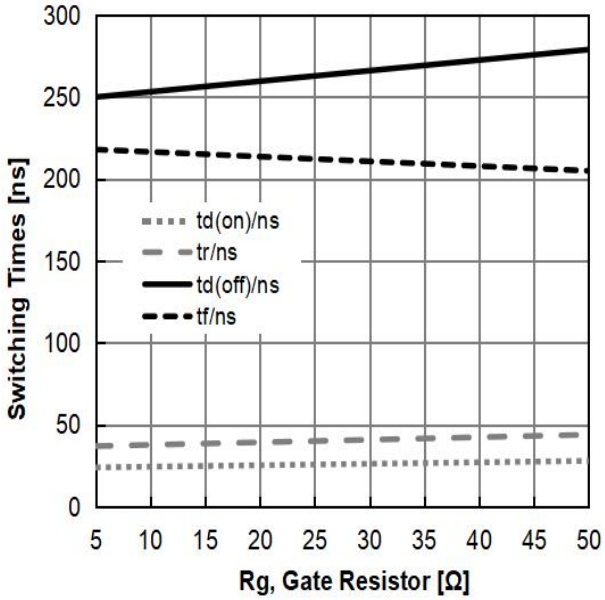


Figure 10: Typical Switching Energy vs. Gate Resistor (T_J=25℃, V_{CE}=600V, V_{GE}=15V, I_C=40A)

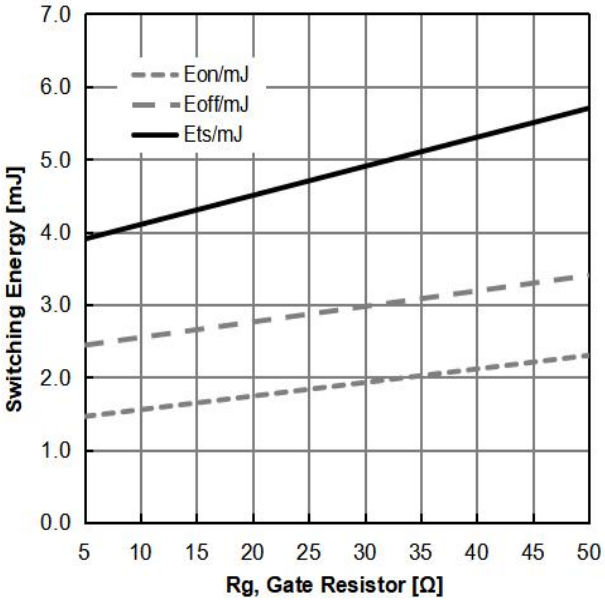


Figure 11: Typical Switching Times vs. Junction Temperature (V_{CE}=600V, V_{GE}=15V, I_C=40A,Rg=5Ω)

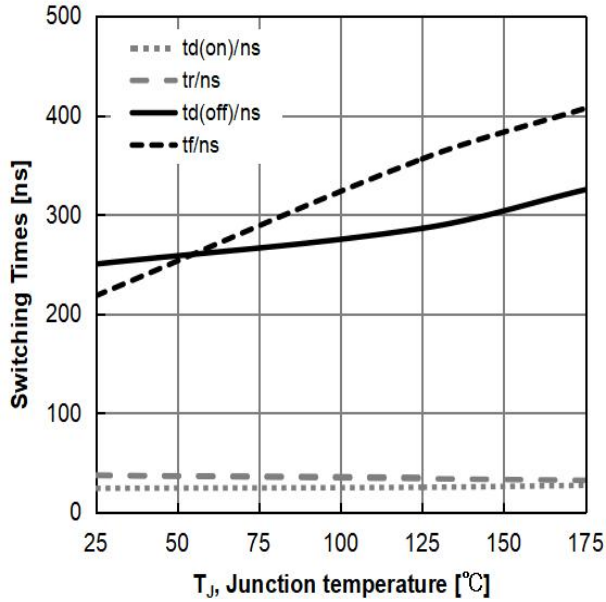


Figure 12: Typical Switching Energy vs. Junction Temperature (V_{CE}=600V, V_{GE}=15V, I_C=40A,Rg=5Ω)

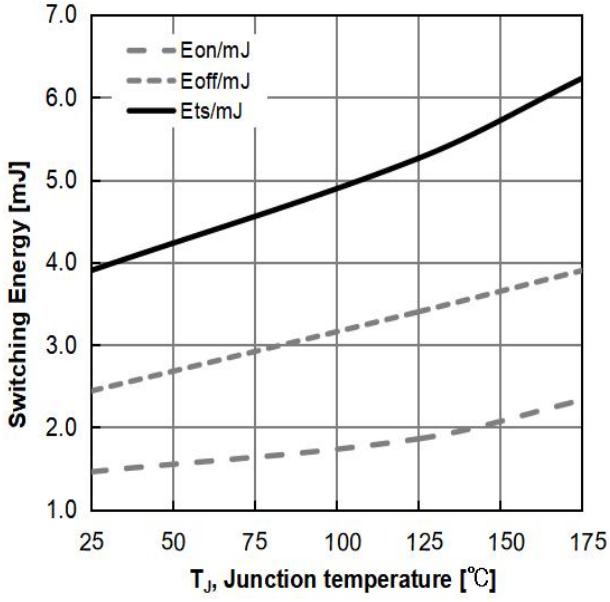


Figure 13: Typical Switching Times vs. Collector Current (T_J=25℃, V_{CE}=600V, V_{GE}=15V,Rg=5Ω)

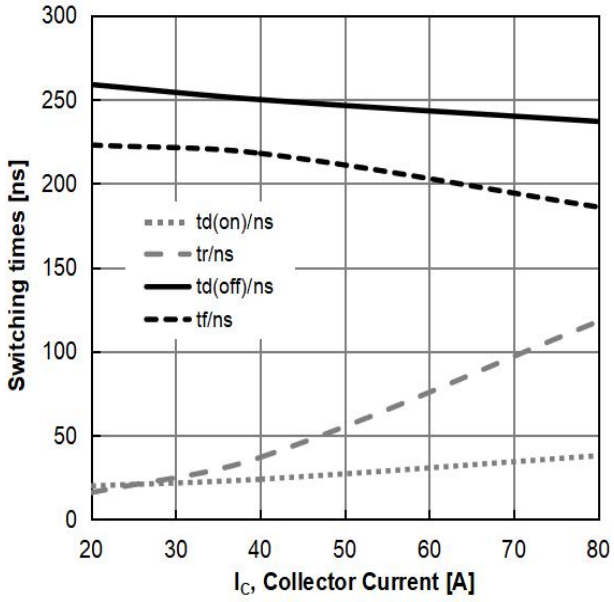


Figure 14: Typical Switching Energy vs. Collector Current (T_J=25℃, V_{CE}=600V, V_{GE}=15V,Rg=5Ω)

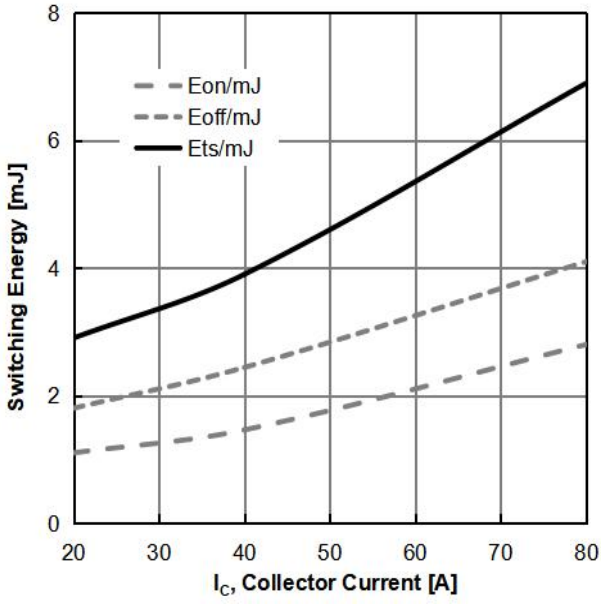


Figure 15: Typical Switching Times vs. VCE (T_J=25℃, V_{GE}=15V, I_C=40A,Rg=5Ω)

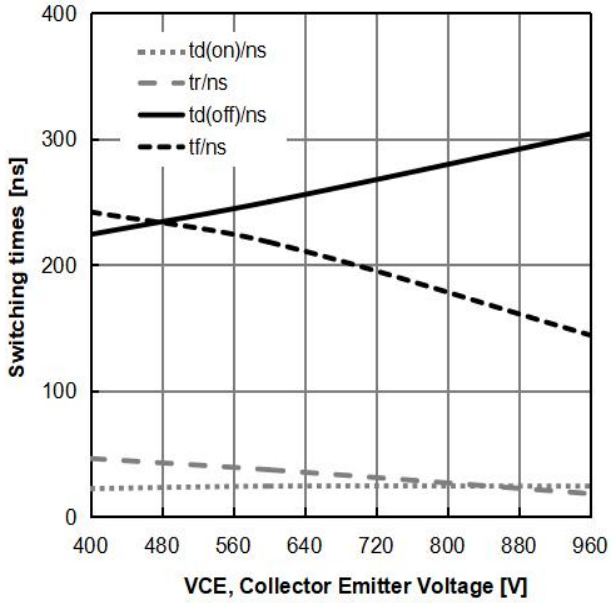


Figure 16: Typical Switching Energy vs. VCE (T_J=25℃, V_{GE}=15V, I_C=40A,Rg=5Ω)

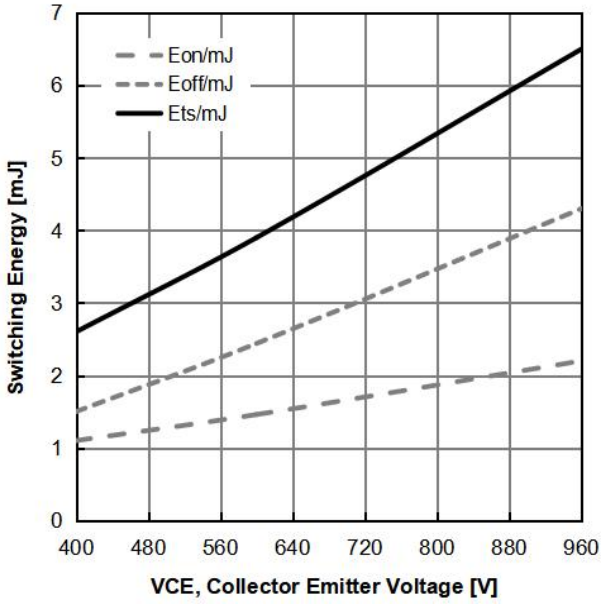


Figure 17: Typical Capacitance vs. Collector- Emitter Voltage

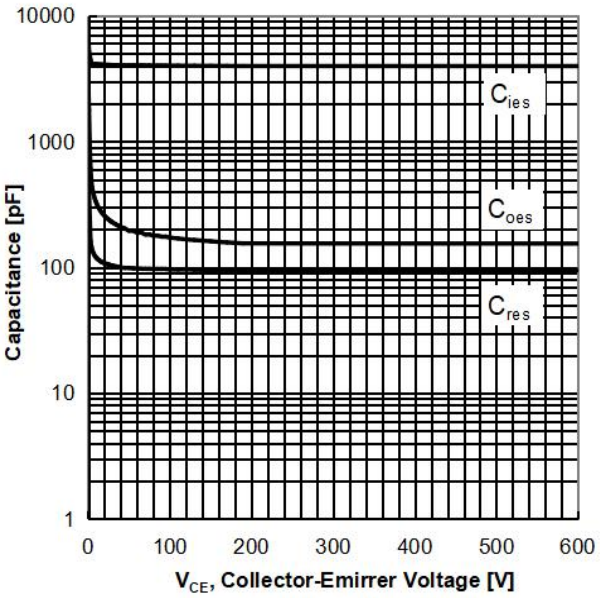


Figure 18: Typical Gate Charge

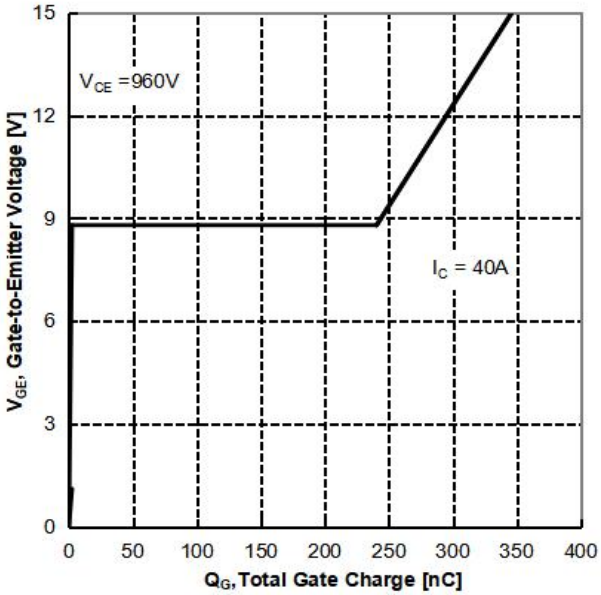


Figure 19: IGBT Transient Thermal Impedance vs. Pulse Width

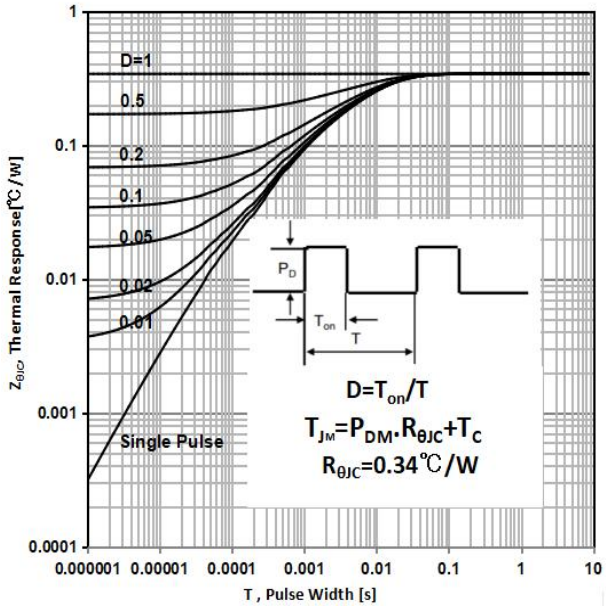


Figure 20: Diode Transient Therma Impedance vs. Pulse Width

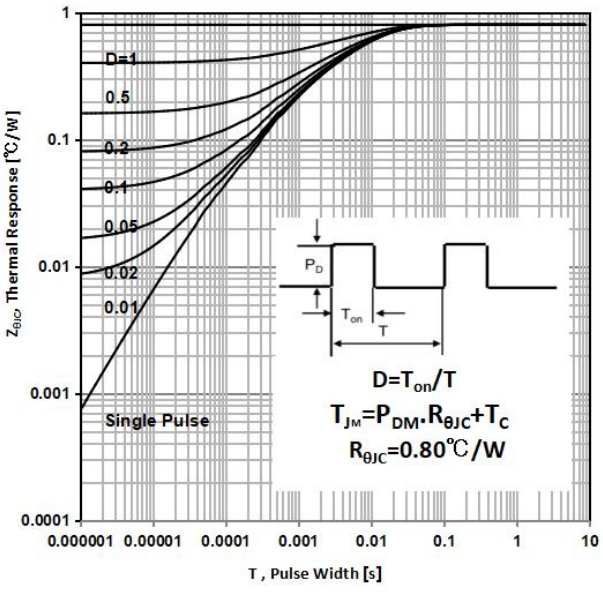
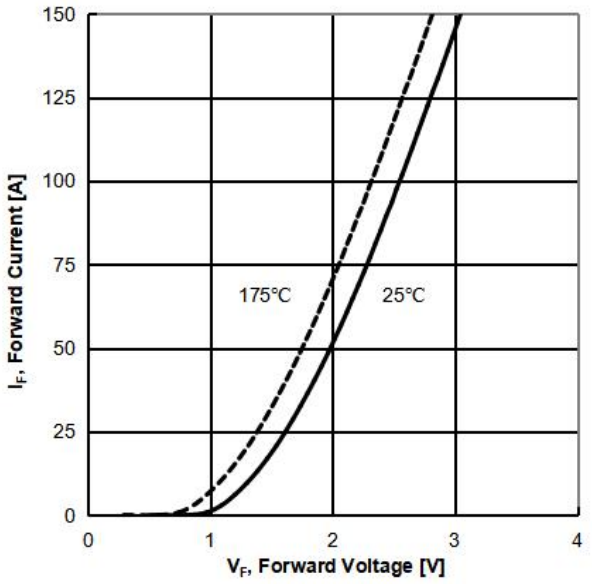


Figure 21: Typical Diode Forward Current vs. Forward Voltage



6. Test Circuit and Waveform

Figure 22. Inductive Switching Test Circuit

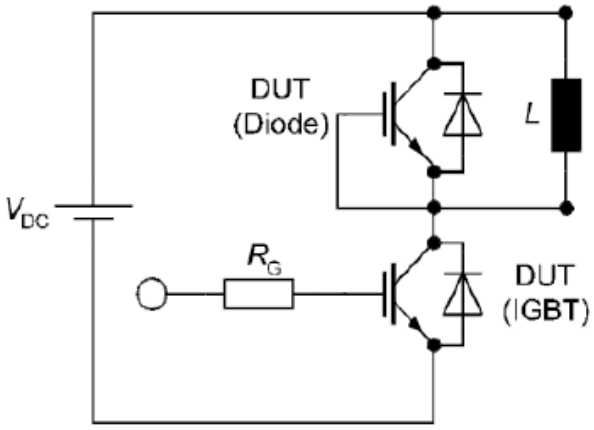


Figure 23. Definition of switching times

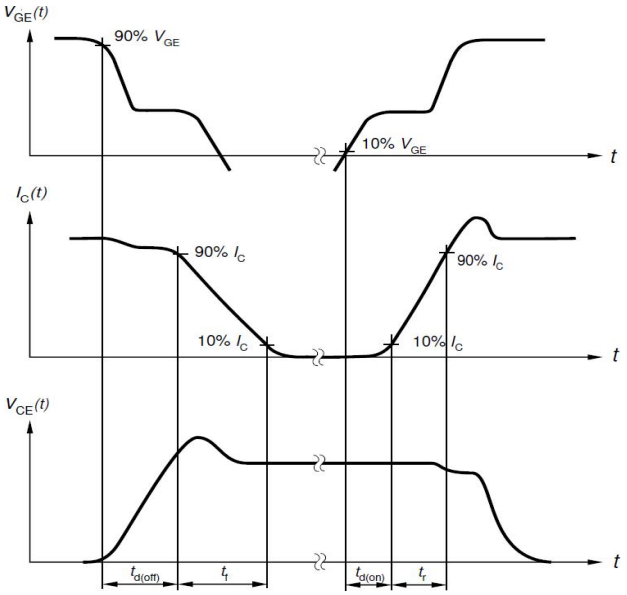


Figure 24. Definition of switching losses

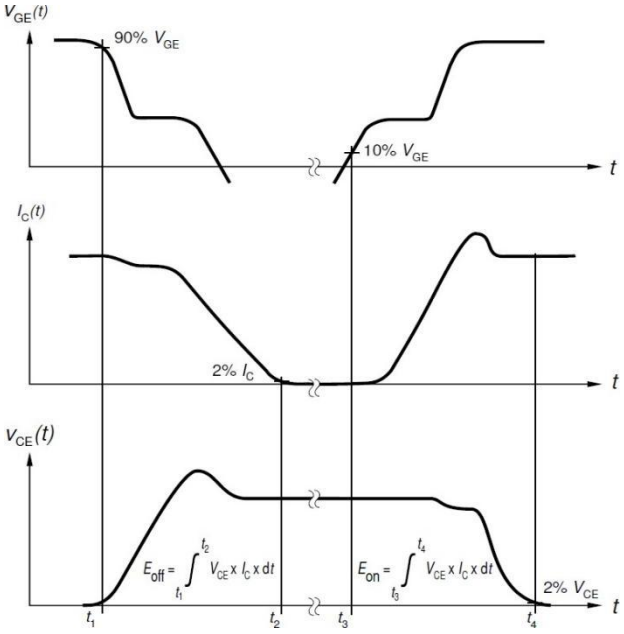
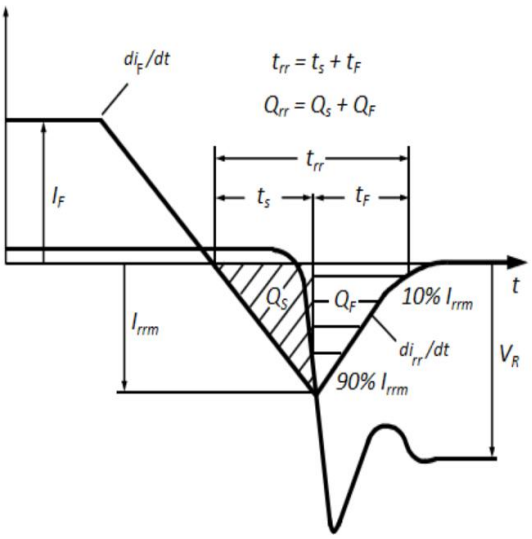
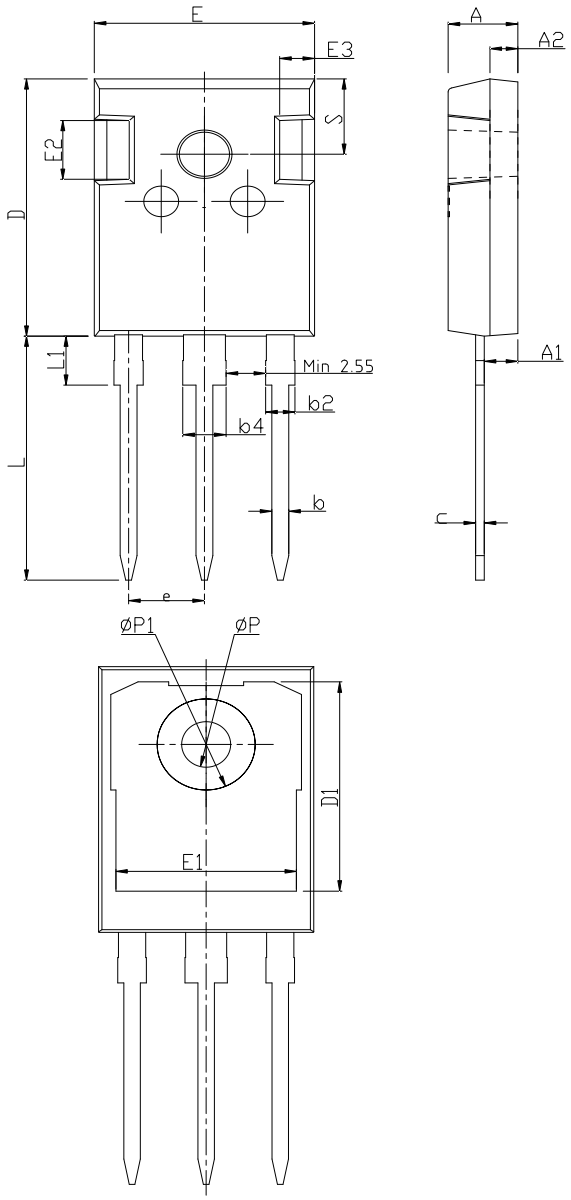


Figure 25. Definition of diode switching characteristics



7. Package Information

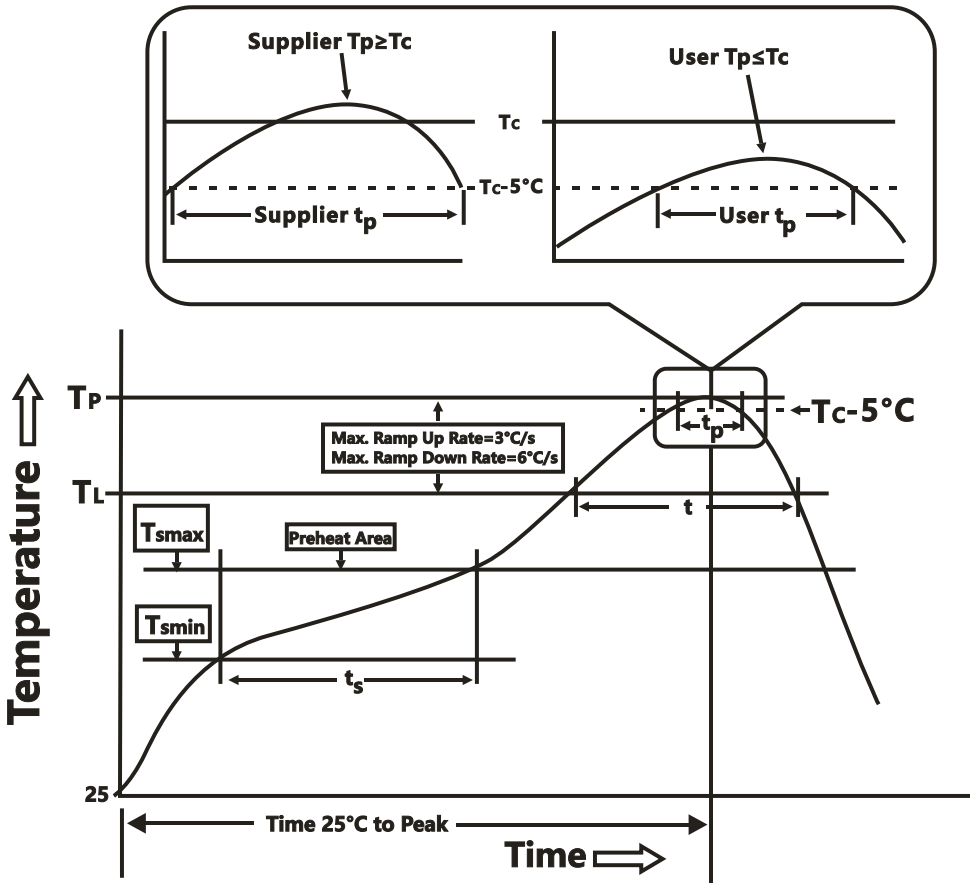
TO-247



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.62	19.92	20.22
L1	-	-	4.30
ΦP	3.40	3.60	3.80
ΦP1	-	-	7.30
S	6.15BSC		

8. Classification Profile



9. Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (Tsmin) Temperature max (Tsmax) Time (Tsmin to Tsmax) (Ts)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds

Peak package body temperature (T_P)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_P)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1.Sn-Pb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	**Volume mm³ <350**	**Volume mm³ ≥350**
＜2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package Thickness	**Volume mm³ <350**	**Volume mm³ 350-2000**	**Volume mm³ ≥2000**
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

10. Reliability Test Program

Test item	Method	Description
HTRB	JESD22-A108B	168/500/1000 Hrs, 100% BV_{CES} @ 175°C
HTGB	JESD22-A108B	168/500/1000 Hrs, 100%V_{GE} @ 175°C
PCT	JESD22-A102	121°C,100%RH, 96 Hrs, 205kPa
TCT	JESD22-A104	250/500/1000 Cycles, -55°C~150°C
BHAST	JESD22-A101	130℃,85%RH,203kPa,U=42V
IOL	MIL-STD-750	Ta=25°C,△T_J≥100°C, Ton/Toff 2min ,8600cycles

Attention

■ Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.

■ HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.

■ Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could
give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

■ In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.