

DEVICE DESCRIPSION

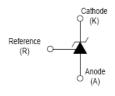
The TL431 is a three-terminal adjust able shunt regulator offering excellent temperature stability . This device has a typical dynamic output impedance of 0.2Ω . The device can be used as a replacement for zener diodes in many applications.

FEATURES

- The output voltage can be adjusted to 36V
- Low dynamic output impedance, its typical value is 0.2K
- Trapping current capability is 1 to 100mA
- Low output noise voltage
- Fast on -state response
- The effective temperature compensation in the working range of full temperature
- The typical value of the equivalent temperature factor in the whole temperature scope is 50 ppm/°C

SOT-23

Equivalent Circuit



APPLICATION

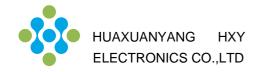
- Shunt Regulator
- High-Current Shunt Regulator
- Precision Current Limiter

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TL431	SOT-23	431	3000

ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

Parameter	Symbol	Value	Unit
Cathode Voltage	V _{KA}	36	V
Cathode Current Range (Continuous)	I _{KA}	-100-+150	mA
Reference Input Current Range	Iref	0.05-+10	mA
Power Dissipation	P _D	300	mW
Thermal Resistance from Junction to Ambient	R _{θJA}	417	°C/W
Operating Temperature	Topr	-25~+85	℃
Junction Temperature	TJ	150	℃
Storage Temperature Range	T _{STG}	-65~+150	℃



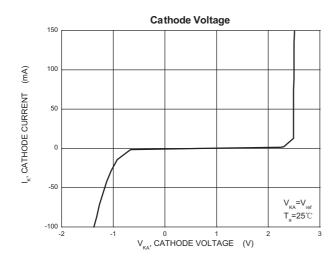
ELECTRICAL CHARACTERISTICS (T_a=25 °C unless otherwise specified)

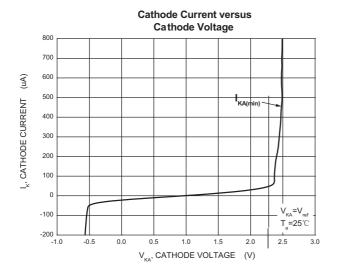
LLLC INICAL CHARACTERISTICS (1a-23 ° unless otherwise specified)							
Parameter	Symbol	Test conditions		Min	Тур	Max	Unit
Reference input voltage	V _{ref}	V _{KA} =V _{REF} , I _{KA} =10mA		2.475	2.5	2.525	V
Deviation of reference Input voltage over temperature (note)	$\triangle V_{ref}/\triangle T$	$V_{KA} = V_{REF}, I_{KA} = 10 \text{mA}$ $T_{MIN} \le T_a \le T_{MAX}$			4.5	17	mV
Ratio of change in reference Input voltage to the change in cathode voltage	$\triangle V_{ref} / \triangle V_{KA}$	I _{KA} =10mA	△V _{KA} =10V~V _{REF}		-1.0	-2.7	mV/V
			△V _{KA} =36V~10V		-0.5	-2.0	mV/V
Reference input current	I _{ref}	I_{KA} = 10mA,R ₁ =10kΩ R ₂ =∞			1.5	4	μΑ
Deviation of reference input current over full temperature range	$\triangle I_{ref}/\triangle T$	I_{KA} =10mA, R ₁ =10kΩ R_2 =∞ T_A =-25 to 85°C			0.4	1.2	μΑ
Minimum cathode current for regulation	I _{KA(min)}	V _{KA} =V _{REF}			0.45	1.0	mA
Off-state cathode current	I _{KA(OFF)}	V _{KA} =36V ,V _{REF} =0			0.05	1.0	μΑ
Dynamic impedance	Z _{KA}	V _{KA} =V _{REF} , I _{KA} =1 to 100mA f≤1.0kHz			0.15	0.5	Ω

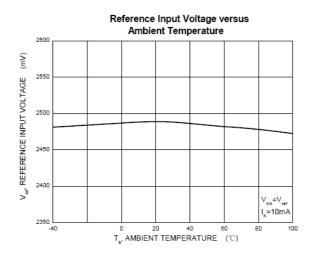
Note:T_{MIN}=-25°C ,T_{MAX}=+85°C

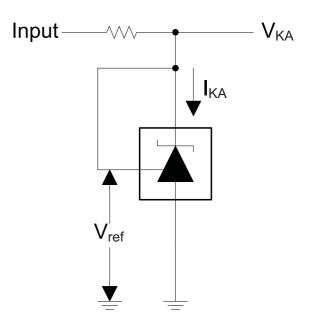
CLASSIFICATION cZVref

Rank	··· 0.5%	1%
Range	2.487-2.513	2.475-2.525

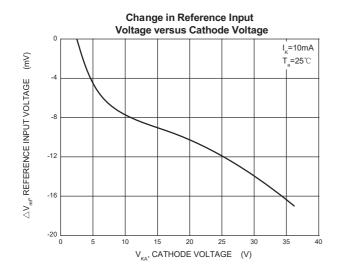


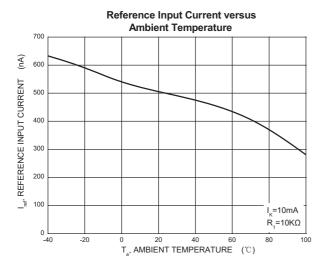


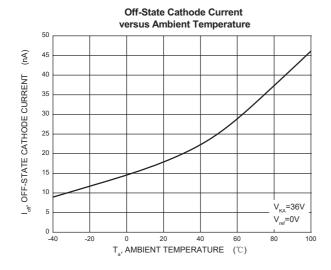


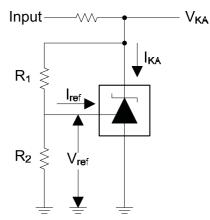


Test Circuit for V_{KA}=V_{ref}

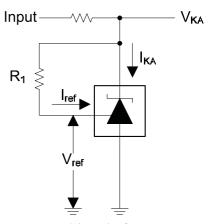




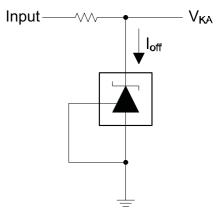




Test Circuit for $V_{KA}=V_{ref}(1+R1/R2)+R1*I_{ref}$



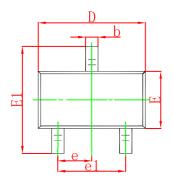
Test Circuit for I_{ref}

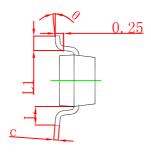


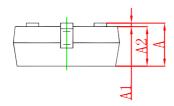
Test Circuit for I_{off}



SOT-23 Package Outline Dimensions

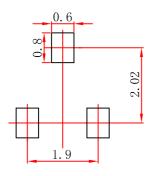






Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

SOT-23 Suggested Pad Layout



- Note:
 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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