

# **Description**

The FDD306P uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

# G G

## **General Features**

 $V_{DS} = 20V I_{D} = 60 A$ 

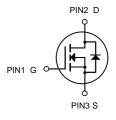
 $R_{DS(ON)} < 6m\Omega @ V_{GS}=4.5V$ 

# **Application**

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

**Package Marking and Ordering Information** 

Product ID	Pack	Marking	Qty(PCS)
FDD306P	TO-252-2L(DPAK)	60N02 XXX YYYY	2500

# Absolute Maximum Ratings (T<sub>c</sub>=25 ℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V <sub>DS</sub>	Drain-Source Voltage	20	V
V <sub>G</sub> s	Gate-Source Voltage	±12	V
I <sub>D</sub>	Drain Current-Continuous	60	Α
I <sub>D</sub> (100°C)	Drain Current-Continuous(Tc=100°C)	42	А
Ірм	Pulsed Drain Current	210	Α
P <sub>D</sub>	Maximum Power Dissipation	60	W
	Derating factor	0.48	W/℃
Eas	Single pulse avalanche energy (Note 5)	200	mJ
Тл,Твтв	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}\!$
Rejc	Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup>	2.1	°C/W



# Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	20	-	-	V
Zero Gate Voltage Drain Current	Ipss	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	Igss	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V	-	1	±100	nA
Gate Threshold Voltage	V <sub>G</sub> S(th)	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	0.5	0.75	1.0	V
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20 A	-	4.5	6	mΩ
Drain-Source On-State Resistance	RDS(ON)	R <sub>DS(ON)</sub> V <sub>GS</sub> =2.5V, I <sub>D</sub> =15A		5.3	8.2	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =10V,I <sub>D</sub> =20A	15	-	-	S
Input Capacitance	C <sub>lss</sub>		-	2000	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V,	-	500	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	200	-	PF
Turn-on Delay Time	td(on)		-	6.4	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}=10V,I_{D}=2A,R_{L}=1\Omega$	-	17.2	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =4.5 $V_{i}R_{G}$ =3 $\Omega$	-	29.6	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	16.8	-	nS
Total Gate Charge	Qg		-	27		nC
Gate-Source Charge	Qgs	V <sub>DS</sub> =10V,I <sub>D</sub> =20A,	-	6.5		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	6.4		nC
Diode Forward Voltage (Note 3)	VsD	V <sub>GS</sub> =0V,I <sub>S</sub> =10A	-		1.2	V
Diode Forward Current (Note 2)	ls		-	-	60	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 20A	-	25	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/μs <sup>(Note3)</sup>	-	24	-	nC
Forward Turn-On Time	ton	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

# Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- $\textbf{5.}~\text{E}_{\text{AS}}~\text{condition}: Tj = 25\,^{\circ}\text{C}~, V_{\text{DD}} = 10 \text{V}, V_{\text{G}} = 10 \text{V}, L = 0.5 \text{mH}, Rg = 25 \Omega,$



## Typical Electrical and Thermal Characteristics (Curves)

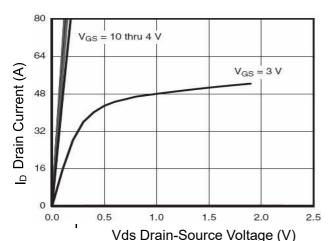
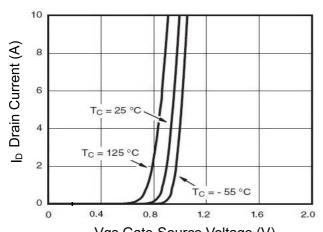


Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V) Figure 2 Transfer Characteristics

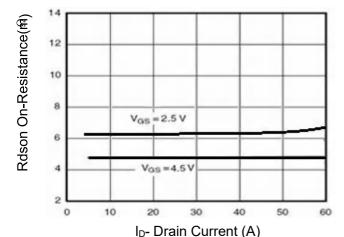


Figure 3 Rdson- Drain Current

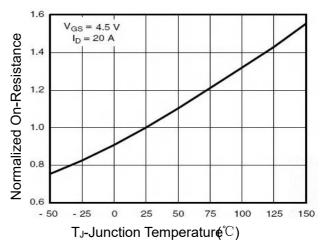
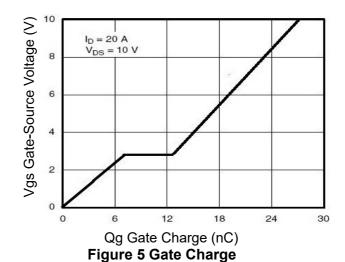


Figure 4 Rdson-JunctionTemperature



Ysd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward



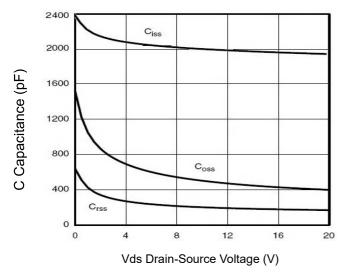


Figure 7 Capacitance vs Vds

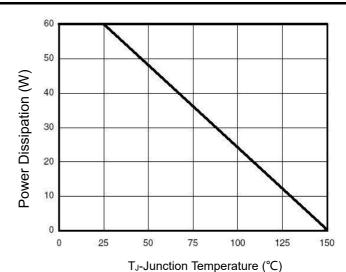


Figure 9 Power De-rating

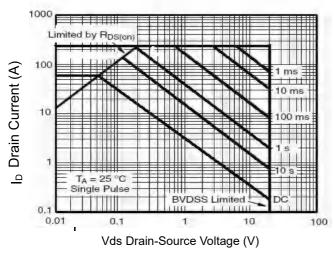


Figure 8 Safe Operation Area

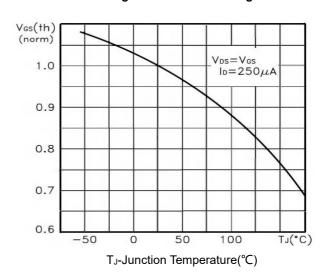
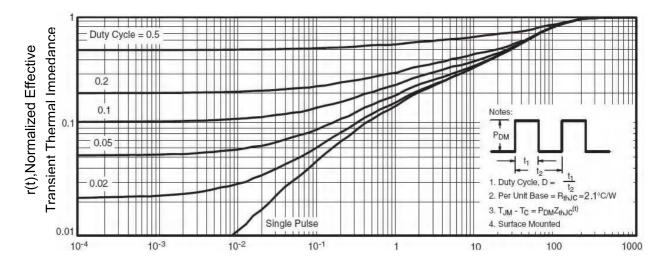


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

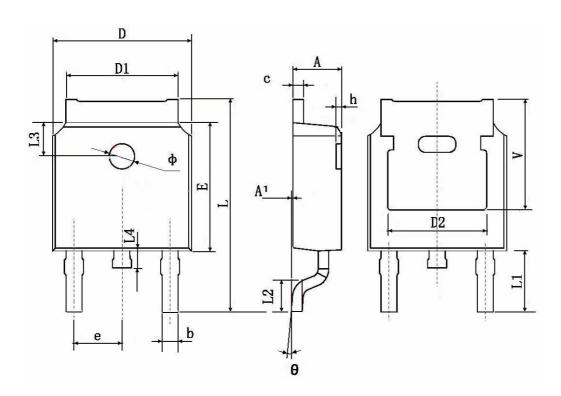


Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance



# TO-252-2L(DPAK) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches			
	Min.	Max.	Min.	Max.		
А	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	0.483	0.483 TYP.		0.190 TYP.		
Е	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067		
L3	1.600	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350 TYP.		0.211 TYP.			



### **Attention**

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.

  HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.