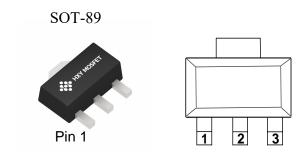
DESCRIPTION

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 100mA of load current with an output voltage adjustable over a 1.2 to 37V. It employs internal current limiting, thermal shut-down and safe area compensation.

FEATURES

- Output Current Excess of 100mA
- Output Adjustable Between 1.2V and 37V
- Internal Thermal Overload Protection
- Internal Short Current Limiting
- Output Transistor Safe-Area Compensation
- Moisture Sensitivity Level 3

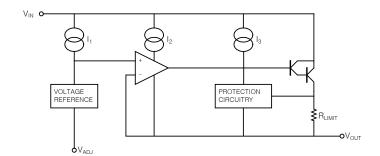
PIN CONFIGURATION



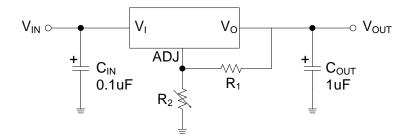
PIN DESCRIPTION

PIN No.	76. T	Functions		
SOT-89	Name	Description		
1	ADJ	Adjustable		
2	V _{OUT}	Output Voltage		
3	V_{IN}	Input Voltage		

BLOCK DIAGRAM



TYPICAL APPLICATION



 $V_{OUT} = 1.25V(1+R_2/R_1)+I_{ADJ}R_2$

- Note 1. C_{IN} is required when regulator is located in appreciable distance from power supply filter.
- Note 2. C_{OUT} is not needed for stability, however, it does improve transient response.
- Note 3. I_{ADJ} is controlled to less than 100uA, the error associated with this term is negligible in most applications.

ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	Value	UNIT
Input-output Voltage Differential	V _I -V _O	40	V
Lead Temperature (Soldering, 10 sec)	T _{SOL}	230	$^{\circ}\!\mathbb{C}$
Power Dissipation	P _D	Internally limited	-
Operating Junction Temperature Range	T_JOPR	-40 ~ 125	$^{\circ}\mathbb{C}$
Storage Temperature Range	T _{STG}	-65 ~ 125	$^{\circ}$ C

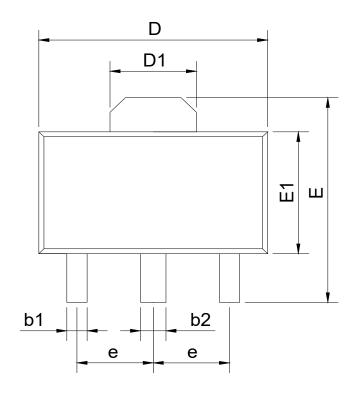
RECOMMENDED OPERATING RATINGS (V₁-V₀=5V, I₀=40mA, -40 $^{\circ}$ C \leq TJ \leq 125 $^{\circ}$ C, unless otherwise specified)

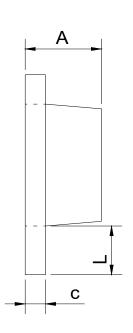
CHARACTERISTIC	SYMBOL	TEST CONDITION		MIN.	TYP.	MAX.	Unit	
Line Regulation	^ \ /-	T _A =-40 ~125°C	3V≤V _I -V _O ≤40V		0.01	0.04	%/V	
	△Vo		3V≤V _I -V _O ≤40V		0.02	0.07	%/V	
		TA=25℃, 10mA≤lo≤l _{MAX}						
Lood Domilation	△Vo	V _O ≤5V			10	25	mV	
		V _O ≥5V			0.1	0.5	%/V	
Load Regulation		10mA≤lo≤lMAX						
		V _O ≤5V			20	80	mV	
		V _O ≥5V			0.3	1.7	%/V	
Adjustable Pin Current	I _{ADJ}				46	100	μA	
		3V≤V _I -V _O ≤40V						
Adjustable Pin Current Change	$\triangle I_{ADJ}$	10 m $A \le I_O \le I_{MAX}$			0.2	5	μA	
		$P {\leq} P_{MAX}$						
Reference Voltage	V_{REF}	$3V \le V_{IN} - V_{OUT} \le 40V$						
		10 m $A \le Io \le I_{MAX}$		1.20	1.25	1.30	V	
		$P_{D} \leq P_{MAX}$						
Temperature Stability	ST _T				0.7		%/Vo	
Minimum Load Current to Maintain Regulation	L _(MIN)	V _I -V _o	=40V		3.5	10	mA	
Maximum Output Current	I _{O(MAX)}	V _I -V _o ≤5V	$P_{D} \leq P_{MAX}$	100	200		mA	
		V _I -V _O ≤40V, P _D :	\leq P _{MAX} , T _A =25 $^{\circ}$ C	0.156	0.4			
RMS Noise, % of VOUT	e _N	T _A =25℃, 10Hz≤f≤10KHz			0.003	0.01	%/V ₀	
Ripple Rejection	RR	V _O =10V,	f=120Hz					
		without C _{ADJ}			60		dB	
		C _{ADJ} =10μF 66 75						
Long-Term Stability,	ST	TA=25 ℃, for end point 0.3 measurements, 1000HR		0.3	1	0/		
T _J =T _{HIGH}							%	

^{*} Load and line regulation are specified at constant junction temperature. Change in VD due to heating effects must be taken into account separately. Pulse testing with low duty is used.

PACKAGE INFORMATION

SOT-89





SYMBOL	mm		
	min	max	
Α	1.40	1.60	
b1	0.35	0.50	
b2	0.45	0.60	
С	0.36	0.46	
D	4.30	4.70	
D1	1.40	1.80	
Е	4.00	4.40	
E1	2.30	2.70	
е	1.50BSC		
L	0.80	1.20	



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