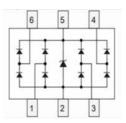


Discription

The HAZC015-04C is a 5-channel ultra low capacitance rail clamp ESD protection diodes array. Each channel consists of a pair of ESD diodes that steer positive or negative ESD current to either the positive or negative rail. A zener diode is integrated in to the array between the positive and negative supply rails. In the typical applications, the negative rail pin (assigned as GND) is connected with system ground. The Positive ESD current is steered to the ground through an ESD diode and Zener diode and the positive ESD voltage is clamped to the zener voltage.



SOT-363



Circuit Diagram

Features

- ★ 5 channels of ESD protection
- ★ Provides ESD protection to IEC61000-4-2 level 4
 - ±27kV air discharge
 - ±15kV contact discharge
- ★ Low clampingvoltage
- ★ Low operating voltage
- ★ Improved zener structure
- ★ Optimized package for easyhigh speed data lines PCB layout
- ★ RoHS compliant

Ordering Information

Product ID	Pack	Qty(PCS)
HAZC015-04C	SOT-363	3000

Absolute Ratings(Tamb = 25°C)

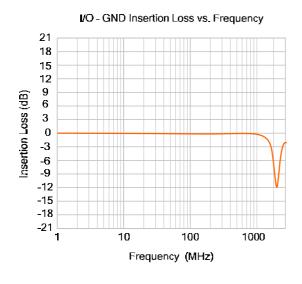
Characteristics	Symbol	Ratings	Unit
Peak Pulse Power(8/20µs)	P _{PP}	55	W
Peak Pulse Current(8/20µs)	I _{PP}	4	А
ESD per IEC 61000-4-2(Air)	V _{ESD1}	±20kV	kV
ESD per IEC 61000-4-2(Contact)	V _{ESD2}	±15kV	kV
Operating Temperature Range	Topr	-55 ~ +125	°C
Storage Temperature Range	Tstg	-55 ~ +150	°C

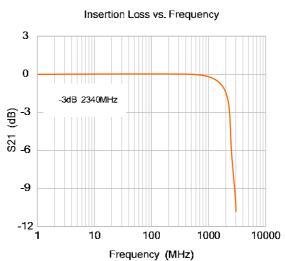


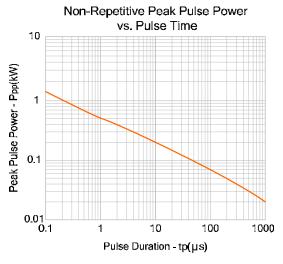
Electrical Characteristics (Tamb=25°C)

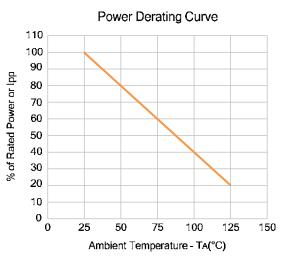
Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Reverse Working	\/	Any I/O pin to GND			_	V
Voltage	V_{RWM}				5	V
Reverse Breakdown	1/	I t=1mA;	6			V
Voltage	V_{BR}	Any I/O pin to GND				
Reverse Leakage	I _R	V _{RWM} =5V, T=25°C;			1	μA
Current		Any I/O pin to GND				
Docitive Clamping		I _{PP} =1A, t _P =8/20 µs;				
Positive Clamping Voltage	V _{C1}	Positive pulse;			13.0	V
		Any I/O pin to GND				
Negative Clamping Voltage	V _{C2}	I _{PP} =1A, t _P =8/20µs;		1.8		V
		Negative pulse;				
		Any I/O pin to GND				
Junction Capacitance	C _{J1}	V _R =0V, f=1MHz;		0.2	0.4	pF
Between Channel		Between I/O pins		0.3	0.4	
Junction Capacitance	C _{J2}	V _R =0V, f=1MHz;	0.	0.0	0.8	pF
Between I/O And GND		Any I/O pin to GND		0.6		

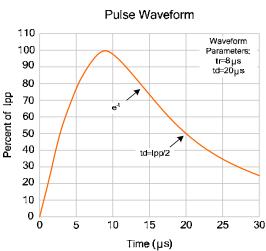
Typical Characteristics

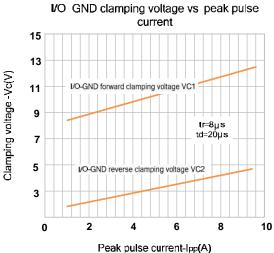


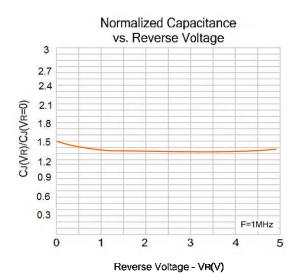






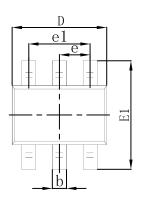


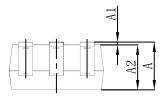


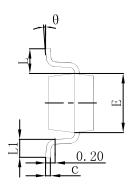




SOT-363 Package Outline Dimensions

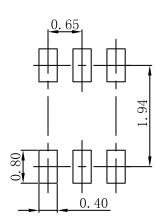






Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.150	0.350	0.006	0.014 0.006 0.087	
С	0.100	0.150	0.004		
D	2.000	2.200	0.079		
Е	1.150	1.350	0.045	0.053	
E1	2.150	2.400	0.085	0.094	
е	0.650) TYP	0.026	STYP	
e1	1.200	1.400	0.047	0.055	
Ĺ	0.525 REF		0.021 REF		
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	

SOT-363 Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.



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