

MOSFET

Features

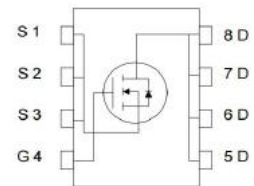
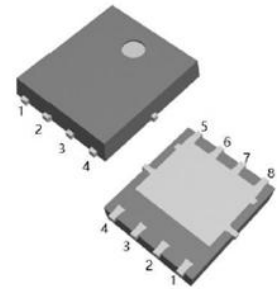
- ◆ Low gate charge
- ◆ Low Ciss
- ◆ Fast switching
- ◆ 100% avalanche tested

Applications

- ◆ Synchronous Rectification for AC/DC Quick Charger
- ◆ Battery management
- ◆ UPS(Uninterruptible Power Supplies)

160A,30V,3.4mΩ,N-CHANNEL MOSFET

View and Internal
Schematic Diagram
PDFN5*6-8L



Parameter	Values	Unit
BV_{DSS}	30	V
I_D	160	A
$R_{Dson(max)}$	3.4	mΩ
$V_{GS(th)}(Typ)$	1.7	V

Ordering Code	Marking	Package	Packaging
X3P4N030TLV8B	X3P4N030TLV8B	PDFN5*6-8L	Tape&Reel

Absolute Maximum Ratings($T_C=25^{\circ}C$,unless otherwise noted)

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Voltage	V_{DSS}	-	-	30	V	-
Gate-Source Voltage	V_{GS}	-20	-	20	V	-
Continuous Drain Current (Note 1)	I_D	-	-	160	A	$T_C=25^{\circ}C$
		-	-	102	A	$T_C=100^{\circ}C$
Pulsed Drain Current(Note 2)	I_{DM}	-	-	640	A	-
Single Pulse Avalanche Energy	E_{AS}	-	-	333	mJ	$L=0.5mH, V_D=24V, T_C=25^{\circ}C$
Maximum Power Dissipation	P_D	-	-	131	W	$T_C=25^{\circ}C$
		-	-	2.2	W	$T_A=25^{\circ}C$
Operating Junction and Storage Temperature Range	T_{j}, T_{STG}	-50	-	150	$^{\circ}C$	-

Thermal Characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Thermal resistance ,Junction to Case	$R_{th(j-c)}$	-	0.95	-	$^{\circ}C/W$	-
Thermal resistance , Junciton to Ambient	$R_{th(j-a)}$	-	55	-		-

Electrical Characteristics (T_j=25°C, unless otherwise noted)

Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	V _{GS} =0V, I _D =250μA
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μA	V _{DS} =30V, V _{GS} =0V
Gate-Body Leakage Current, Forward	I _{GSSF}	-	-	100	nA	V _{GS} =20V, V _{DS} =0V
Gate-Body Leakage Current, Reverse	I _{GSSR}	-	-	-100	nA	V _{GS} =-20V, V _{DS} =0V
Gate-Source Threshold Voltage	V _{GS(th)}	1	-	2.4	V	V _{DS} =V _{GS} , I _D =250μA
Drain-Source On-State Resistance	R _{DS(on)}	-	2.5	3.4	mΩ	V _{GS} =10V, I _D =20A
		-	4	6	mΩ	V _{GS} =4.5V, I _D =20A
Gate Resistance	R _g	-	2.3	-	Ω	V _{GS} =0V, V _{DS} Open, f=1MHz
Forward Transconductance	g _{fs}	-	27	-	S	V _{DS} =20V, I _D =20A

Dynamic characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Input Capacitance	C _{iss}	-	3993	-	pF	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ
Output Capacitance	C _{oss}	-	441	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	402	-	pF	
Turn-On Delay Time	t _{d(on)}	-	15	-	ns	V _{DD} =15V, V _{GS} =10V, R _G =3.3Ω, I _D =15A
Turn-On Rise Time	t _r	-	67	-	ns	
Turn-Off Delay Time	t _{d(off)}	-	90	-	ns	
Turn-Off Fall Time	t _f	-	61	-	ns	

Gate charge characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Total Gate Charge	Q _g	-	79	-	nC	V _{DS} =15V, V _{GS} =10V, I _D =15A
Gate-Source Charge	Q _{gs}	-	13	-	nC	
Gate-Drain Charge	Q _{gd}	-	15	-	nC	

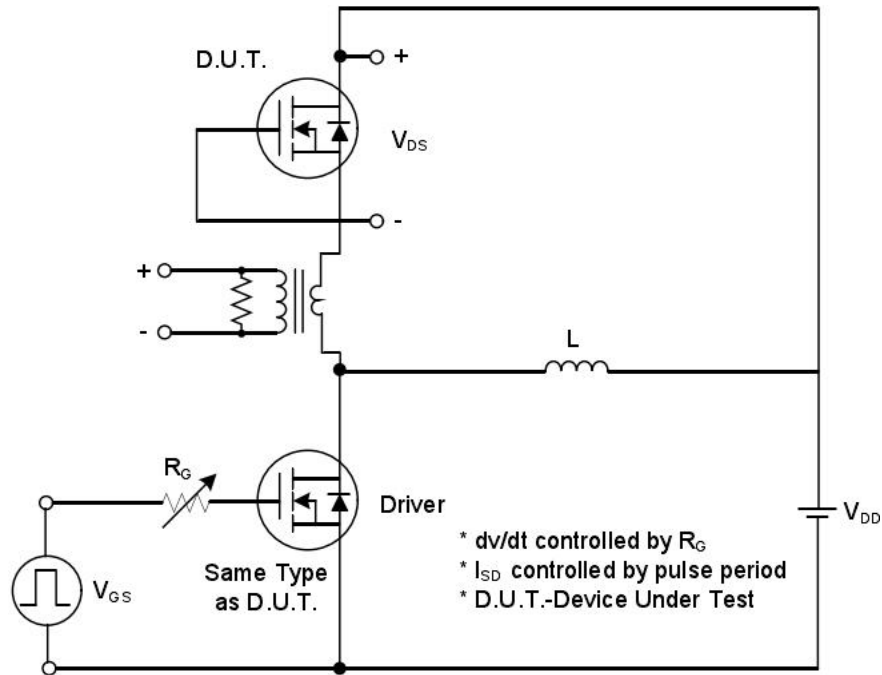
Reverse diode

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Continuous Diode Forward Current	I _S	-	-	160	A	-
Pulsed Diode Forward Current	I _{SM}	-	-	640	A	-
Diode Forward Voltage	V _{SD}	-	-	1.2	V	I _S =20A, V _{GS} =0V
Reverse Recovery Time	t _{rr}	-	20	-	ns	V _{DS} =20V, I _S =1A
Reverse Recovery Charge	Q _{rr}	-	10	-	nC	di/dt=100A/μs

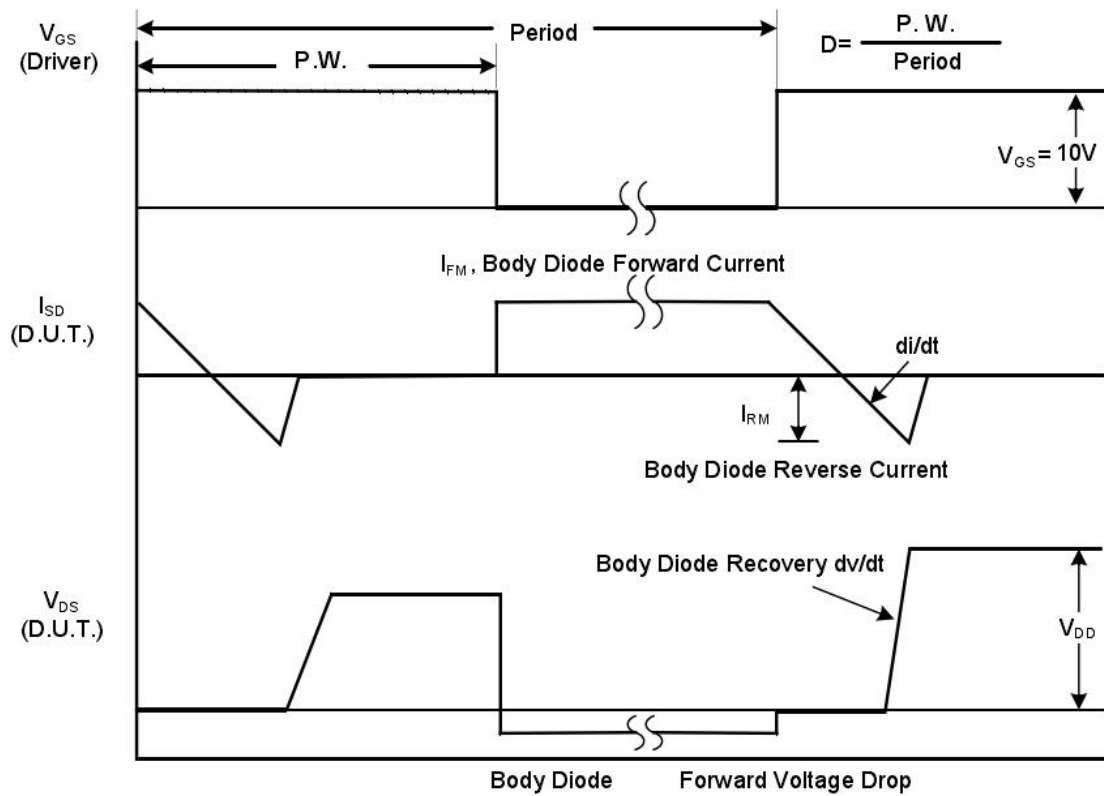
Notes

1. Computed continuous current assumes the condition of T_{j,Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under T_{j,Max} = 150°C.

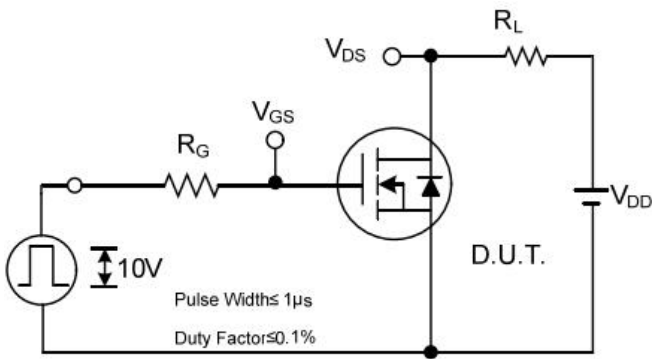
RATING AND CHARACTERISTIC CURVES



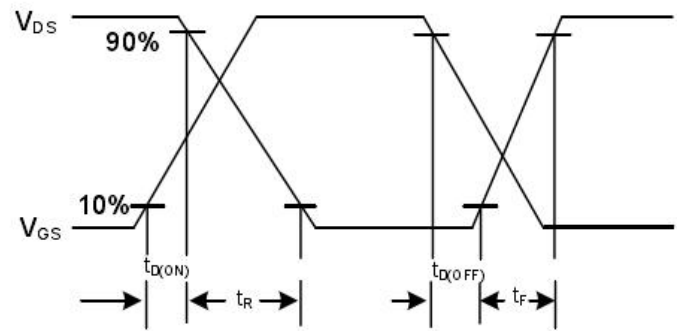
Peak Diode Recovery dv/dt Test Circuit



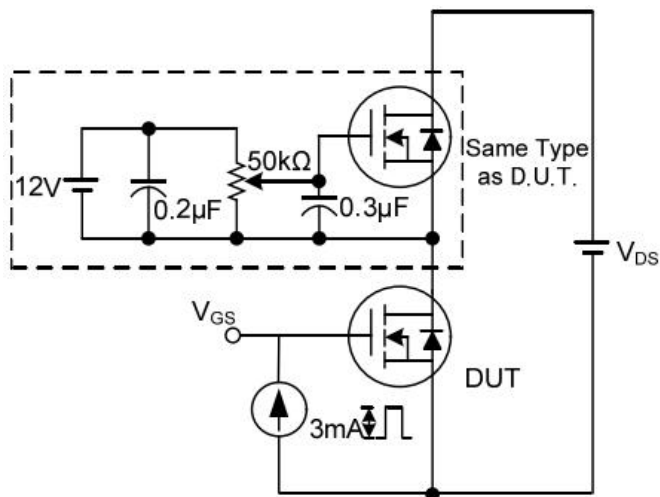
Peak Diode Recovery dv/dt Waveforms



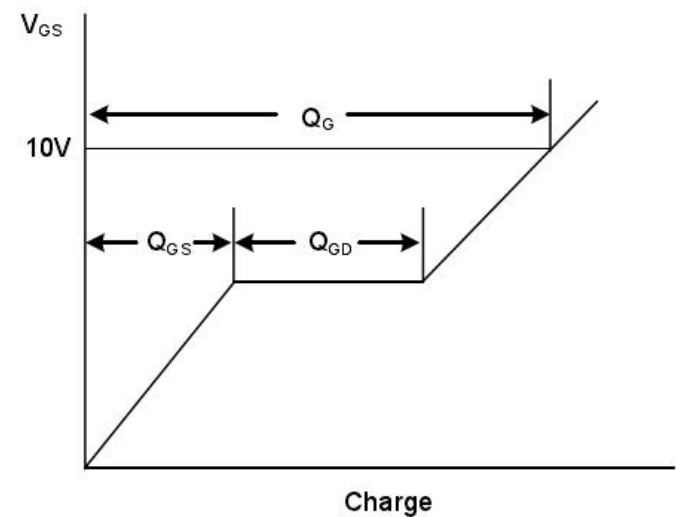
Switching Test Circuit



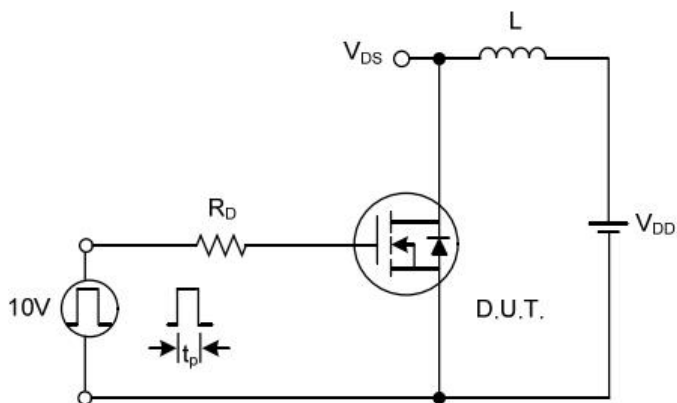
Switching Waveforms



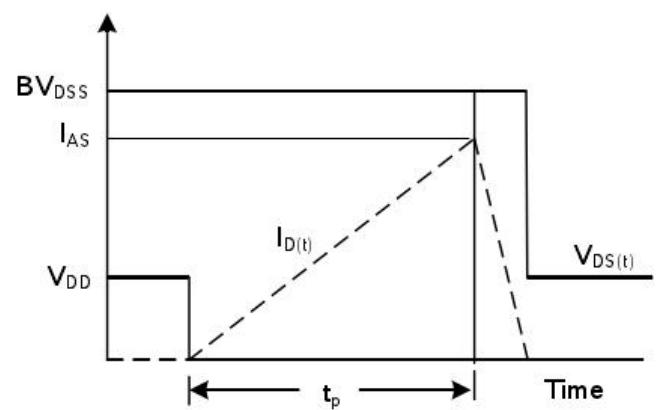
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES

Figure.1 Output Characteristics $T_J=25^{\circ}\text{C}$

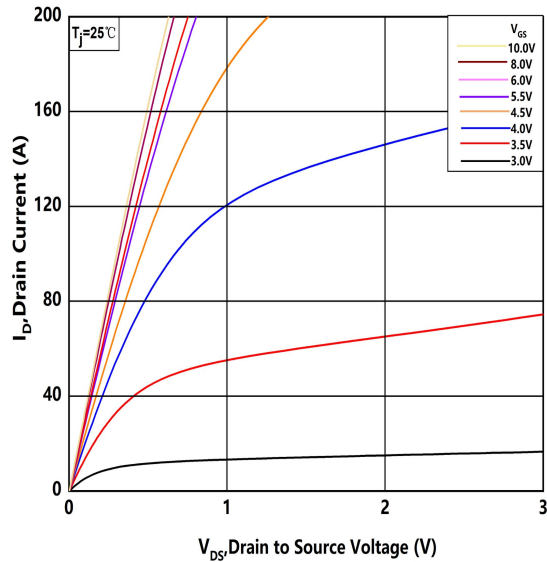


Figure.2 Transfer Characteristic for Various Junction Temperatures

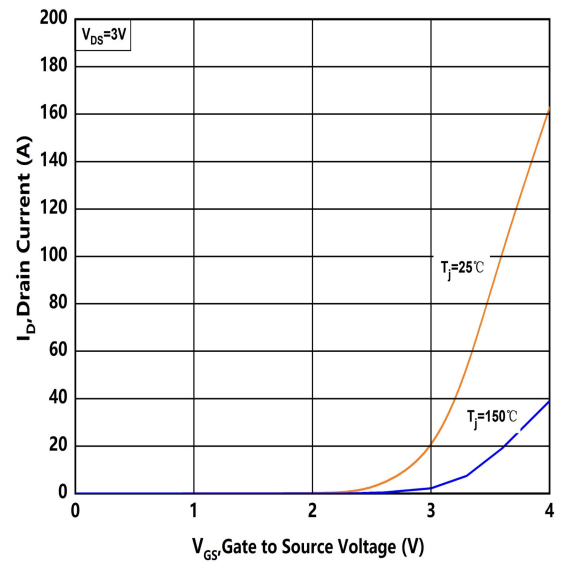


Figure.3 On-Resistance vs Drain Current For Various Gate Voltage

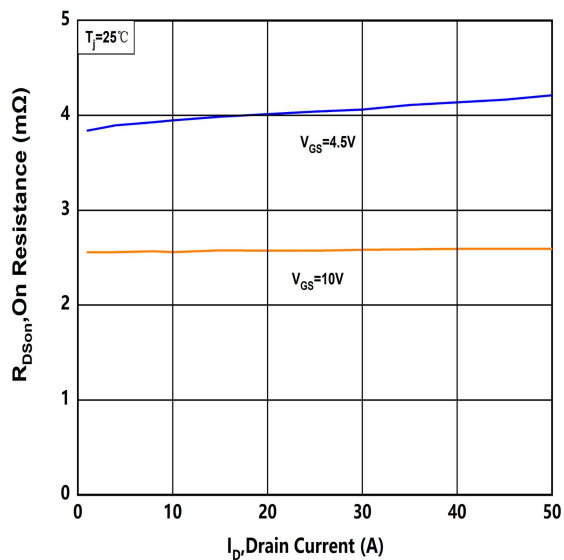


Figure.4 Typical On-Resistance vs Junction Temperature

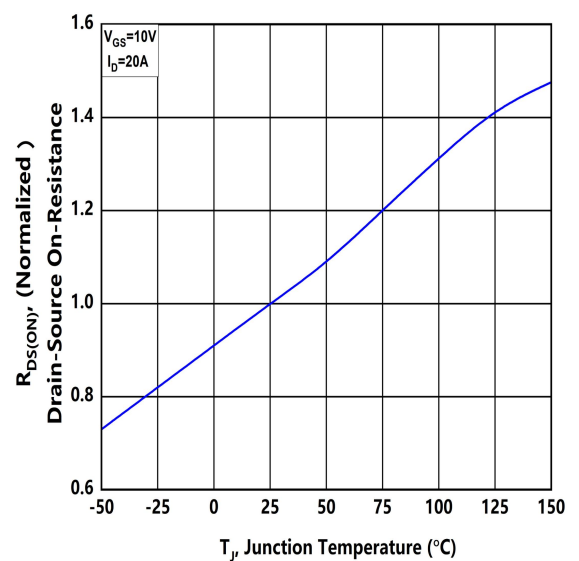




Figure.5 Typical Threshold Voltage vs Junction Temperature

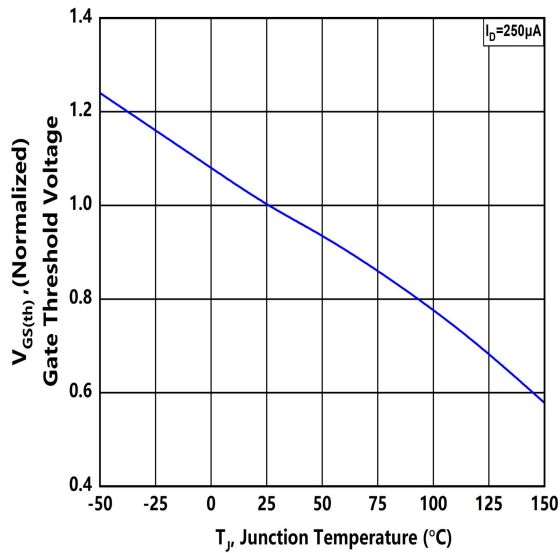


Figure.6 Typical Breakdown Voltage vs Junction Temperature

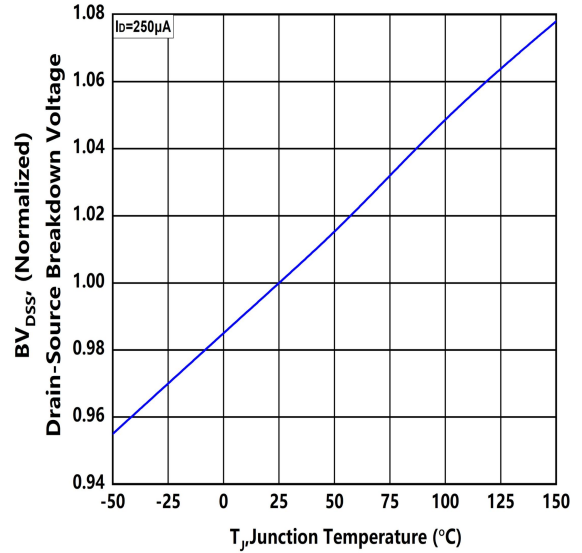


Figure.7 Typical Capacitance vs Drain to Source Voltage

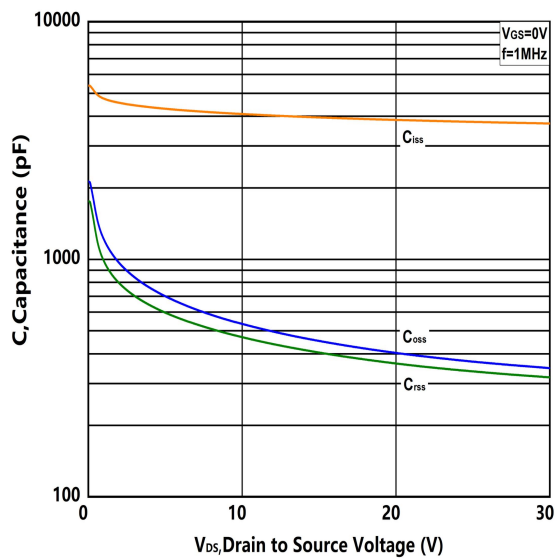


Figure.8 Typical Gate Charge vs Gate to Source Voltage

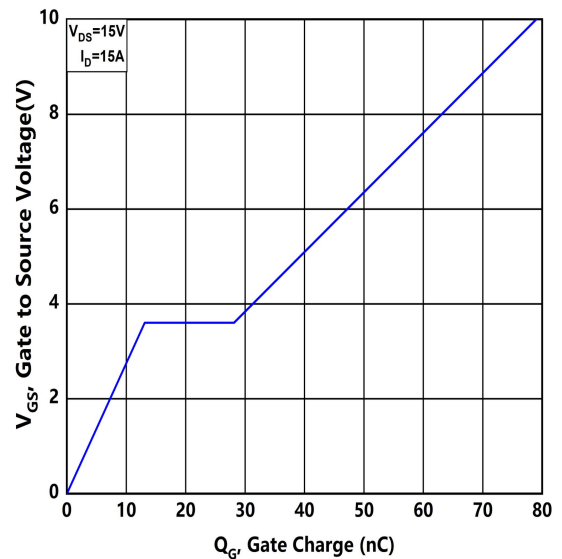


Figure.9 Typical Body Diode Characteristics

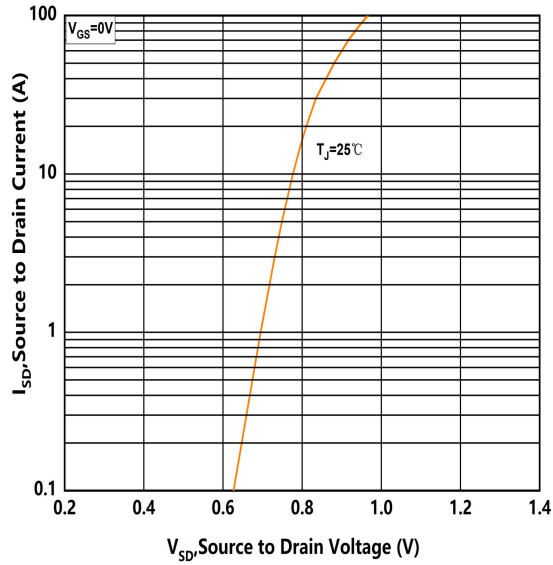


Figure.10 Maximum power Dissipation Derating vs Case Temperature

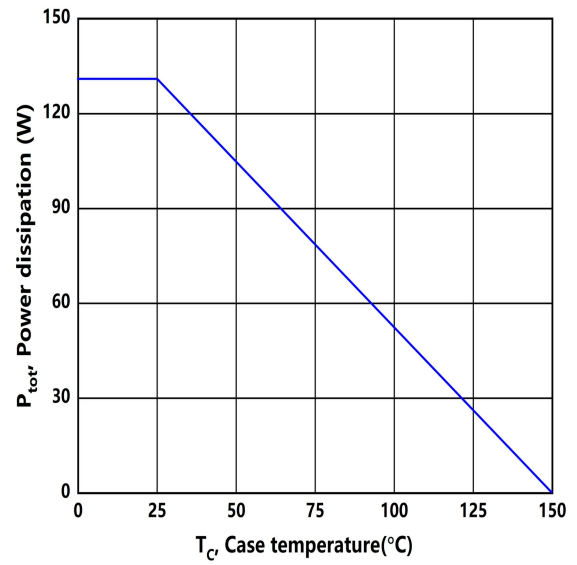


Figure.11 Continuous Drain Current Derating vs Case Temperature

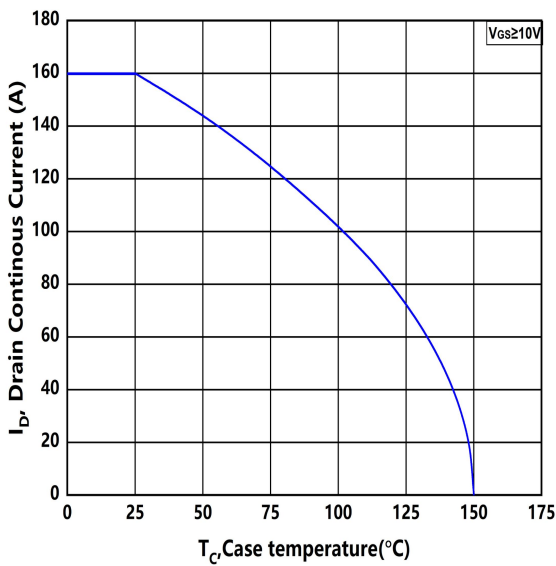


Figure.12 Safe Operating Area

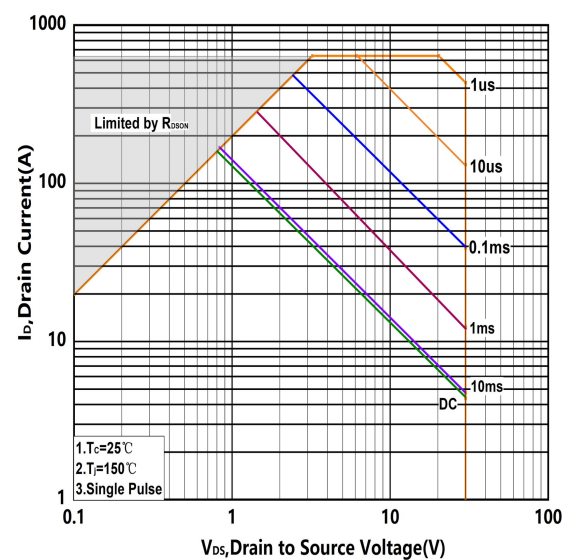
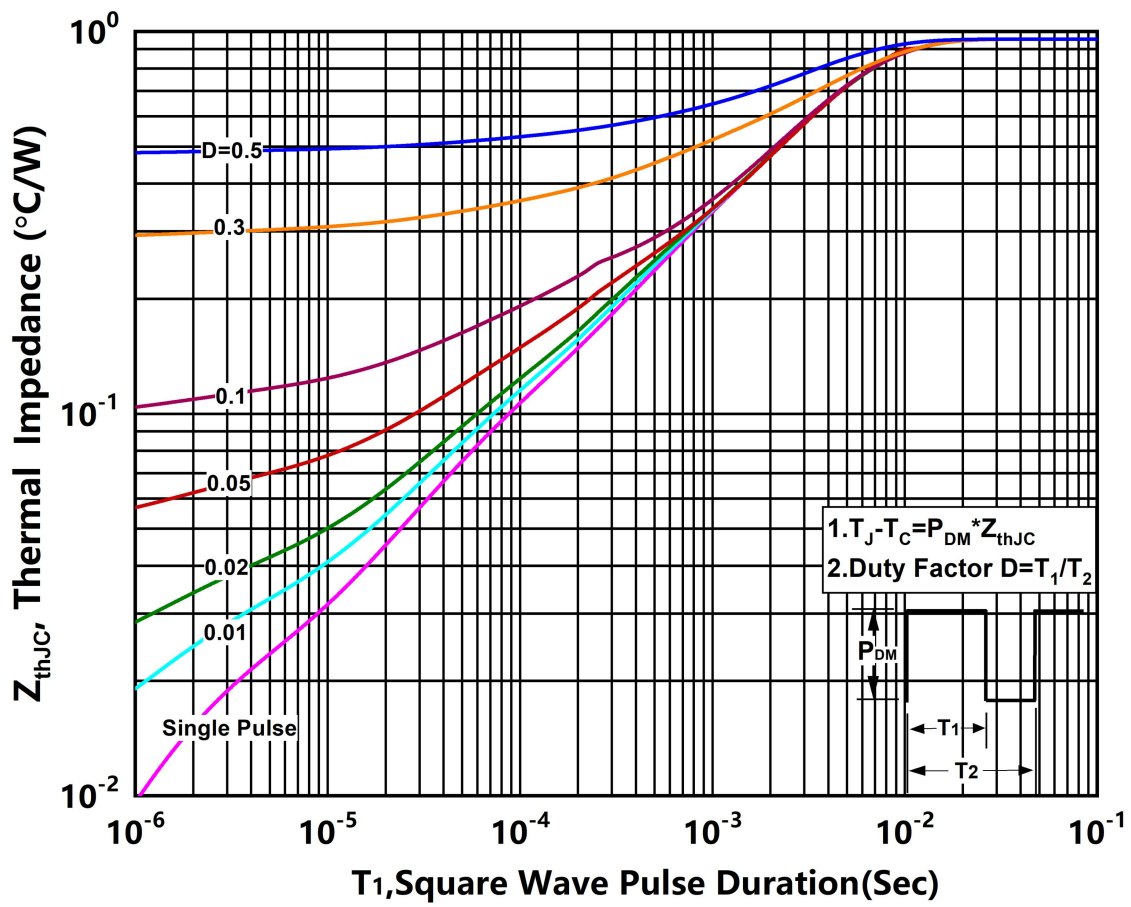
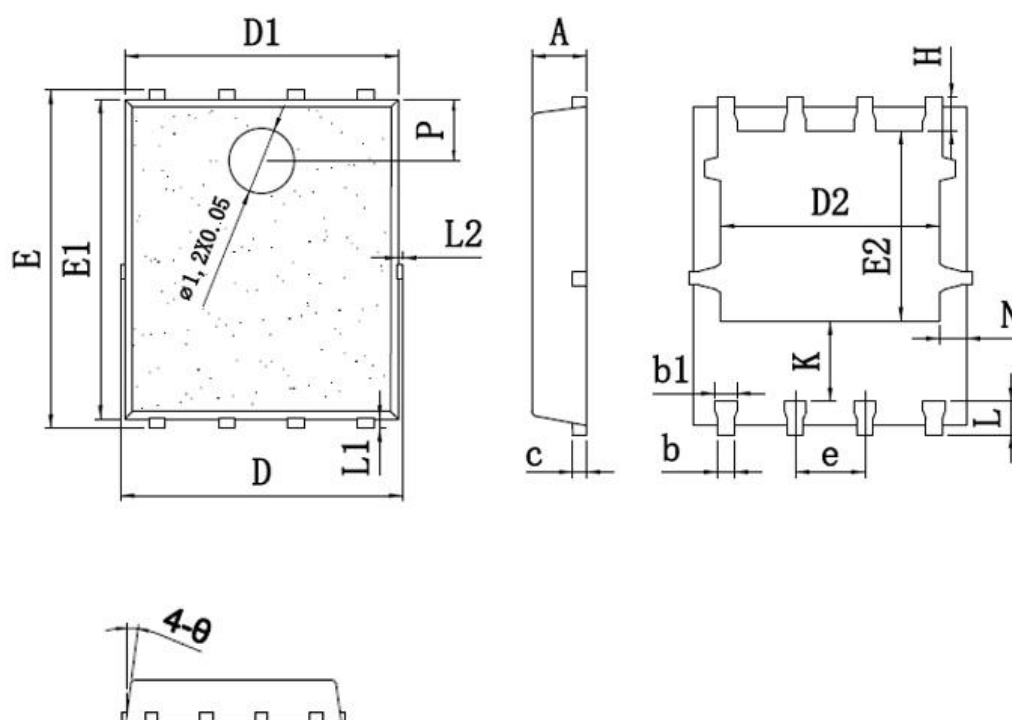


Figure.13 Transient Thermal Impedance (Junction - Case)



Package Outline:PDFN5*6-8L



SYMBOL	mm		
	MIN	NOM	MAX
A	0.95	1.00	1.05
b	0.25	0.30	0.35
b 1	0.30	0.40	0.50
C	0.20	0.25	0.30
D	5.15BSC		
D1	4.90	5.00	5.10
D2	3.90	4.01	4.20
e	1.17	1.27	1.37
E	6.15BSC		
E1	5.75	5.85	5.95
E2	3.35	3.50	3.65
H	0.51	0.61	0.71
K	1.10	1.35	1.50
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
L2	-	-	0.12
N	0.40	0.50	0.60
P	0.95	1.10	1.25
θ	9°	11°	13°