

MOSFET

113A,60V,3.5mΩ N-CHANNEL MOSFET

Features

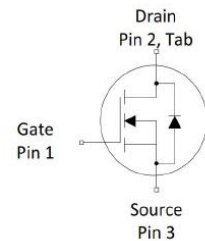
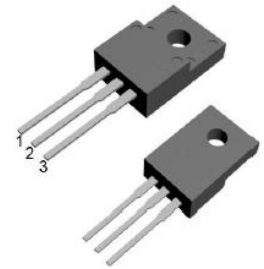
- ◆ Extremely low on-resistance $R_{DS(on)}$
- ◆ Excellent $Q_g \times R_{DS(on)}$ product(FOM)
- ◆ Excellent Low Ciss
- ◆ High robustness and reliability
- ◆ Increases maximum current capability
- ◆ Low power loss, high power density
- ◆ Easy paralleling

Applications

- ◆ Synchronous Rectification for AC/DC Quick Charger
- ◆ Battery management
- ◆ UPS(Uninterruptible Power Supplies)

Parameter	Values	Unit
BV_{DSS}	60	V
I_D	113	A
$R_{DS(on)}$ (max)	3.5	mΩ
$V_{GS(th)}$ (Typ)	1.75	V

View and Internal
Schematic Diagram
TO-220MF-3L



Ordering Code	Marking	Package	Packaging
X3P5N060GLQ3	X3P5N060GLQ3	TO-220MF-3L	Tube

Absolute Maximum Ratings($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Values	Unit	Note/Test Conditions
Drain-Source Voltage	V_{DS}	60	V	-
Gate-Source Voltage	V_{GS}	± 20	V	-
Continuous Drain Current(Note 1)	I_D	113	A	$T_C=25^{\circ}\text{C}$
		71		$T_C=100^{\circ}\text{C}$
Pulsed Drain Current(Note 2)	I_{DM}	452	A	-
Single Pulse Avalanche Energy	E_{AS}	127	mJ	$L=0.5\text{mH}, V_D=48\text{V}, T_C=25^{\circ}\text{C}$
Power Dissipation	P_D	63	W	$T_C=25^{\circ}\text{C}$
		1.8		$T_a=25^{\circ}\text{C}$
Operating Junction and Storage Temperature Range	T_j, T_{STG}	-55~150	$^{\circ}\text{C}$	-

Thermal Characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Thermal resistance , junction – case	$R_{th(j-c)}$	-	-	2.0	$^{\circ}\text{C/W}$	-
Thermal resistance , junction - ambient(min.footprint)	$R_{th(j-a)}$	-	-	70		-

Electrical Characteristics (T_j=25°C, unless otherwise noted)

Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Breakdown Voltage	BV _{DSS}	60	-	-	V	V _{GS} =0V, I _D =250μA
Zero Gate Voltage Drain Current	I _{DSS}	-	0.03	1	μA	V _{DS} =60V, V _{GS} =0V, T _j =25°C
		-	-	100		V _{DS} =60V, V _{GS} =0V, T _j =150°C
Gate-Body Leakage Current	I _{GSS}	-	±10	±100	nA	V _{GS} =±20V, V _{DS} =0V
Gate-Source Threshold Voltage	V _{GS(th)}	1.0	-	2.5	V	V _{DS} =V _{GS} , I _D =250μA
Drain-Source On-State Resistance	R _{DS(on)}	-	2.55	3.5	mΩ	V _{GS} =10V, I _D =20A
		-	3.7	4.8		V _{GS} =4.5V, I _D =20A
Gate Resistance	R _g	-	2	-	Ω	V _{GS} =0V, V _{DS} =0V, f=1MHz
Forward Transconductance	g _{fs}	-	55	-	S	V _{DS} =5V, I _D =20A

Dynamic characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Input Capacitance	C _{iss}	-	3942	-	pF	V _{DS} =30V, V _{GS} =0V, f=1.0MHz
Output Capacitance	C _{oss}	-	3105	-		
Reverse Transfer Capacitance	C _{rss}	-	344	-		
Turn-On Delay Time	t _{d(on)}	-	18	-	ns	V _{DD} =30V, I _D =30A, V _{GS} =10V, R _G =10Ω
Turn-On Rise Time	t _r	-	4	-		
Turn-Off Delay Time	t _{d(off)}	-	90	-		
Turn-Off Fall Time	t _f	-	127	-		

Gate charge characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Total Gate Charge	Q _g	-	73	-	nC	V _{DS} =30V, I _D =20A, V _{GS} =10V
Gate-Source Charge	Q _{gs}	-	14	-		
Gate-Drain Charge	Q _{gd}	-	15	-		

Diode Characteristic

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Continuous Diode Forward Current	I _S	-	-	113	A	-
Pulsed Diode Forward Current	I _{SM}	-	-	452	A	-
Diode Forward Voltage	V _{SD}	-	0.8	1.2	V	I _S =20A, V _{GS} =0V
Reverse Recovery Time	t _{rr}	-	96	-	ns	V _{GS} =0V, I _S =30A
Reverse Recovery Charge	Q _{rr}	-	242	-	nC	di/dt=100A/μs

Notes

1. Computed continuous current assumes the condition of T_{j,Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under T_{j,Max} = 150°C.

RATING AND CHARACTERISTIC CURVES



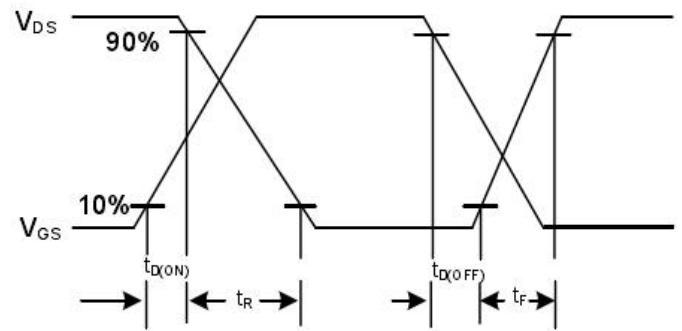
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms



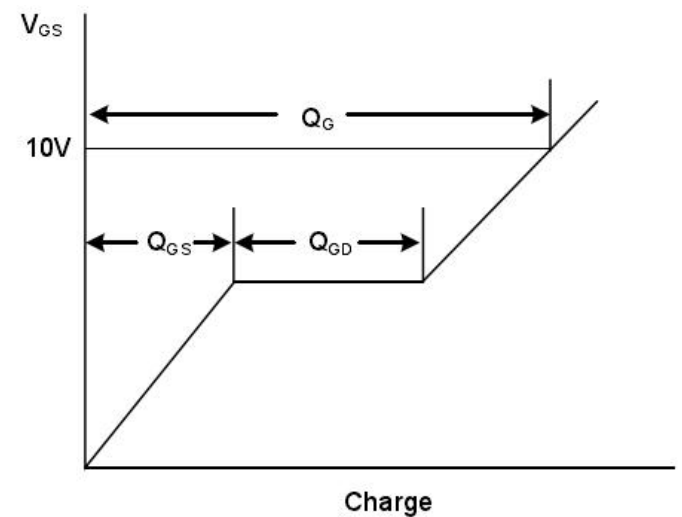
Switching Test Circuit



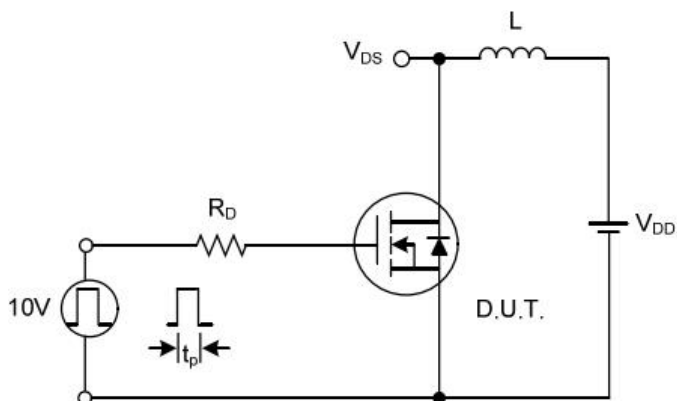
Switching Waveforms



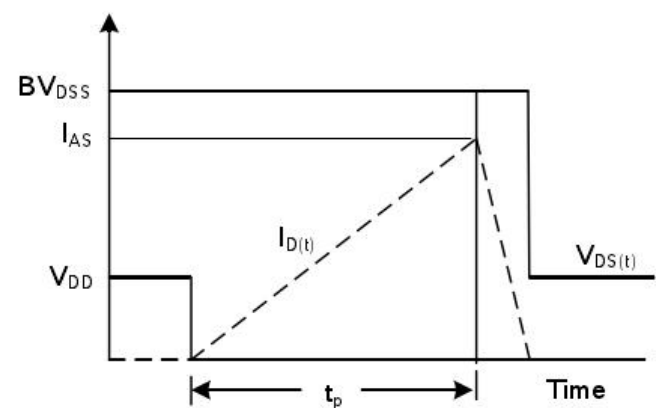
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES

Figure.1 Typical Output Characteristics

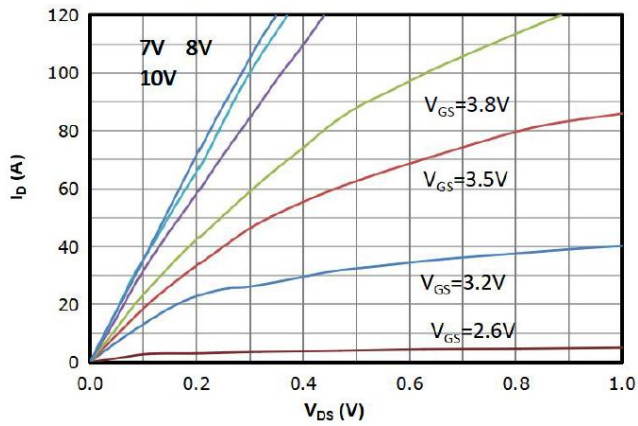


Figure.2 Transfer Characteristics

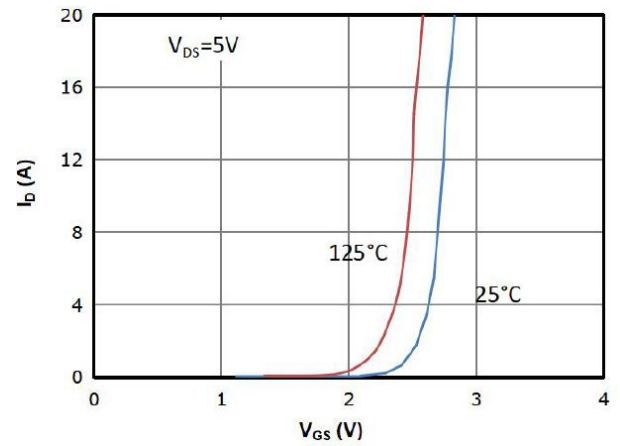


Figure.3 Rds(on) vs Drain Current and Gate Voltage

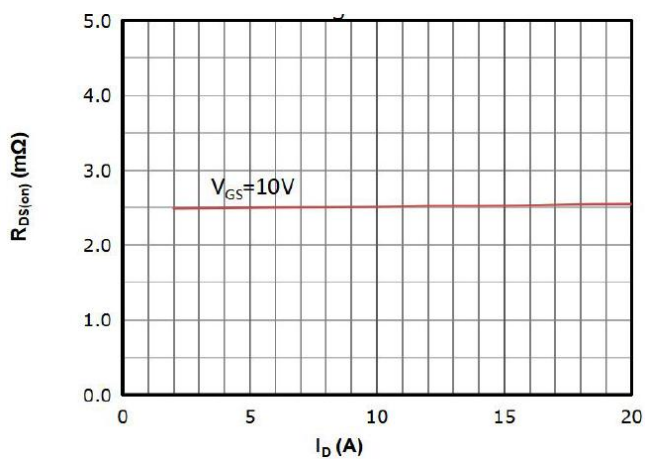


Figure.4 Rds(on) vs Gate Voltage

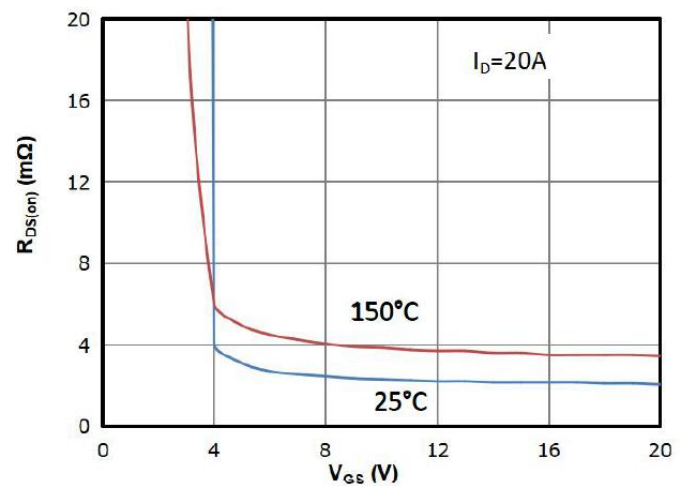


Figure.5 Rds(on) vs Temperature

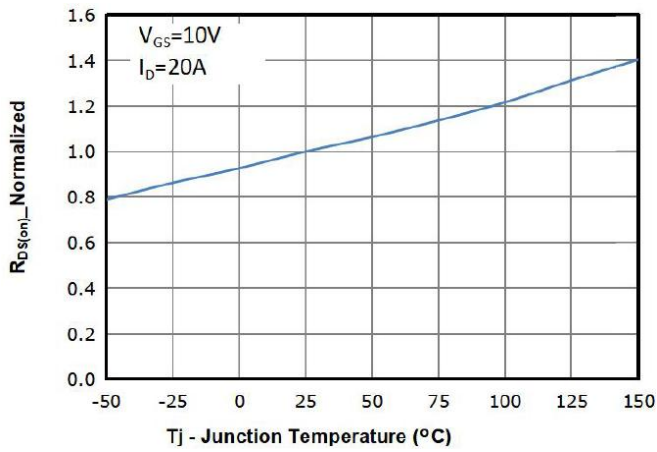


Figure.6 Vgs(th) vs Temperature

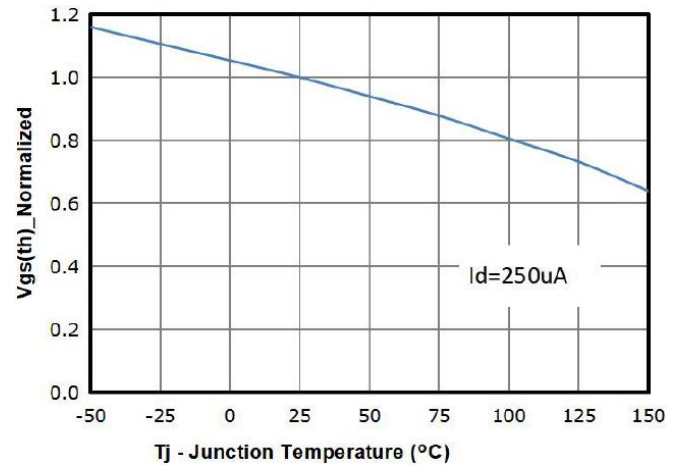


Figure.7 BVdss vs Temperature

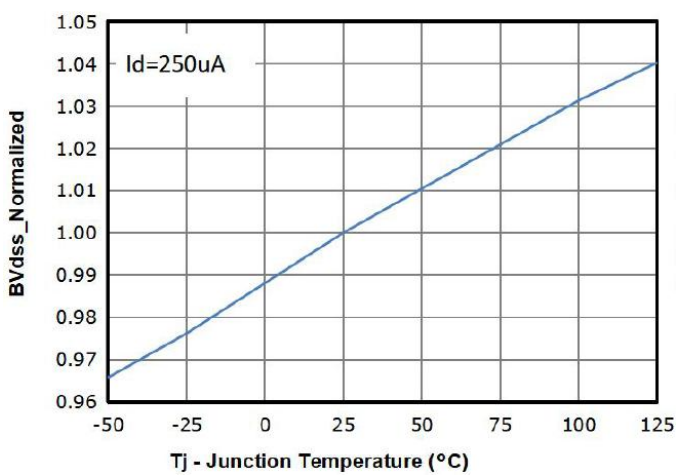


Figure.8 Capacitance Characteristics

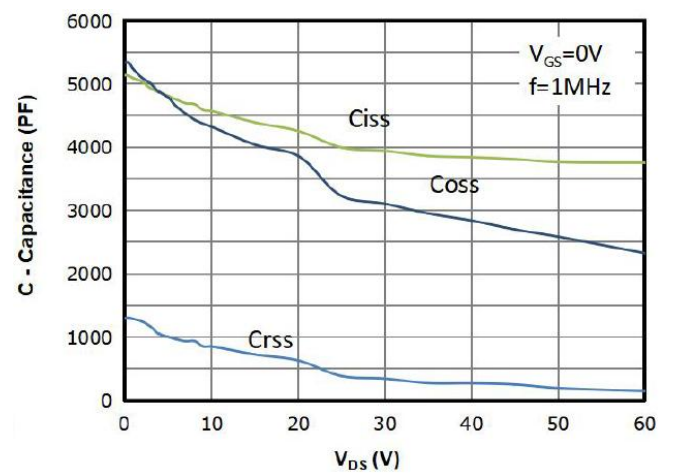


Figure.9 Gate Charge characteristics

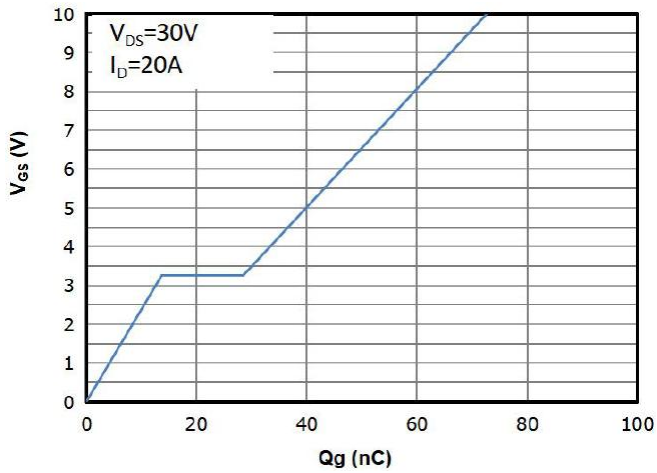


Figure.10 Body-Diode Forward Characteristics

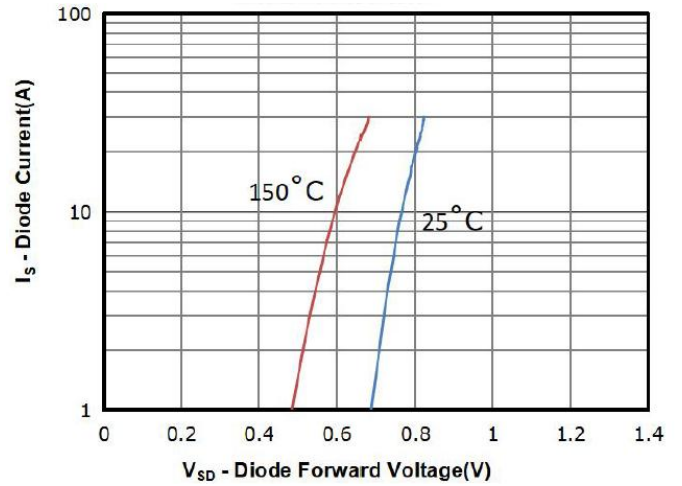


Figure.11 Power Dissipation

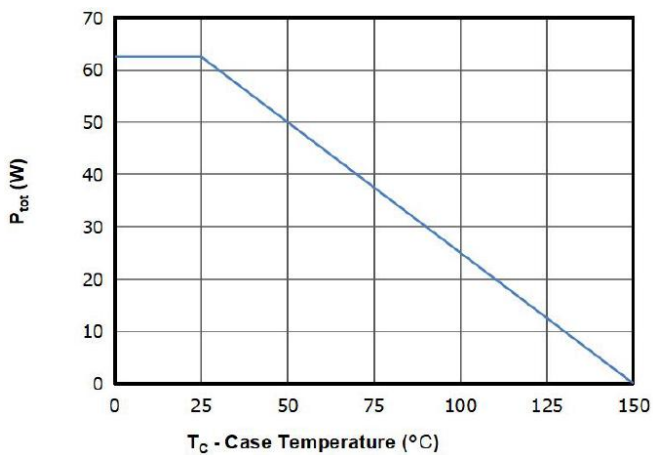


Figure.12 Drain Current Derating

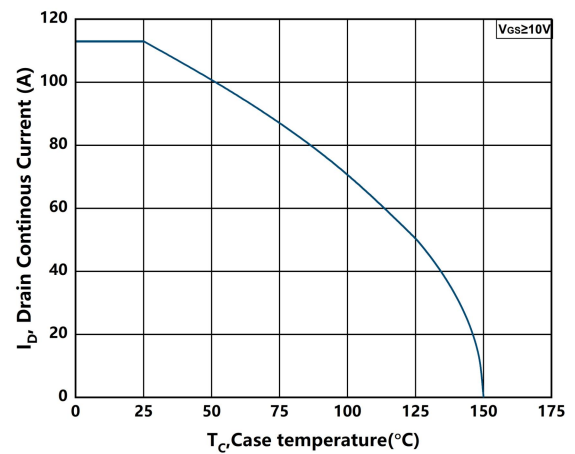


Figure.13 Safe Operating Area

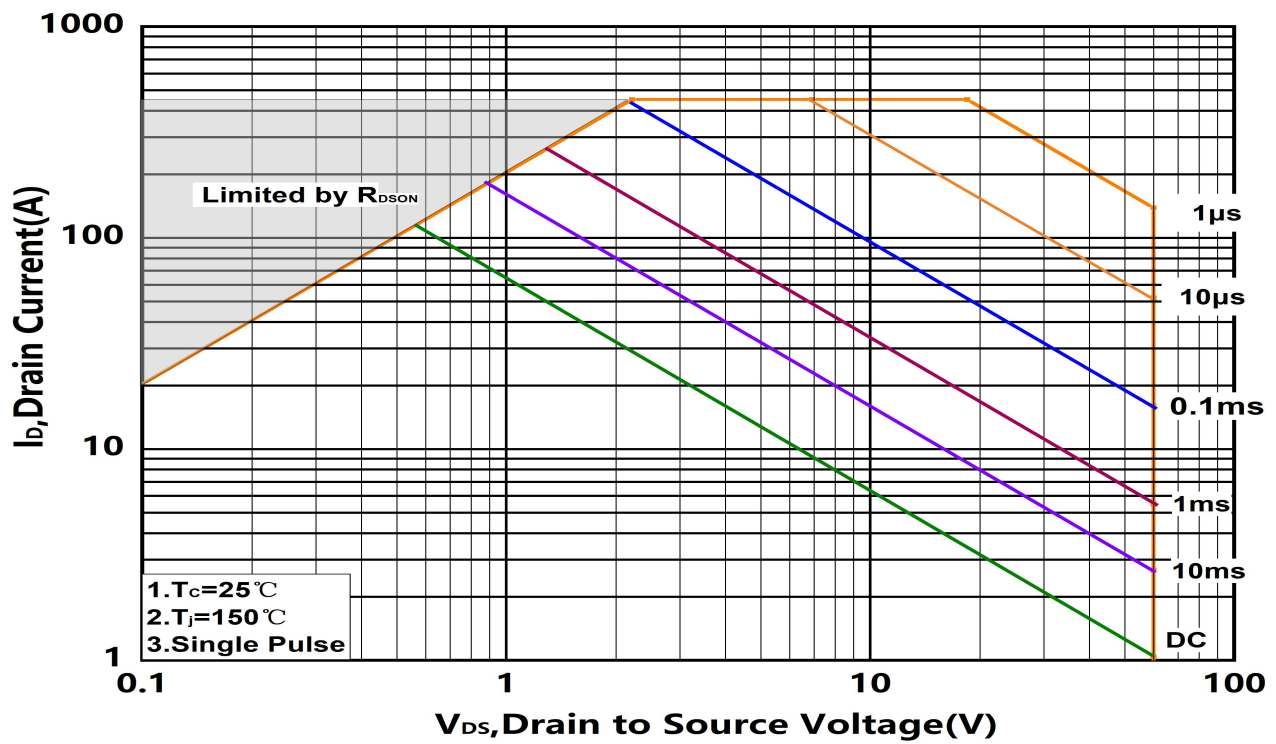
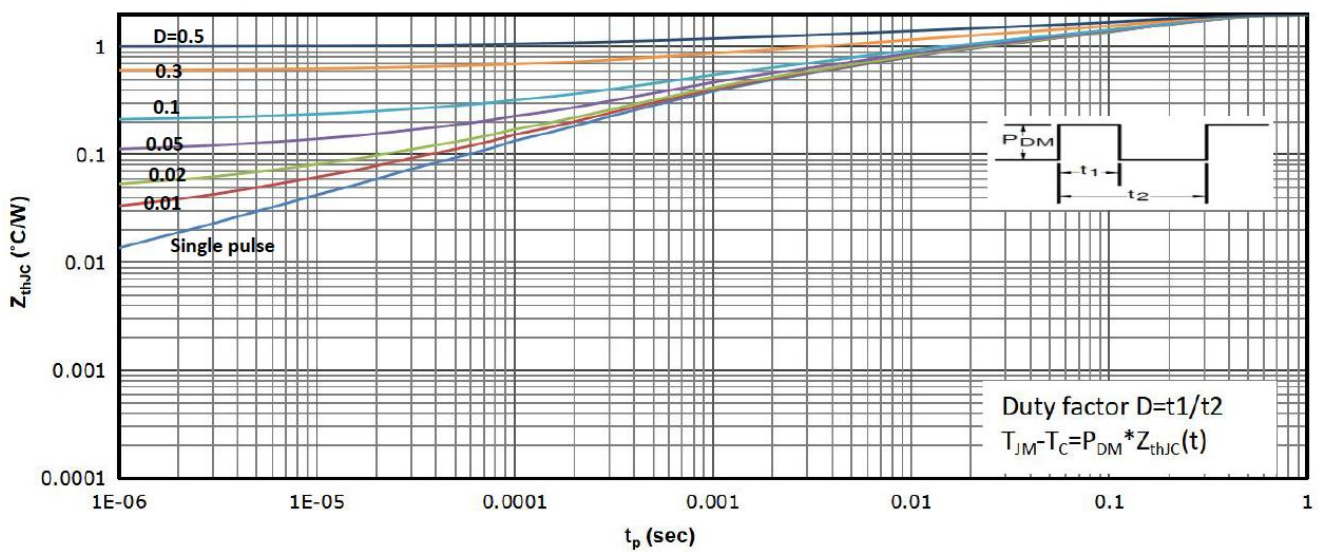
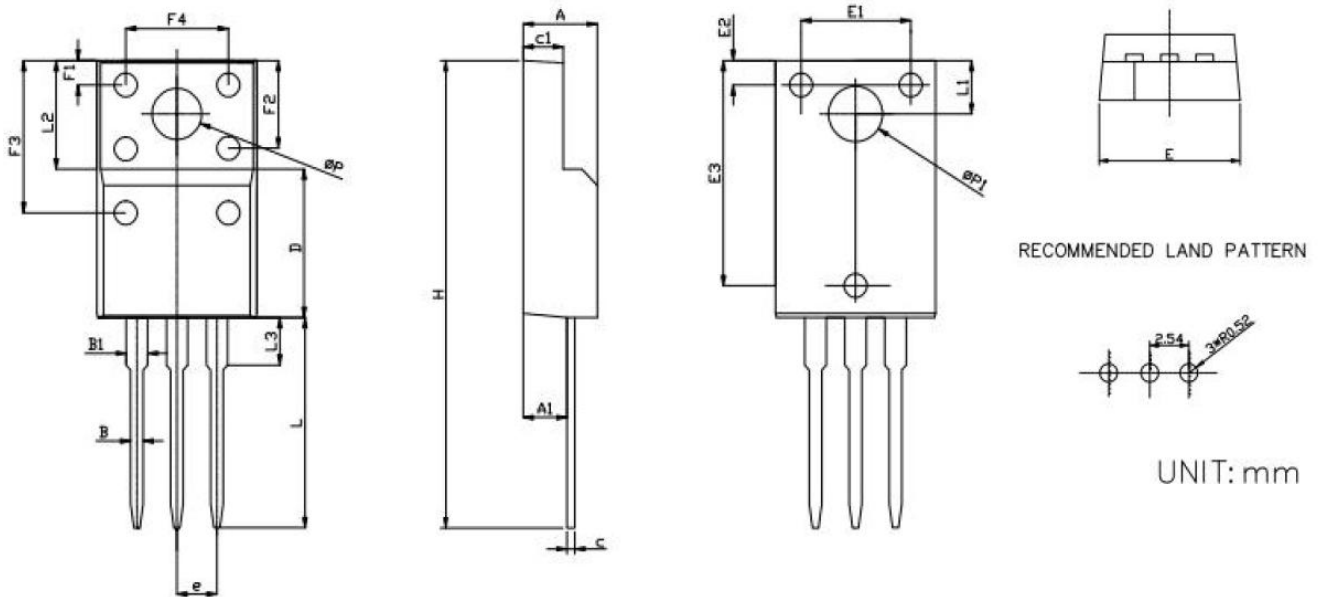


Figure.14 Max .Transient Thermal Impedance



Package Outline: TO-220MF-3L



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.50	4.90	0.177	0.193
A1	2.63	2.89	0.104	0.114
B	0.75	0.90	0.030	0.035
B1	1.15	1.55	0.045	0.061
C	0.40	0.60	0.016	0.024
C1	2.34	2.74	0.092	0.108
D	8.87	9.47	0.349	0.373
e	2.54		0.100	
E	9.86	10.46	0.388	0.412
E1	6.86	7.06	0.270	0.278
E2	1.40	1.60	0.055	
E3	13.80	14.00	0.543	0.551
F1	1.40	1.60	0.055	
F2	5.15	5.65	0.203	0.222
F3	9.10	9.70	0.358	0.382
F4	6.70	7.30	0.264	0.287
H	28.50	29.50	1.122	1.161
L	12.58	13.38	0.495	0.527
L1	3.15	3.45	0.124	0.136
L2	6.70		0.264	
L3	2.63	3.23	0.104	0.127
φP	2.90	3.48	0.114	0.137
φP1	3.15	3.75	0.124	0.148