

MOSFET

113A, 150V, 7.5mΩ N-CHANNEL MOSFET

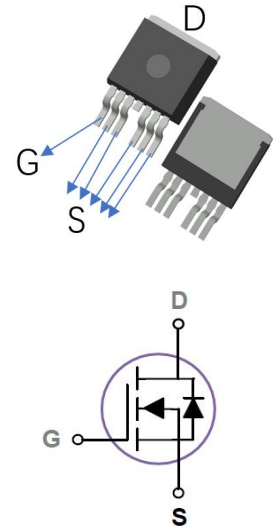
Features

- ◆ Extremely low on-resistance $R_{DS(on)}$
- ◆ Excellent Low Ciss

Applications

- ◆ Networking
- ◆ Load Switch
- ◆ LED applications
- ◆ Quick Charge

View and Internal
Schematic Diagram
TO-263-6L



Parameter	Values	Unit
BV_{DSS}	150	V
I_D	113	A
$R_{DS(on)}$ (max)	7.5	mΩ
$V_{GS(th)}$ (Typ)	3	V

Ordering Code	Marking	Package	Packaging
X7P5N15GHF6	X7P5N15GHF6	TO-263-6L	Tape and reel

Absolute Maximum Ratings($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Voltage	V_{DSS}	-	-	150	V	-
Gate-Source Voltage	V_{GS}	-20	-	20	V	-
Continuous Drain Current(Note 1)	I_D	-	-	113	A	$T_C=25^{\circ}\text{C}$
		-	-	71	A	$T_C=100$
Pulsed Drain Current(Note 2)	I_{DM}	-	-	452	A	-
Single Pulse Avalanche Energy	E_{AS}	-	-	992	mJ	$L=0.5\text{mH}, V_D=50\text{V}, T_C=25^{\circ}\text{C}$
Maximum Power Dissipation	P_D	-	-	208	W	$T_C=25^{\circ}\text{C}$
		-	-	2.2	W	$T_A=25^{\circ}\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-50	-	150	$^{\circ}\text{C}$	-

Thermal Characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Thermal resistance, Junction to Case	$R_{th(J-c)}$	-	0.6	-	$^{\circ}\text{C}/\text{W}$	-
Thermal resistance, Junction to Ambient	$R_{th(J-a)}$	-	56	-	$^{\circ}\text{C}/\text{W}$	-

Electrical Characteristics ($T_j=25^{\circ}\text{C}$, unless otherwise noted)

Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Breakdown Voltage	BV_{DSS}	150	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=150V, V_{GS}=0V$
Gate-Body Leakage Current, Forward	I_{GSSF}	-	-	100	nA	$V_{GS}=20V, V_{DS}=0V$
Gate-Body Leakage Current, Reverse	I_{GSSR}	-	-	-100	nA	$V_{GS}=-20V, V_{DS}=0V$
Gate-Source Threshold Voltage	$V_{GS(th)}$	2	-	4	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Drain-Source On-State Resistance	$R_{DS(on)}$	-	6.2	7.5	m Ω	$V_{GS}=10V, I_D=80A$
Gate Resistance	R_g	-	1.4	-	Ω	$V_{GS}=0V, V_{DS}=0V, f=1\text{MHz}$
Forward Transconductance	g_{fs}	-	85	-	S	$V_{DS}=5V, I_D=80A$

Dynamic characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Input Capacitance	C_{iss}	-	6713	-	pF	$V_{DS}=75V, V_{GS}=0V, f=1.0\text{MHz}$
Output Capacitance	C_{oss}	-	495	-		
Reverse Transfer Capacitance	C_{rss}	-	28	-		
Turn-On Delay Time	$t_{d(on)}$	-	27	-	ns	$V_{DD}=75V, R_{G-ext}=1.6\Omega, V_{GS}=10V, I_D=37.5A$
Turn-On Rise Time	t_r	-	83	-		
Turn-Off Delay Time	$t_{d(off)}$	-	91	-		
Turn-Off Fall Time	t_f	-	50	-		

Gate charge characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Total Gate Charge	Q_g	-	134	-	nC	$V_{DS}=75V, I_D=40A, V_{GS}=10V$
Gate-Source Charge	Q_{gs}	-	27	-		
Gate-Drain Charge	Q_{gd}	-	46	-		

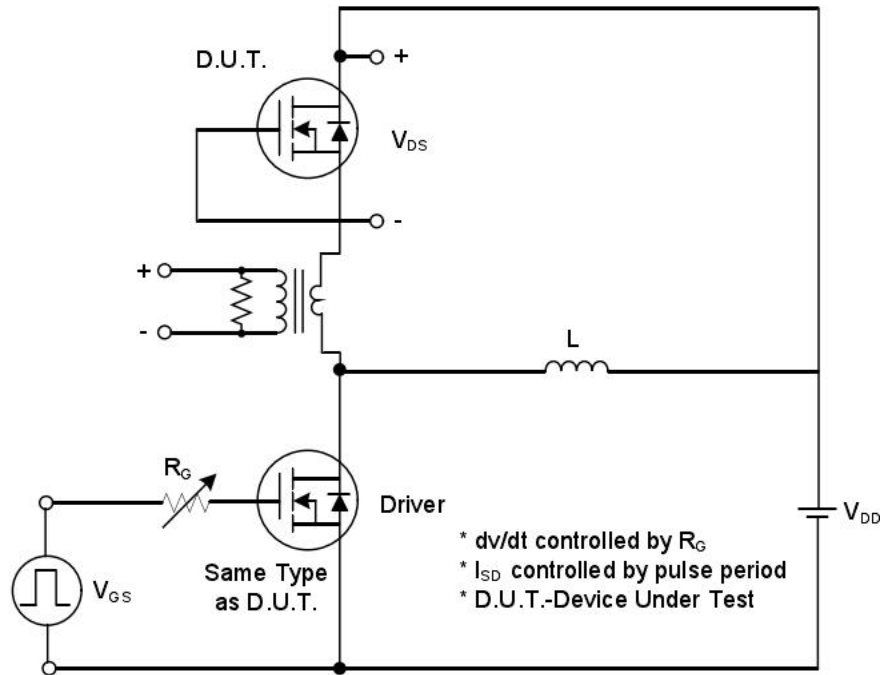
Reverse diode

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Continuous Diode Forward Current	I_S	-	-	113	A	-
Pulsed Diode Forward Current	I_{SM}	-	-	452	A	-
Diode Forward Voltage	V_{SD}	-	-	1.2	V	$I_S=80A, V_{GS}=0V$
Reverse Recovery Time	t_{rr}	-	80	-	ns	$V_{GS}=0V, I_S=40A, di/dt=100A/\mu s, T_j=25^{\circ}\text{C}$
Reverse Recovery Charge	Q_{rr}	-	545	-	nC	

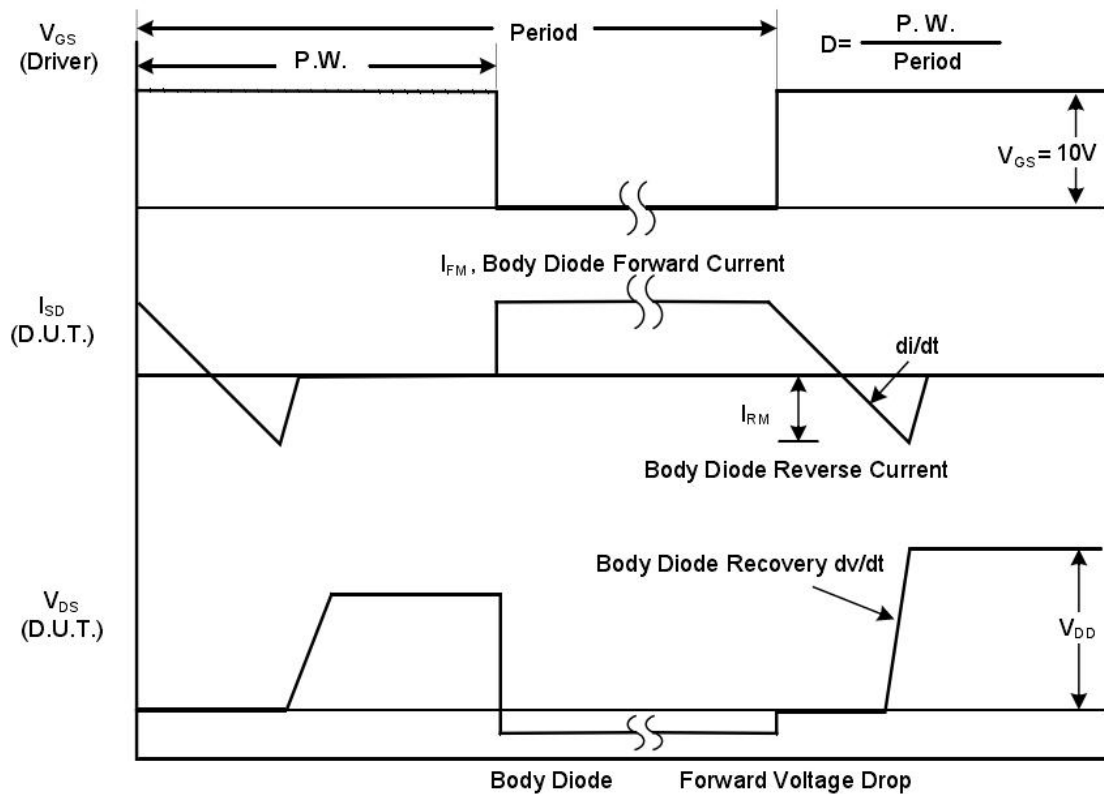
Notes

1. Computed continuous current assumes the condition of T_{j_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under $T_{j_Max} = 150^{\circ}\text{C}$.

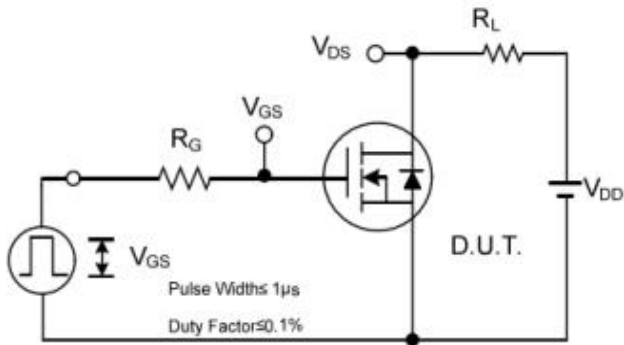
RATING AND CHARACTERISTIC CURVES



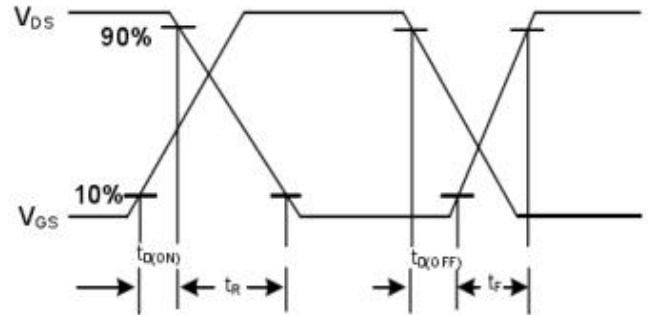
Peak Diode Recovery dv/dt Test Circuit



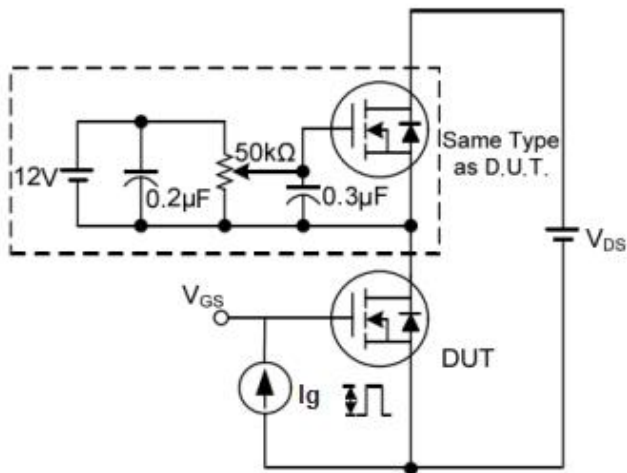
Peak Diode Recovery dv/dt Waveforms



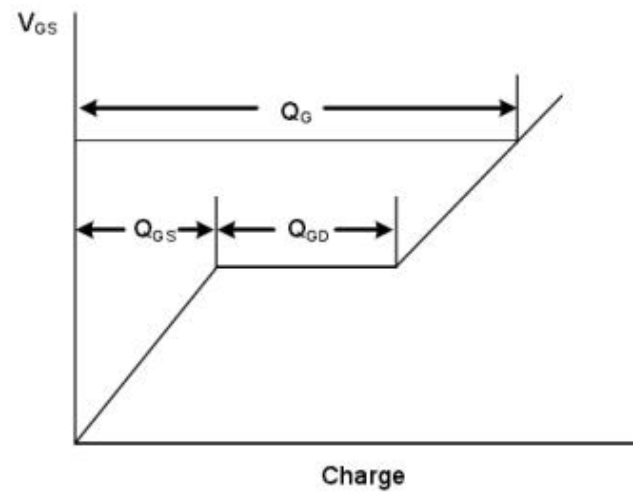
Switching Test Circuit



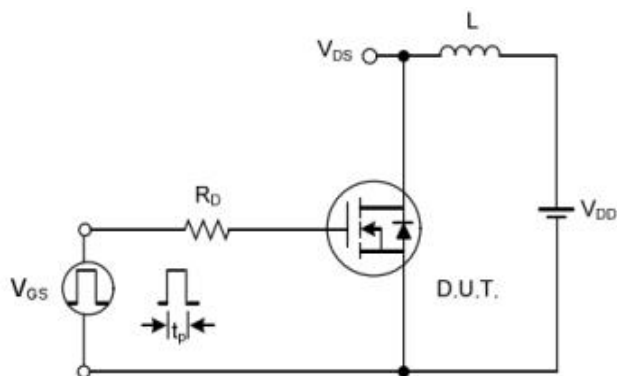
Switching Waveforms



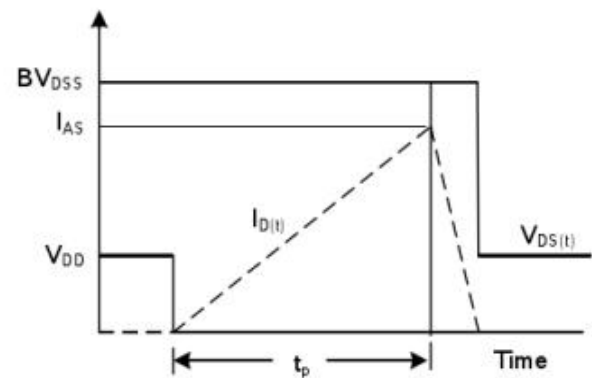
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES

Figure.1 Output Characteristics $T_J=25^\circ\text{C}$

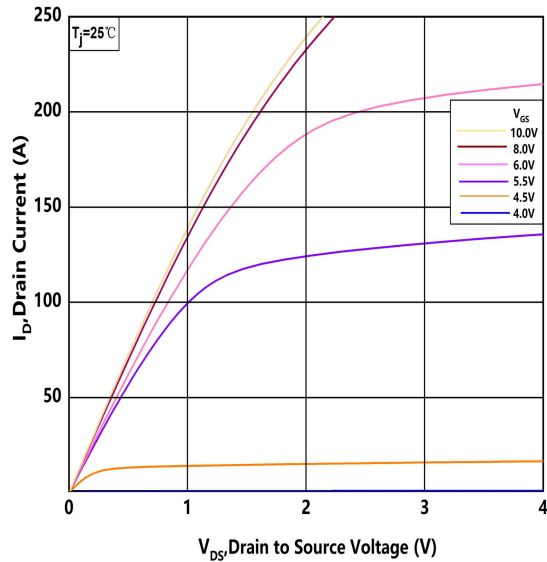


Figure.2 Transfer Characteristic for Various Junction Temperatures

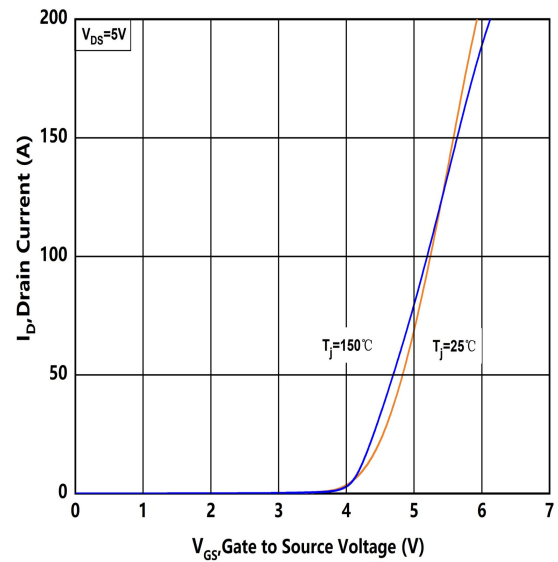


Figure.3 On-Resistance vs Drain Current For Various Gate Voltage

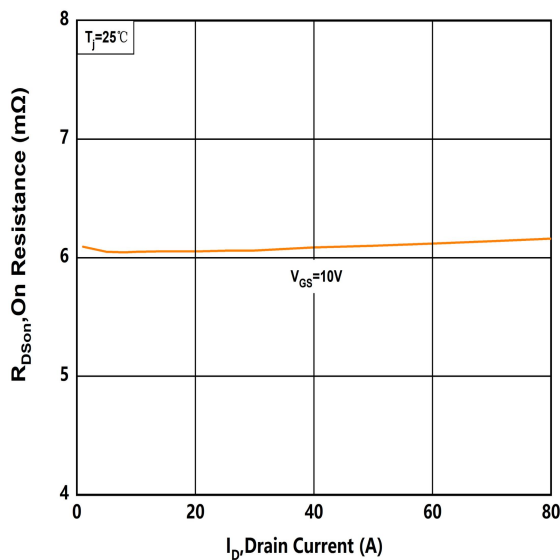


Figure.4 Typical On-Resistance vs Junction Temperature

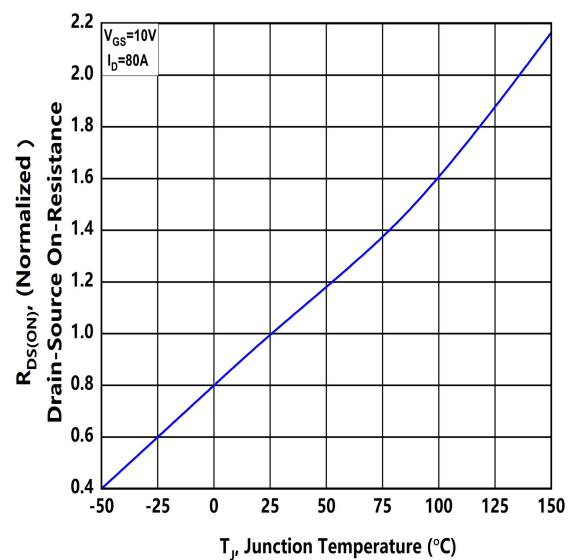


Figure.5 Typical Threshold Voltage vs Junction Temperature

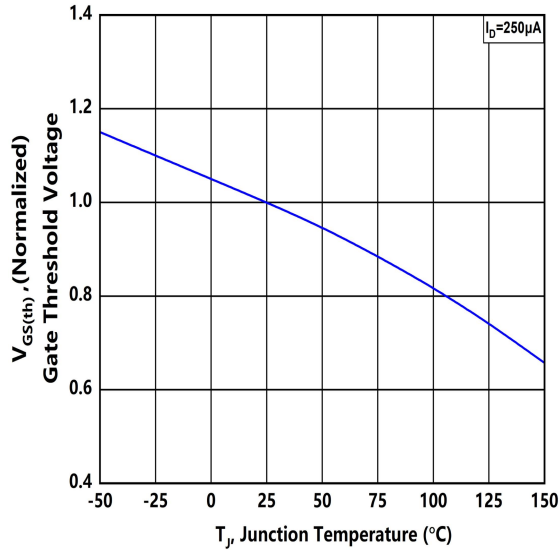


Figure.6 Typical Breakdown Voltage vs Junction Temperature

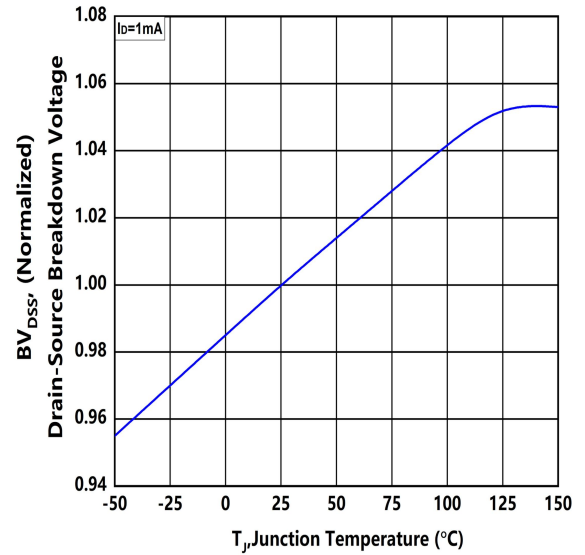


Figure.7 Typical Capacitance vs Drain to Source Voltage

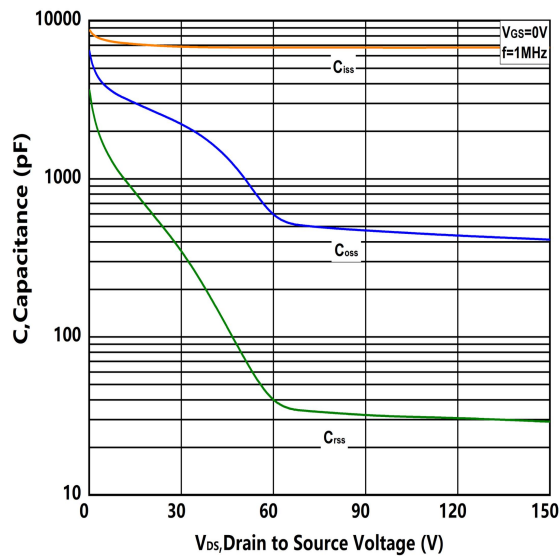


Figure.8 Typical Gate Charge vs Gate to Source Voltage

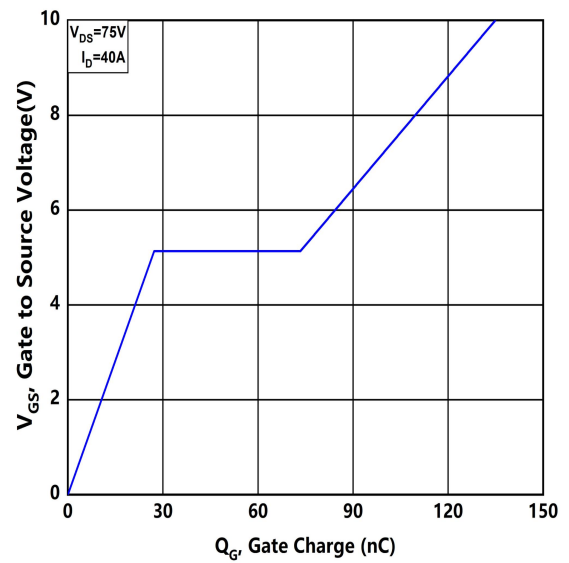


Figure.9 Typical Body Diode Characteristics

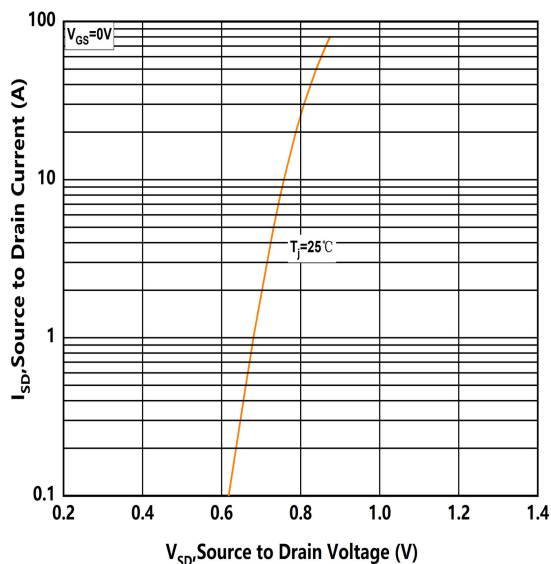


Figure.10 Maximum power Dissipation Derating vs Case Temperature

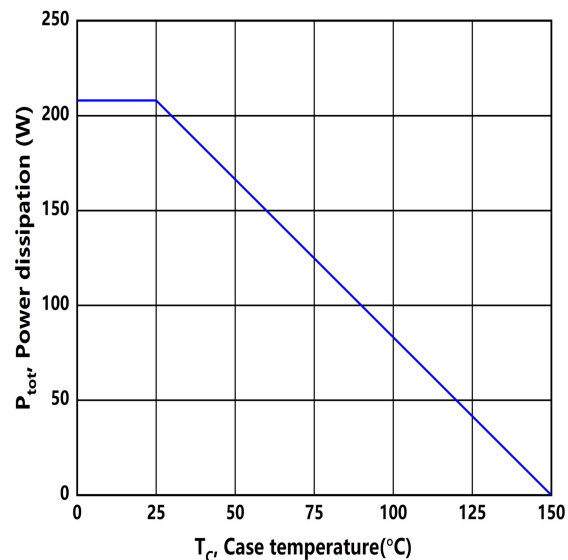


Figure.11 Continuous Drain Current Derating vs Case Temperature

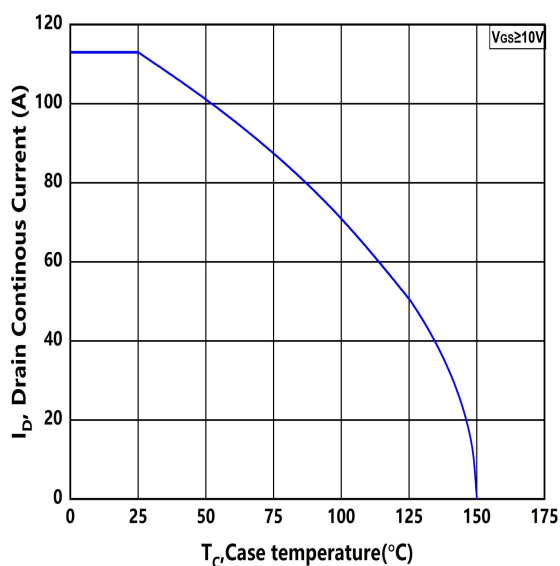


Figure.12 Safe Operating Area

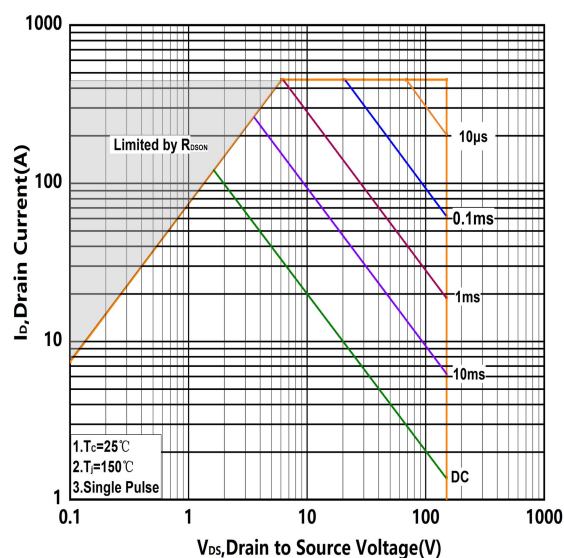
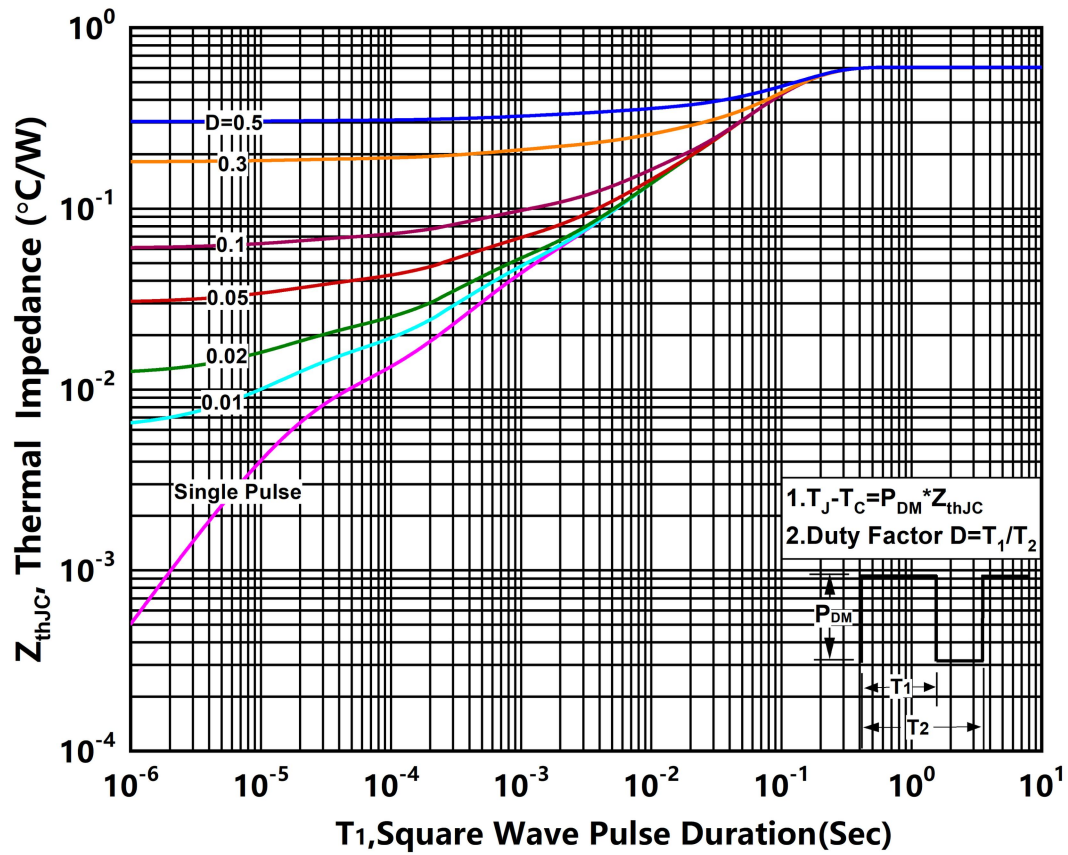
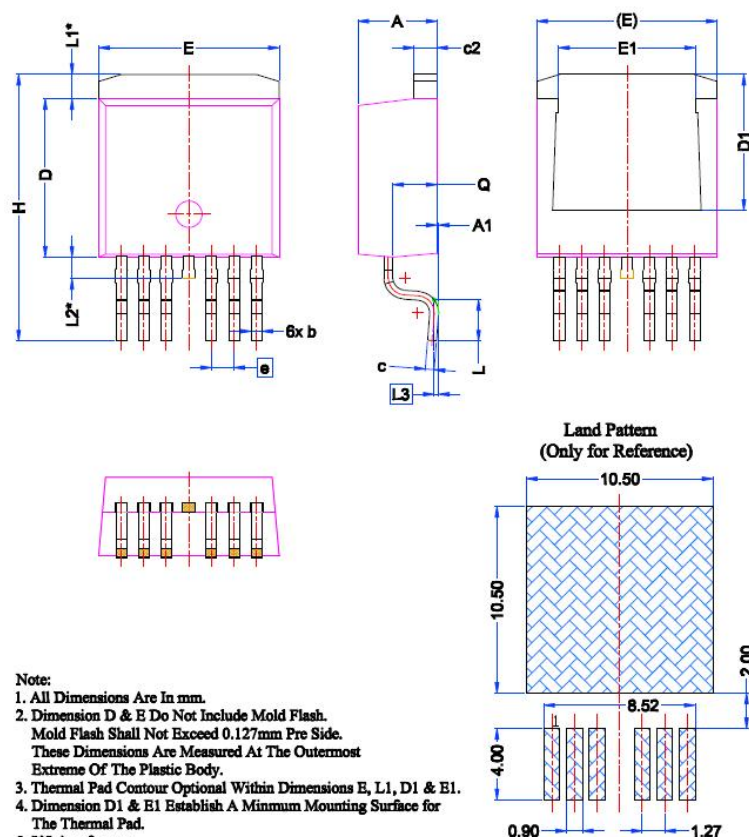


Figure.13 Transient Thermal Impedance (Junction - Case)



Package Outline:TO-263-6L



- Note:
1. All Dimensions Are In mm.
 2. Dimension D & E Do Not Include Mold Flash.
Mold Flash Shall Not Exceed 0.127mm Pre Side.
These Dimensions Are Measured At The Outermost Extreme Of The Plastic Body.
 3. Thermal Pad Contour Optional Within Dimensions E, L1, D1 & E1.
 4. Dimension D1 & E1 Establish A Minimum Mounting Surface for The Thermal Pad.
 5. "REF" is reference.
 6.  is Exposed Cu.
 7. There Is Exposed Cu and Molding Flash Bleeding At The Pin Which Is Close To Package.

SYMBOL	DIMENSIONS		
	MIN	NOM	MAX
A	4.24	4.44	4.64
A1	0.00	0.10	0.25
b	0.50	0.60	0.70
c	0.40	0.50	0.60
c 2	1.15	1.27	1.40
D	8.82	8.92	9.02
D1	6.86	7.65	-
E	9.96	10.16	10.36
E1	6.89	7.77	7.89
e	1.27BSC		
H	14.61	15.00	15.88
L	1.78	2.32	2.79
L1	1.36REF		
L2	1.20REF		
L3	0.25REF		
Q	2.30	2.48	2.70