

MOSFET

120A,60V,3mΩ,N-CHANNEL MOSFET

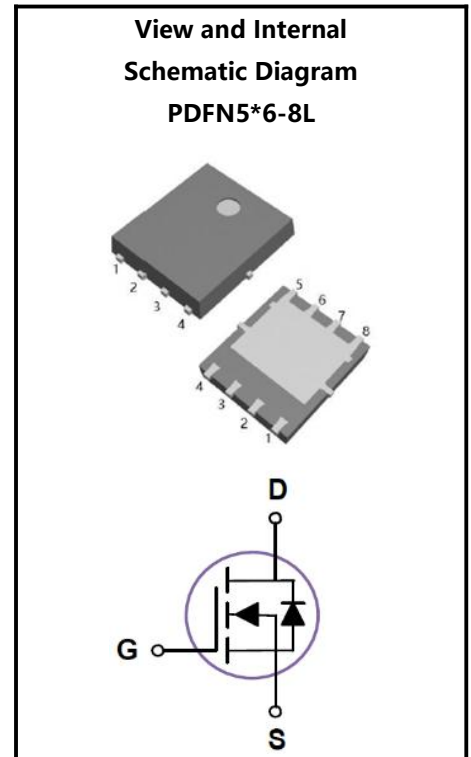
Features

- ◆ Low gate charge
- ◆ Low Ciss
- ◆ Fast switching
- ◆ 100% avalanche tested

Applications

- ◆ Networking
- ◆ Secondary Side SR
- ◆ High-frequency SMPS
- ◆ DC-DC Power Management

Parameter	Values	Unit
BV_{DSS}	60	V
I_D	120	A
$R_{Dson}(max)$	3	mΩ
$V_{GS(th)}(Typ)$	1.6	V



Ordering Code	Marking	Package	Packaging
X3N060GLV8	X3N060GLV8	PDFN5*6-8L	Tape & Reel

Absolute Maximum Ratings($T_C=25^{\circ}C$, unless otherwise noted)

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Voltage	V_{DSS}	-	-	60	V	-
Gate-Source Voltage	V_{GS}	-16	-	16	V	-
Continuous Drain Current(Note 1)	I_D	-	-	120	A	$T_C=25^{\circ}C$
		-	-	76	A	$T_C=100^{\circ}C$
Pulsed Drain Current(Note 2)	I_{DM}	-	-	480	A	-
Single Pulse Avalanche Energy	E_{AS}	-	-	351	mJ	$L=0.5mH, V_D=48V, T_C=25^{\circ}C$
Maximum Power Dissipation	P_D	-	-	73	W	$T_C=25^{\circ}C$
		-	-	2	W	$T_A=25^{\circ}C$
Operating Junction and Storage Temperature Range	T_{j}, T_{STG}	-50	-	150	$^{\circ}C$	-

Thermal Characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Thermal resistance , Junction to Case	$R_{th(J-c)}$	-	1.71	-	$^{\circ}C/W$	-
Thermal resistance , Junction to Ambient	$R_{th(J-a)}$	-	63	-		-

Electrical Characteristics ($T_j=25^{\circ}\text{C}$, unless otherwise noted)

Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Breakdown Voltage	BV_{DSS}	60	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=60V, V_{GS}=0V$
Gate-Body Leakage Current, Forward	I_{GSSF}	-	-	100	nA	$V_{GS}=16V, V_{DS}=0V$
Gate-Body Leakage Current, Reverse	I_{GSSR}	-	-	-100	nA	$V_{GS}=-16V, V_{DS}=0V$
Gate-Source Threshold Voltage	$V_{GS(th)}$	1	-	2.5	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Drain-Source On-State Resistance	$R_{DS(on)}$	-	2.5	3	m Ω	$V_{GS}=10V, I_D=15A$
		-	4.2	5.4	m Ω	$V_{GS}=4.5V, I_D=10A$
Gate Resistance	R_g	-	2.3	-	Ω	$V_{GS}=0V, V_{DS}=0V, f=1\text{MHz}$
Forward Transconductance	g_{fs}	-	42	-	S	$V_{DS}=5V, I_D=20A$

Dynamic characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Input Capacitance	C_{iss}	-	3487	-	pF	$V_{DS}=25V, V_{GS}=0V, f=1.0\text{MHz}$
Output Capacitance	C_{oss}	-	1335	-	pF	
Reverse Transfer Capacitance	C_{rss}	-	122	-	pF	
Turn-On Delay Time	$t_{d(on)}$	-	19	-	ns	$V_{DD}=30V, R_g=10\Omega, V_{GS}=10V, I_D=30A$
Turn-On Rise Time	t_r	-	98	-	ns	
Turn-Off Delay Time	$t_{d(off)}$	-	199	-	ns	
Turn-Off Fall Time	t_f	-	201	-	ns	

Gate charge characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Total Gate Charge	Q_g	-	73	-	nC	$V_{DS}=30V, I_D=20A, V_{GS}=10V$
Gate-Source Charge	Q_{gs}	-	11	-	nC	
Gate-Drain Charge	Q_{gd}	-	19	-	nC	

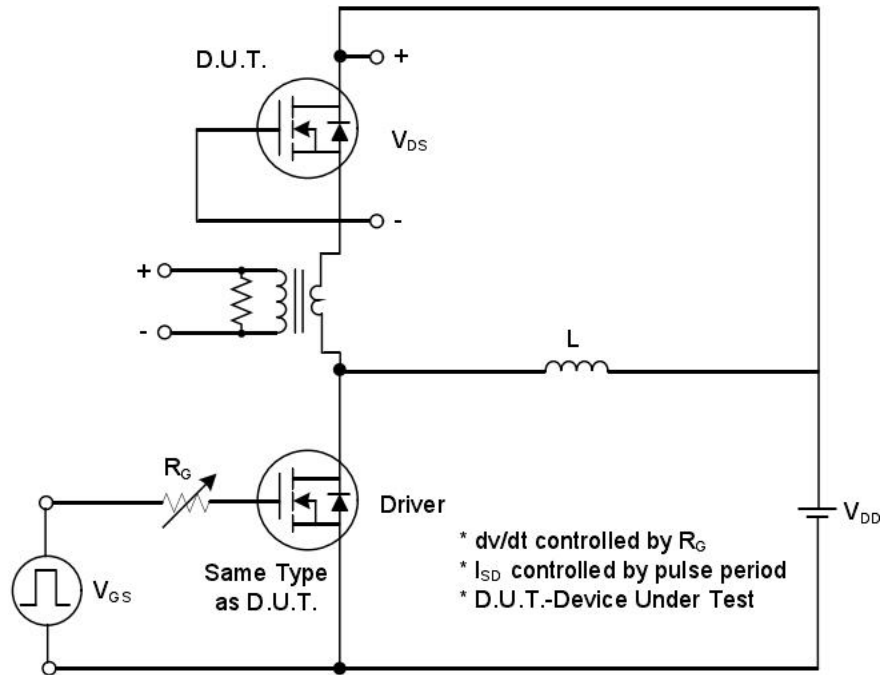
Reverse diode

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Continuous Diode Forward Current	I_S	-	-	120	A	-
Pulsed Diode Forward Current	I_{SM}	-	-	480	A	-
Diode Forward Voltage	V_{SD}	-	-	1.2	V	$I_S=15A, V_{GS}=0V$
Reverse Recovery Time	t_{rr}	-	20	-	ns	$V_{DS}=30V, V_{GS}=0V, I_S=30A, T_j=25^{\circ}\text{C}$
Reverse Recovery Charge	Q_{rr}	-	6	-	nC	$di/dt=100A/\mu s$

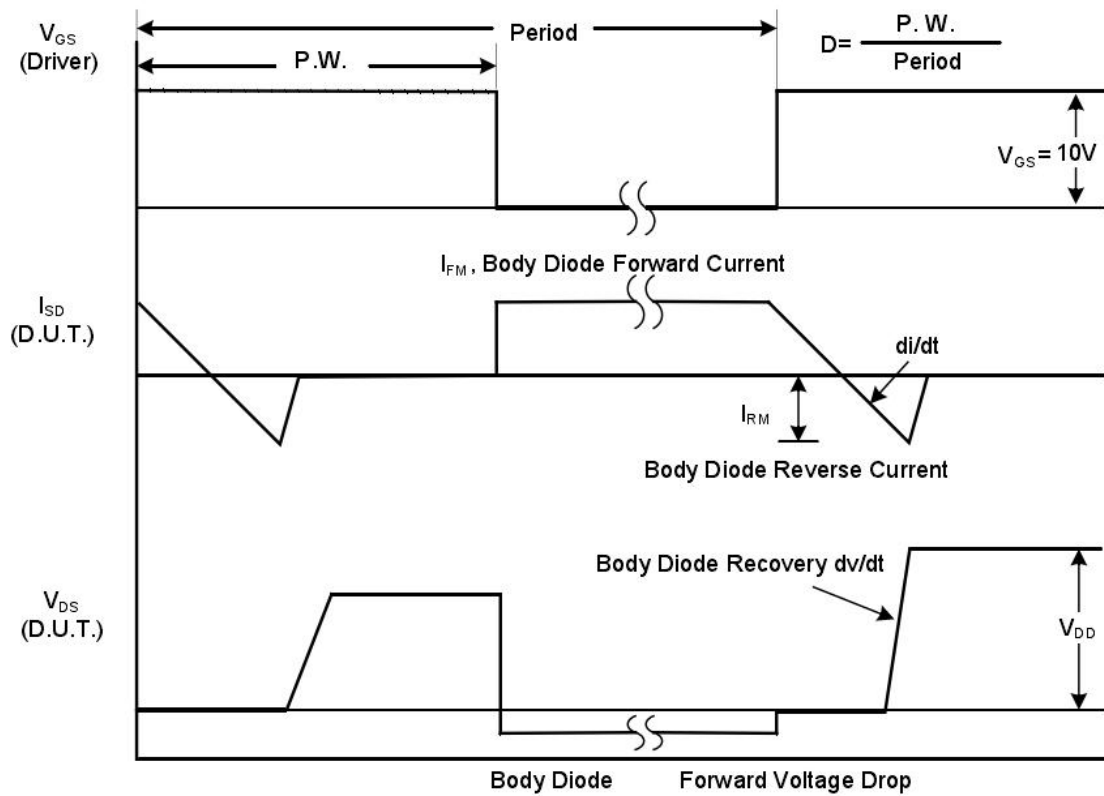
Notes

1. Computed continuous current assumes the condition of T_{j_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under $T_{j_Max} = 150^{\circ}\text{C}$.

RATING AND CHARACTERISTIC CURVES



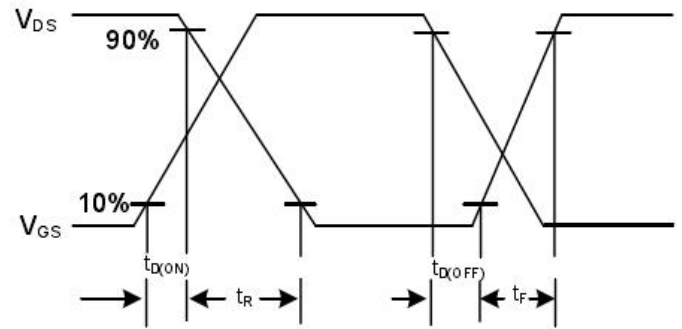
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms



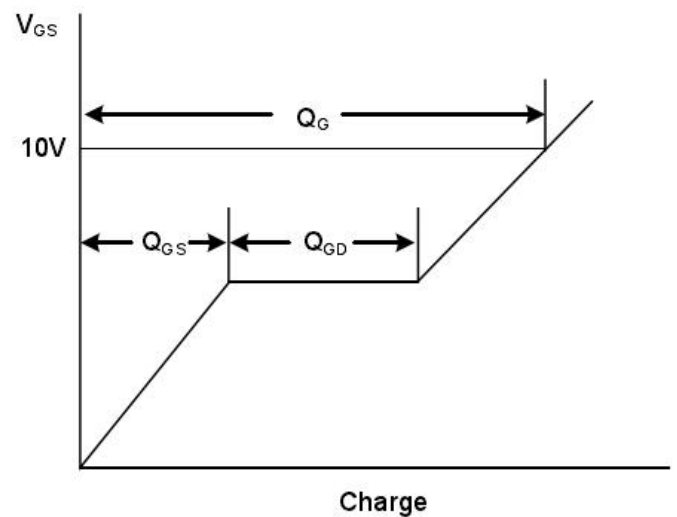
Switching Test Circuit



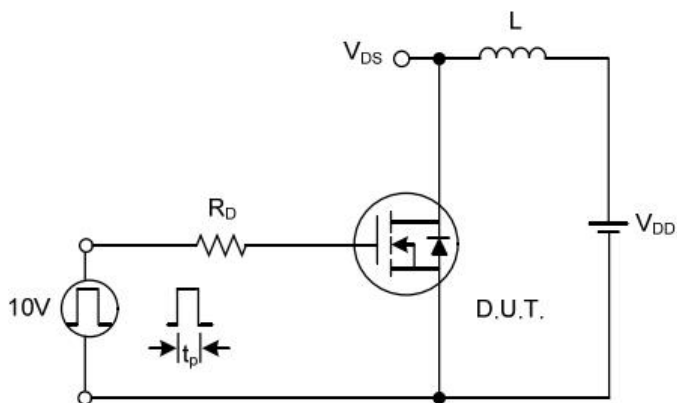
Switching Waveforms



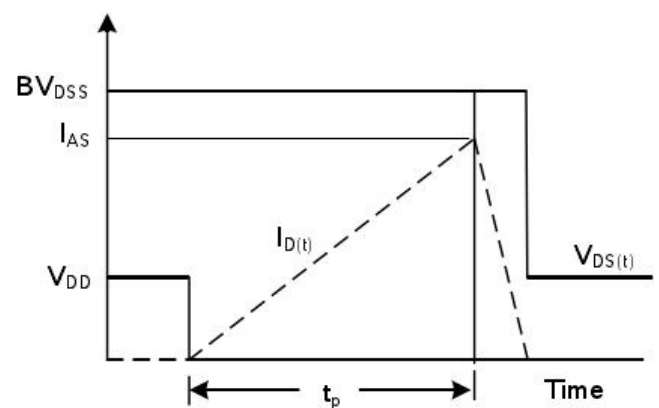
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES

Figure.1 Output Characteristics $T_J=25^{\circ}\text{C}$

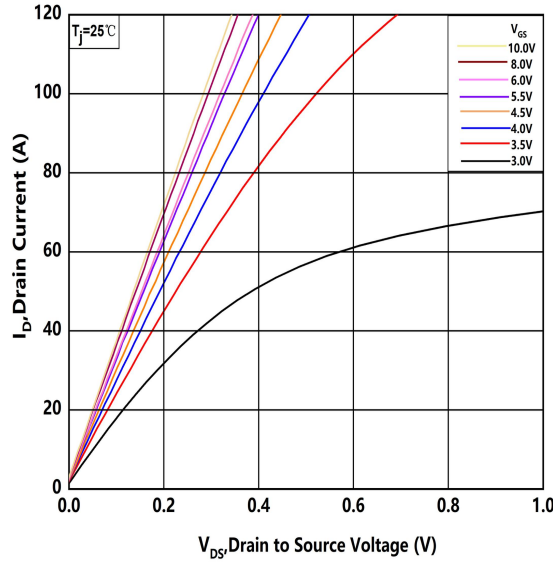


Figure.2 Transfer Characteristic for Various Junction Temperatures

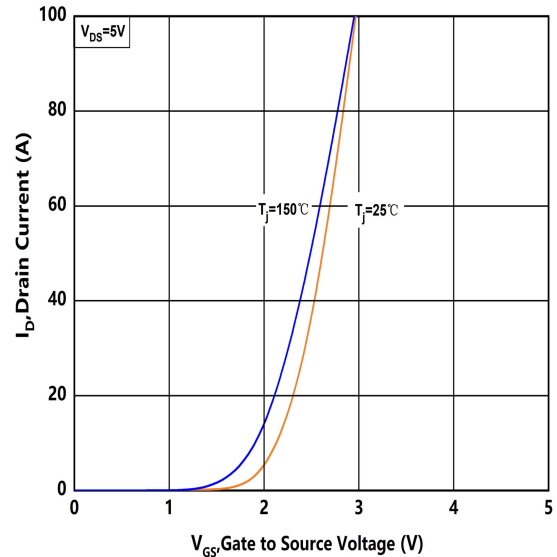


Figure.3 On-Resistance vs Drain Current For Various Gate Voltage

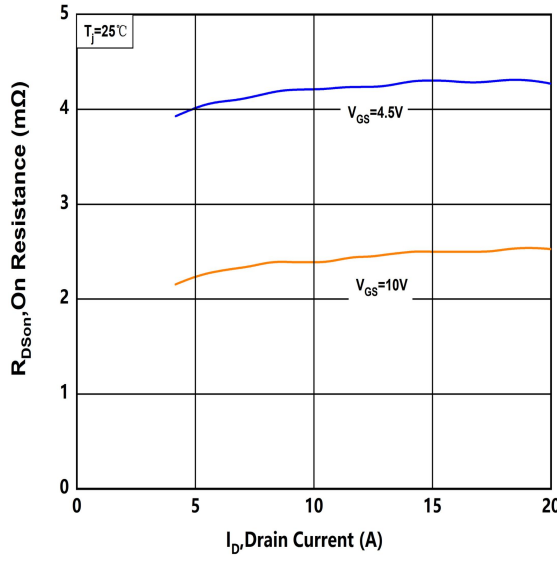


Figure.4 Typical On-Resistance vs Junction Temperature

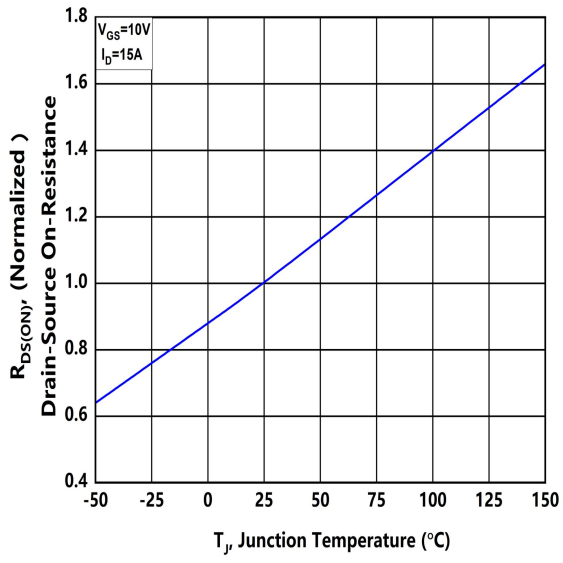


Figure.5 Typical Threshold Voltage vs Junction Temperature

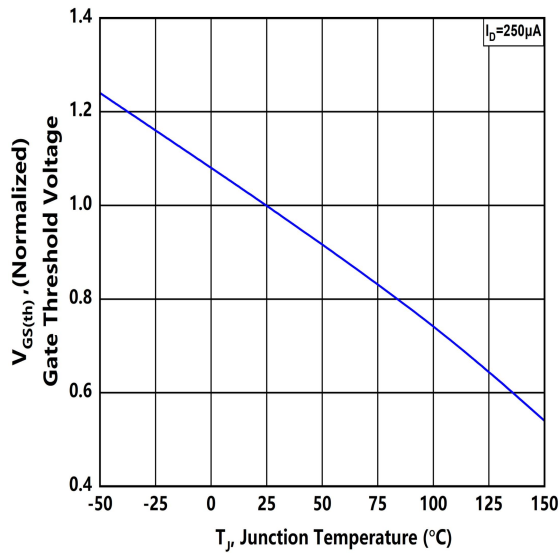


Figure.6 Typical Breakdown Voltage vs Junction Temperature

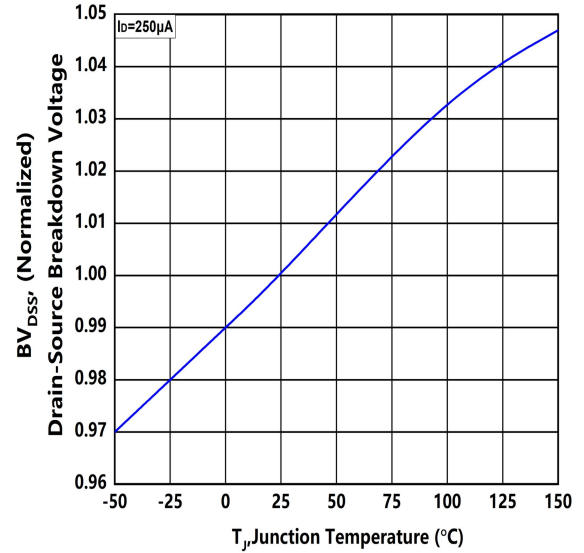


Figure.7 Typical Capacitance vs Drain to Source Voltage

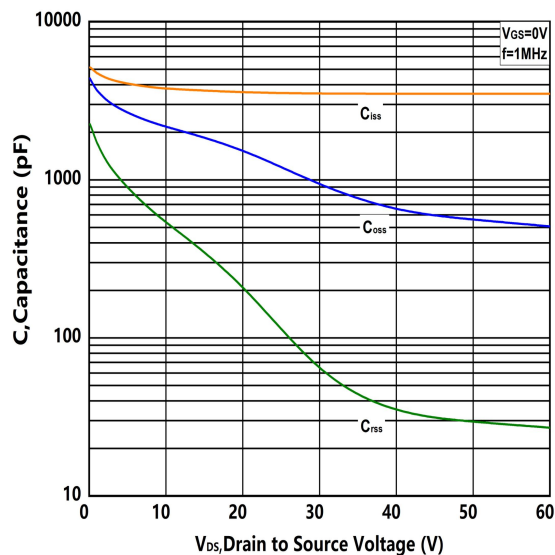


Figure.8 Typical Gate Charge vs Gate to Source Voltage

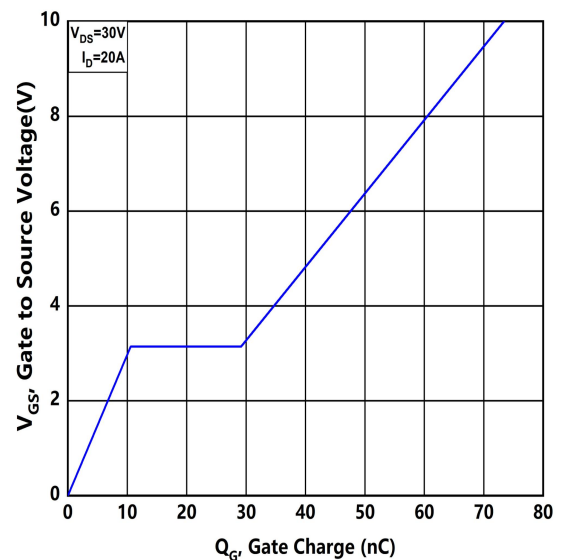


Figure.9 Typical Body Diode Characteristics

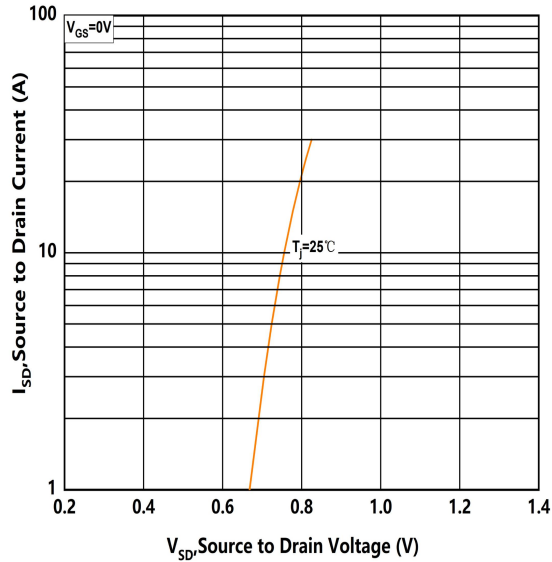


Figure.10 Maximum power Dissipation Derating vs Case Temperature

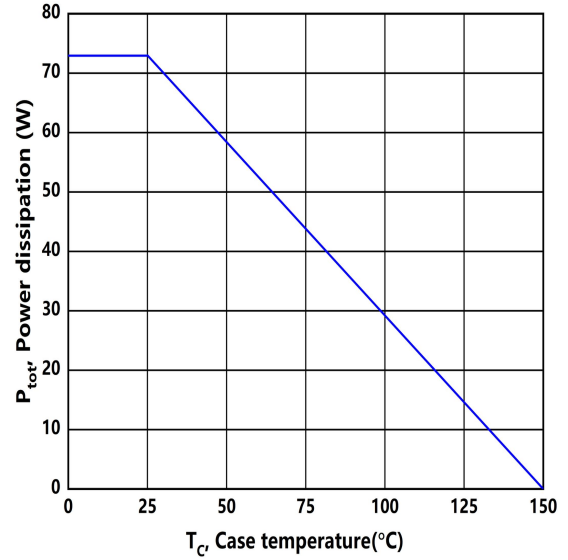


Figure.11 Continuous Drain Current Derating vs Case Temperature

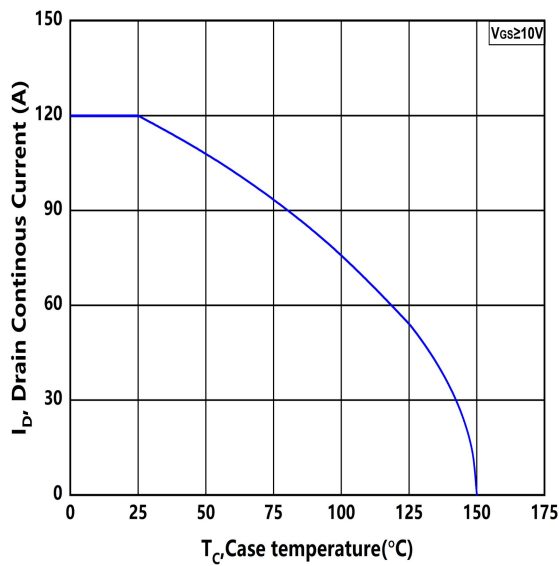


Figure.12 Safe Operating Area

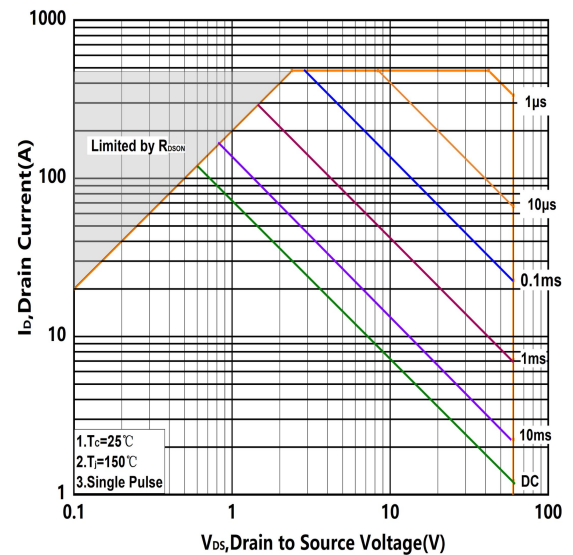
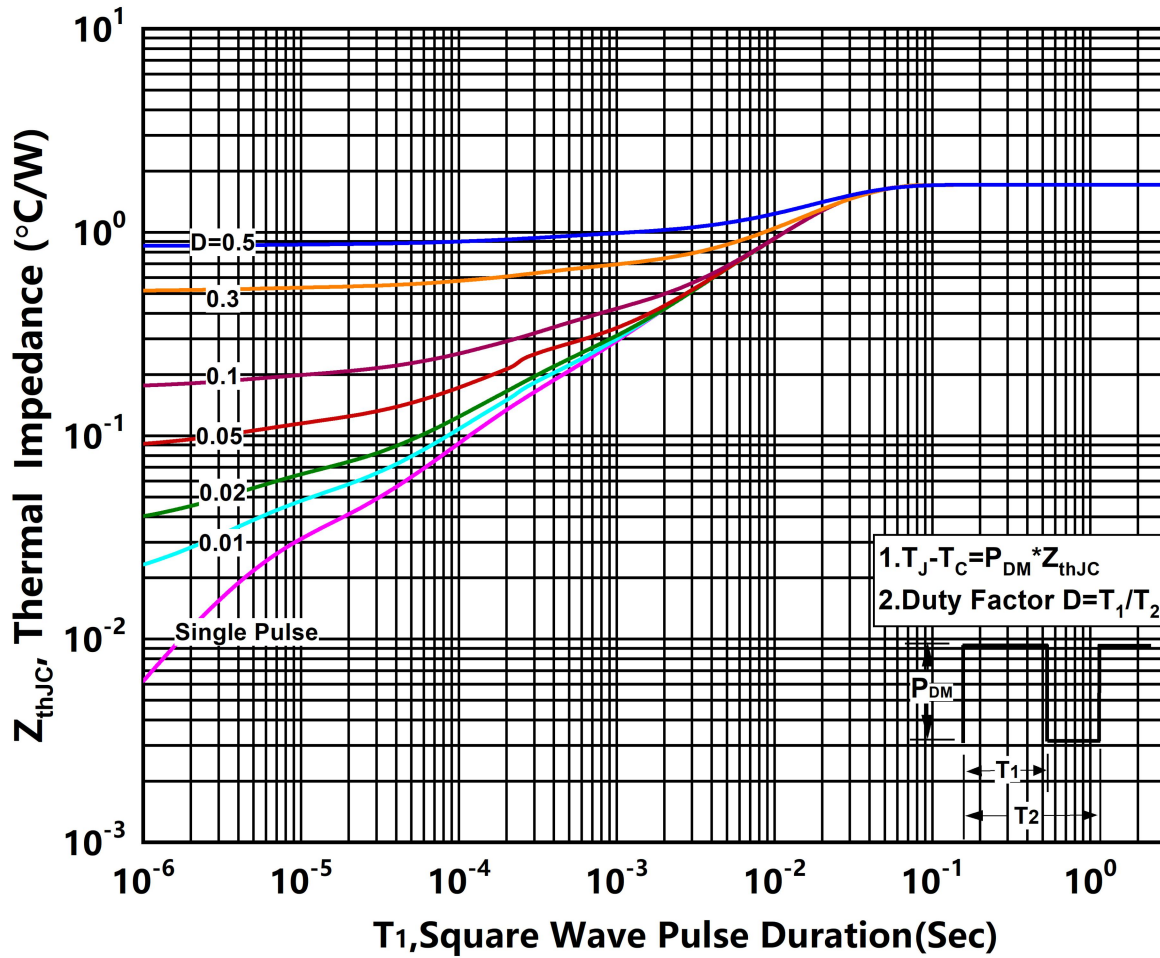
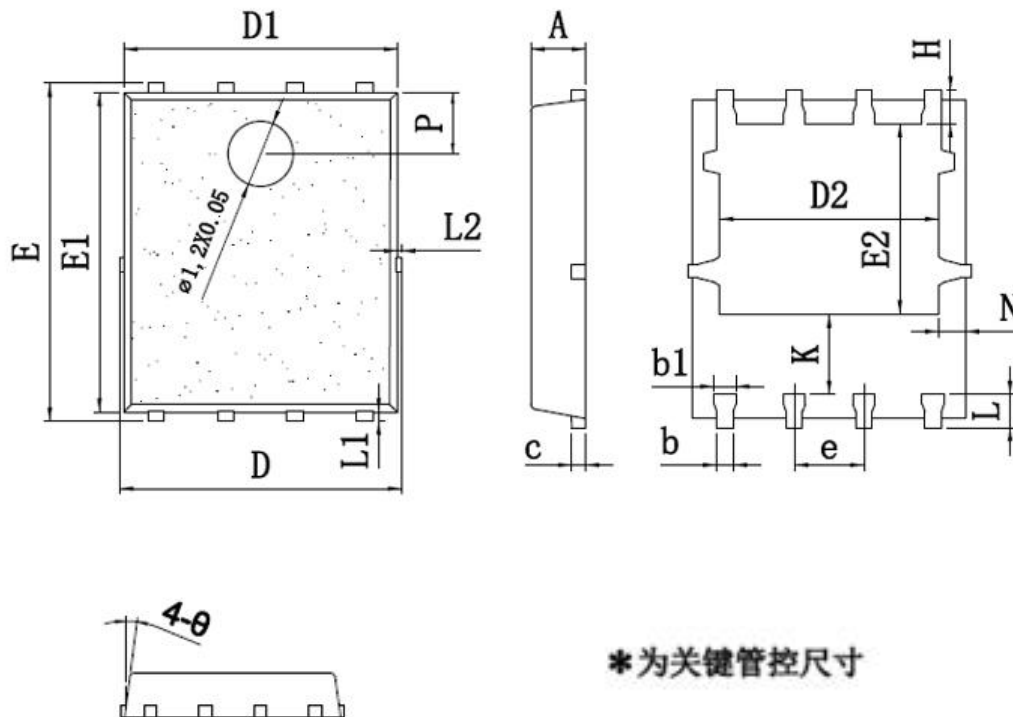


Figure.13 Transient Thermal Impedance (Junction - Case)



Package Outline:PDFN5*6-8L



*为关键管控尺寸

SYMBOL	mm		
	MIN	NOM	MAX
*A	0.95	1.00	1.05
*b	0.25	0.30	0.35
*b1	0.30	0.40	0.50
*c	0.20	0.25	0.30
D	5.15BSC		
*D1	4.90	5.00	5.10
D2	3.90	4.01	4.20
*e	1.17	1.27	1.37
E	6.15BSC		
*E1	5.75	5.85	5.95
E2	3.35	3.50	3.65
H	0.51	0.61	0.71
K	1.10	1.35	1.50
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
L2	-	-	0.12
N	0.40	0.50	0.60
P	0.95	1.10	1.25
θ	9°	11°	13°