

MOSFET

20A,650V,180mΩ,N-CHANNEL MOSFET

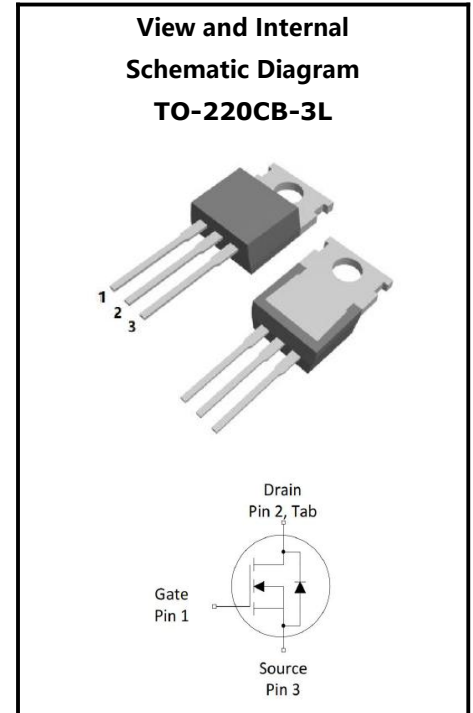
Features

- ◆ Extremely low on-resistance $R_{DS(on)}$
- ◆ Excellent Low Ciss

Applications

- ◆ PFC stages, hard switching PWM stages and resonant switching
- ◆ PWM stages for e.g. PC Silverbox, Adapter, Server, Telecom and UPS

Parameter	Values	Unit
BV_{dss}	650	V
I_D	20	A
$R_{Dson(max)}$	180	mΩ
$V_{GS(th)}(Typ)$	3.5	V



Ordering Code	Marking	Package	Packaging
X180N65JEFHP3	X180N65JEFHP3	TO-220CB-3L	Tube

Absolute Maximum Ratings($T_C=25^{\circ}C$,unless otherwise noted)

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Voltage	V_{DSS}	-	-	650	V	-
Gate-Source Voltage	V_{GS}	-20	-	20	V	-
Continuous Drain Current	I_D	-	-	20	A	$T_C=25^{\circ}C$
		-	-	13	A	$T_C=100^{\circ}C$
Pulsed Drain Current	$I_{D pluse}$	-	-	80	A	$T_C=25^{\circ}C, t_p=100us$
Single Pulse Avalanche Energy	E_{AS}	-	-	195	mJ	$L=10mH, V_{DS}=50V$
Maximum Power Dissipation	P_{tot}	-	-	125	W	$T_C=25^{\circ}C$
		-	-	1.5	W	$T_A=25^{\circ}C$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55	-	150	$^{\circ}C$	-
Soldering temperature, wave soldering only allowed at leads(1.6mm from case for 10s)	T_{sold}	-	-	260	$^{\circ}C$	

Thermal Characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Thermal resistance , junction to Case	$R_{th(j-c)}$	-	-	1	°C/W	-
Thermal resistance , junction to Ambient	$R_{th(j-a)}$	-	-	82		-

Electrical Characteristics ($T_j=25^{\circ}\text{C}$, unless otherwise noted)

Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Breakdown Voltage	BV_{DSS}	650	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=650V, V_{GS}=0V$
Gate-Body Leakage Current, Forward	I_{GSSF}	-	-	100	nA	$V_{GS}=20V, V_{DS}=0V$
Gate-Body Leakage Current, Reverse	I_{GSSR}	-	-	-100	nA	$V_{GS}=-20V, V_{DS}=0V$
Gate-Source Threshold Voltage	$V_{GS(th)}$	2.5	-	4.5	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Drain-Source On-State Resistance	$R_{DS(on)}$	-	150	180	m Ω	$V_{GS}=10V, I_D=10A$
Gate Resistance	R_g	-	10	-	Ω	$V_{GS}=0V, V_{DS}$ Open, $f=1\text{MHz}$
Forward Transconductance	g_{fs}	-	12	-	S	$V_{DS}=5V, I_D=10A$

Dynamic characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Input Capacitance	C_{iss}	-	1625	-	pF	$V_{DS}=400V, V_{GS}=0V, f=1.0\text{MHz}$
Output Capacitance	C_{oss}	-	52	-	pF	
Reverse Transfer Capacitance	C_{rss}	-	43	-	pF	
Turn-On Delay Time	$t_{d(on)}$	-	30	-	ns	$V_{DD}=325V, R_G=10\Omega, V_{GS}=10V, I_D=3.6A$
Turn-On Rise Time	t_r	-	40	-	ns	
Turn-Off Delay Time	$t_{d(off)}$	-	50	-	ns	
Turn-Off Fall Time	t_f	-	25	-	ns	

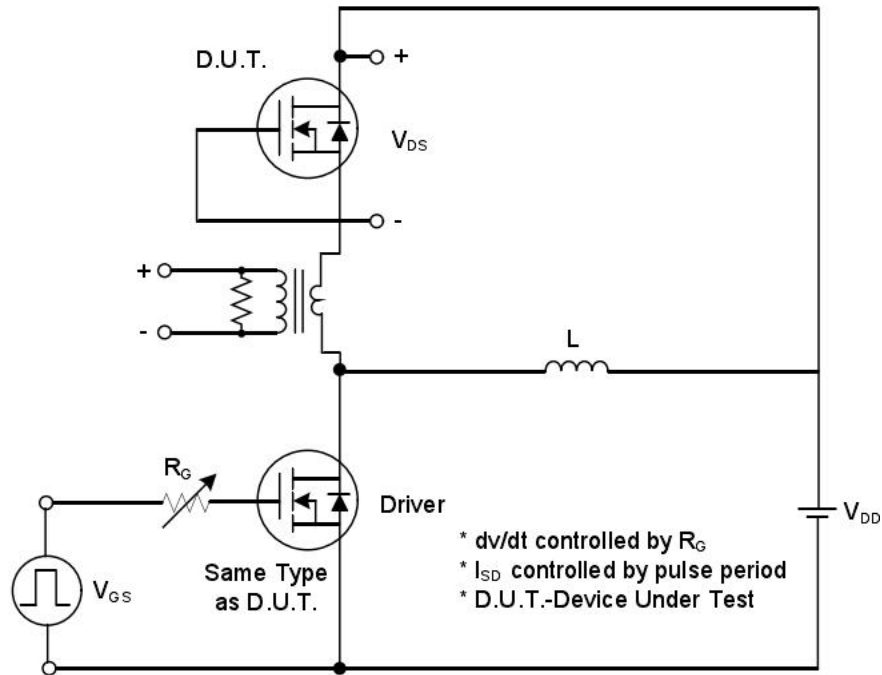
Gate charge characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Total Gate Charge	Q_g	-	42	-	nC	$V_{DS}=520V, I_D=20A, V_{GS}=10V$
Gate-Source Charge	Q_{gs}	-	11	-	nC	
Gate-Drain Charge	Q_{gd}	-	18	-	nC	

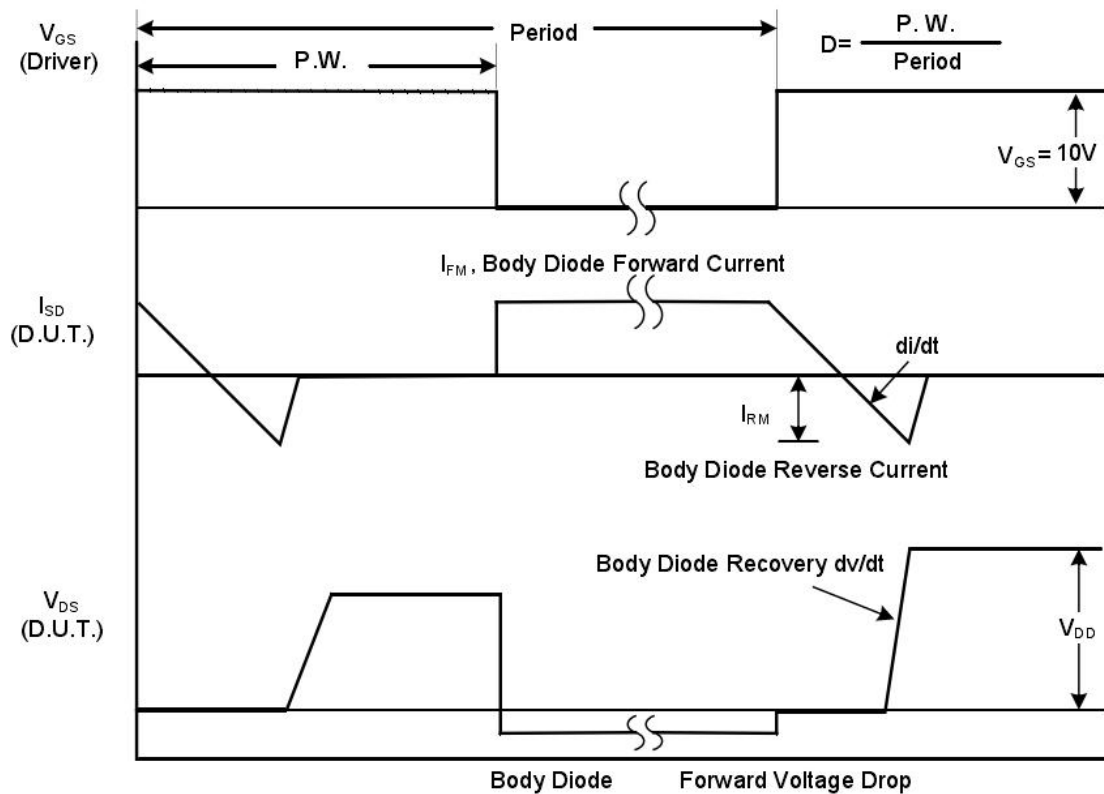
Reverse diode

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Continuous Diode Forward Current	I_S	-	-	20	A	-
Pulsed Diode Forward Current	$I_{S\ pluse}$	-	-	81	A	-
Diode Forward Voltage	V_{SD}	-	-	1.5	V	$I_S=10A, V_{GS}=0V$
Reverse Recovery Time	t_{rr}	-	125	-	ns	$V_R=400V, I_F=7A, di/dt=100A/\mu s$
Reverse Recovery Charge	Q_{rr}	-	562	-	nC	

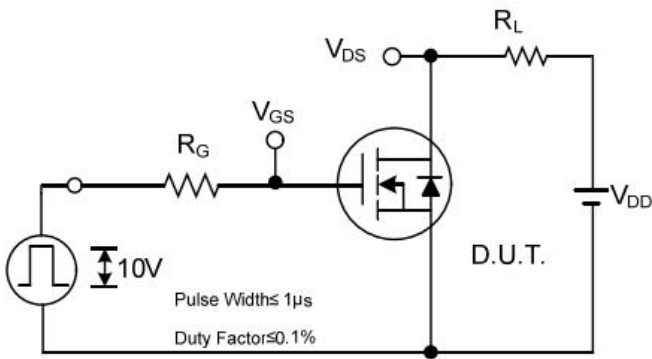
RATING AND CHARACTERISTIC CURVES



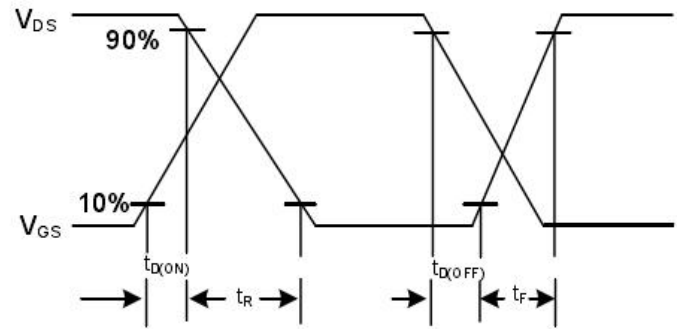
Peak Diode Recovery dv/dt Test Circuit



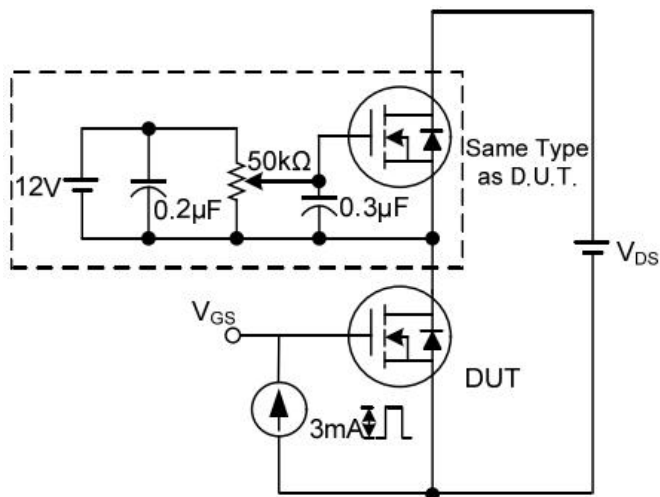
Peak Diode Recovery dv/dt Waveforms



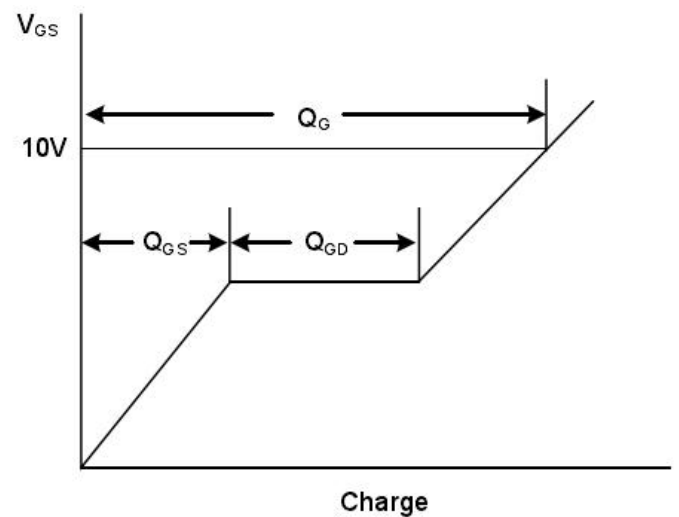
Switching Test Circuit



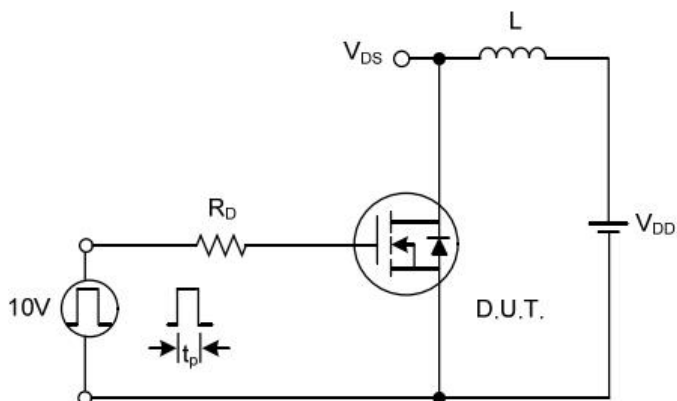
Switching Waveforms



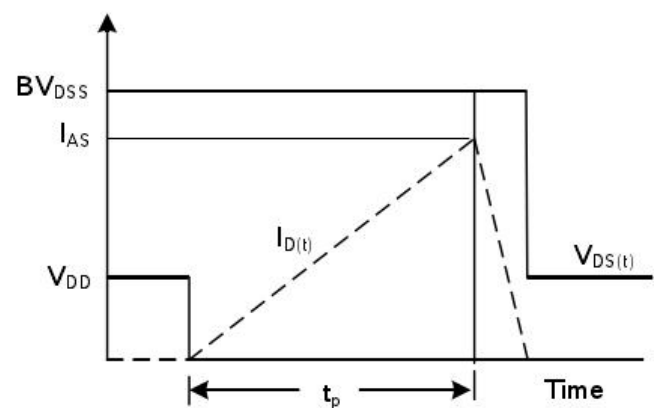
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES

Figure.1 Output Characteristics

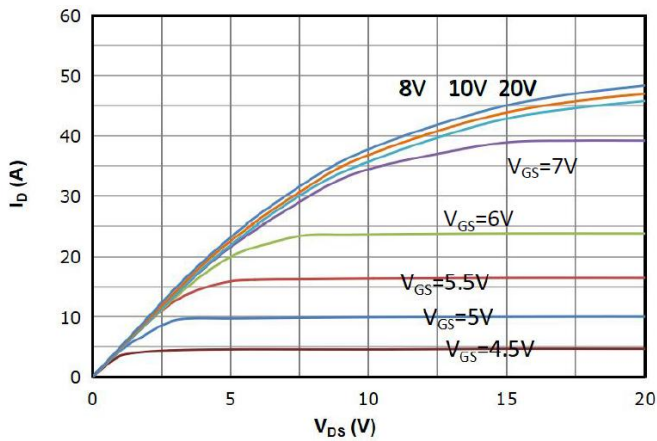


Figure.2 Transfer Characteristics

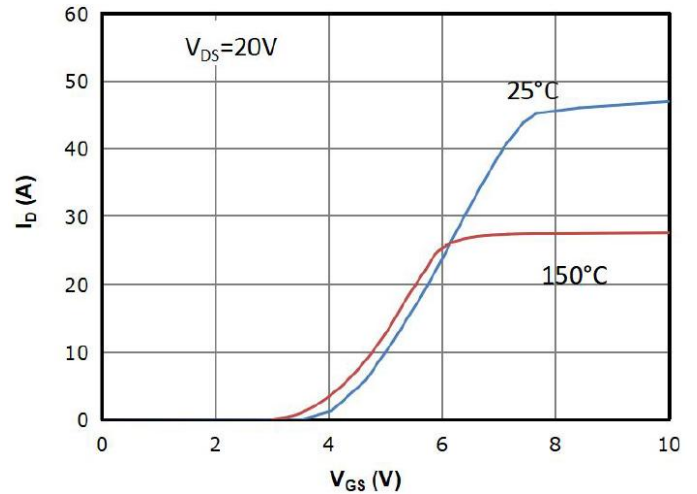


Figure.3 Rds(on) vs Drain Current and Gate Voltage

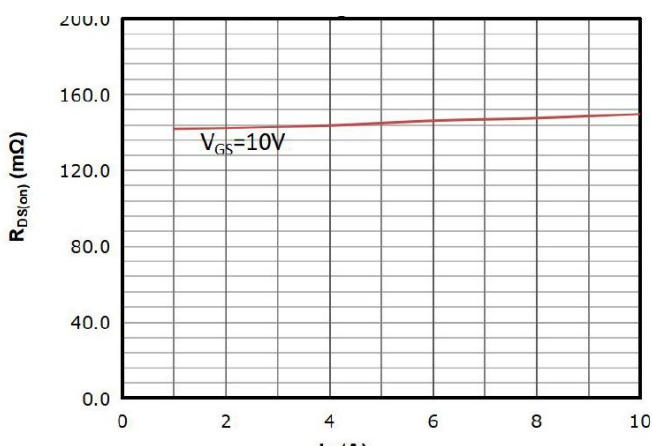


Figure.4 Rds(on) vs Gate Voltage

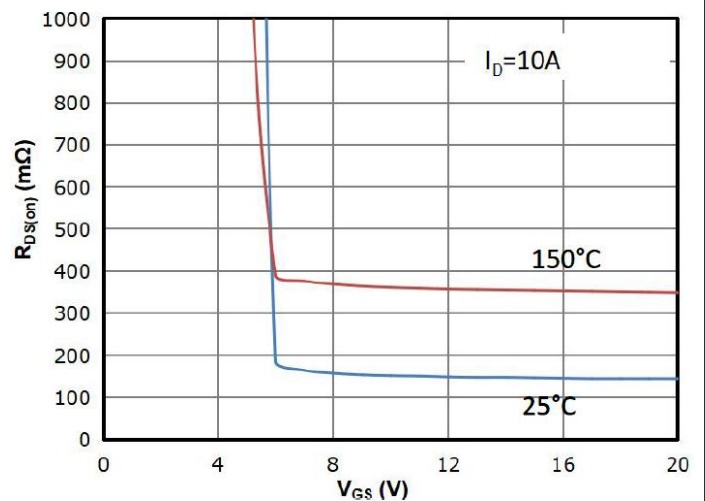


Figure.5 Rds(on) vs. Temperature

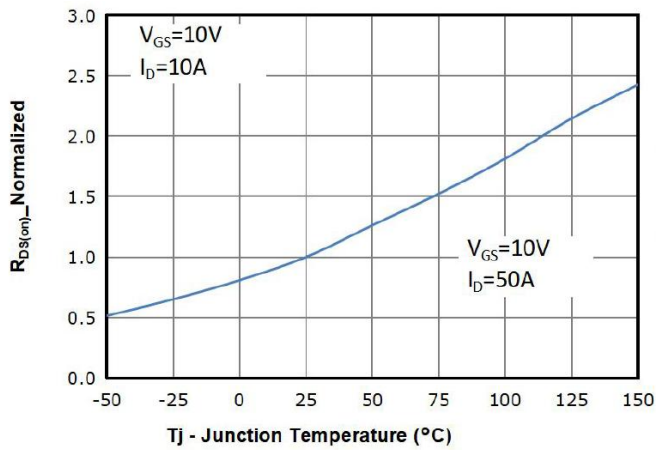


Figure.6 Vgs(th) vs. Temperature

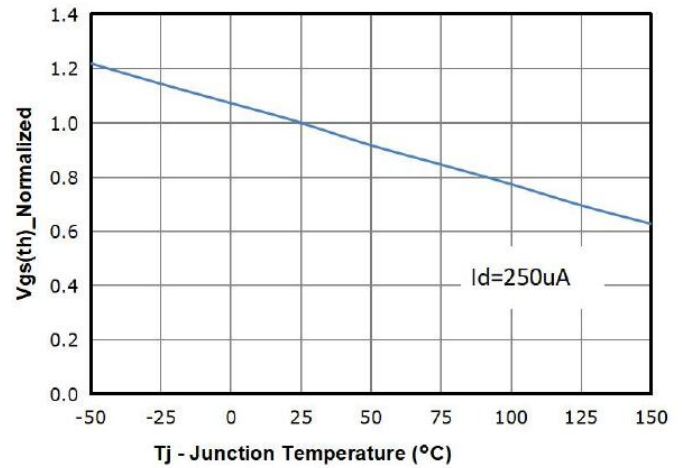


Figure.7 BVdss vs. Temperature

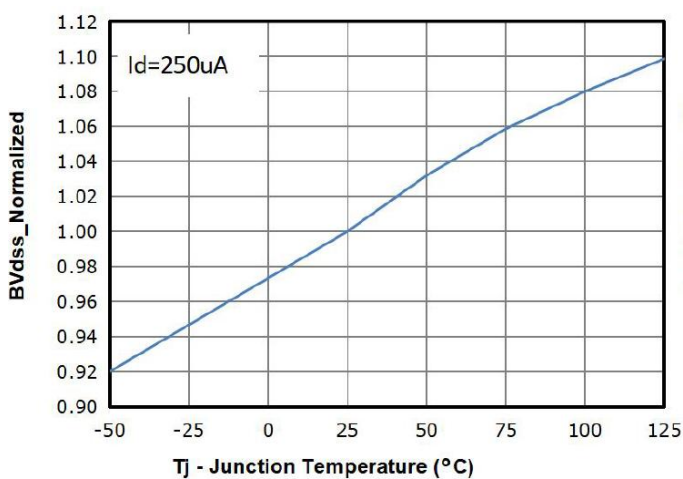


Figure.8 Capacitance Characteristics

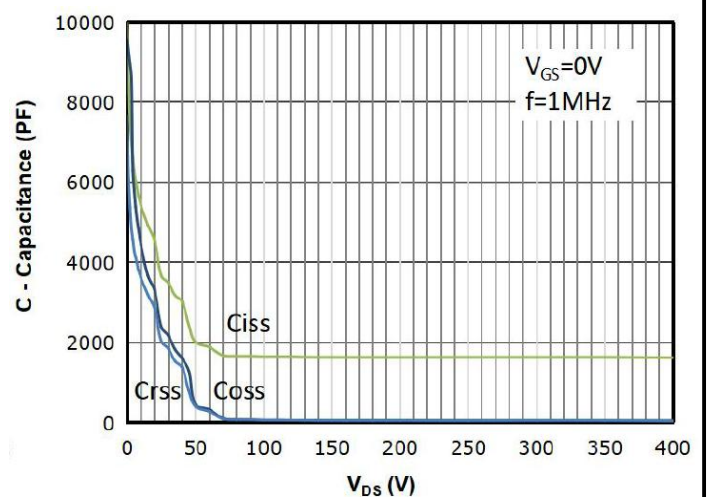


Figure.9 Gate Charge Characteristics

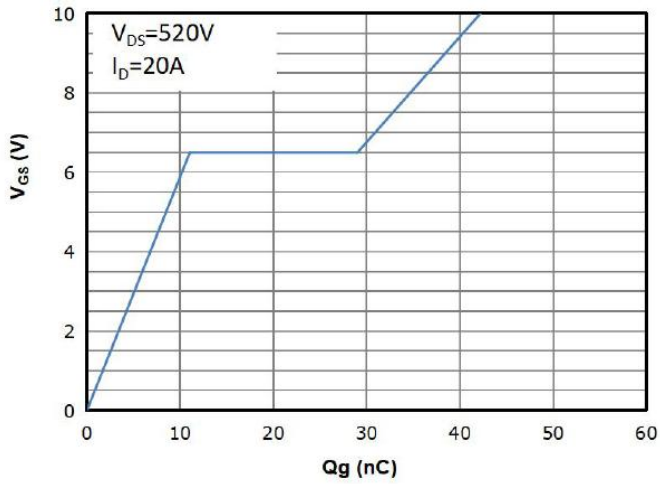


Figure.10 Body-diode Forward Characteristics

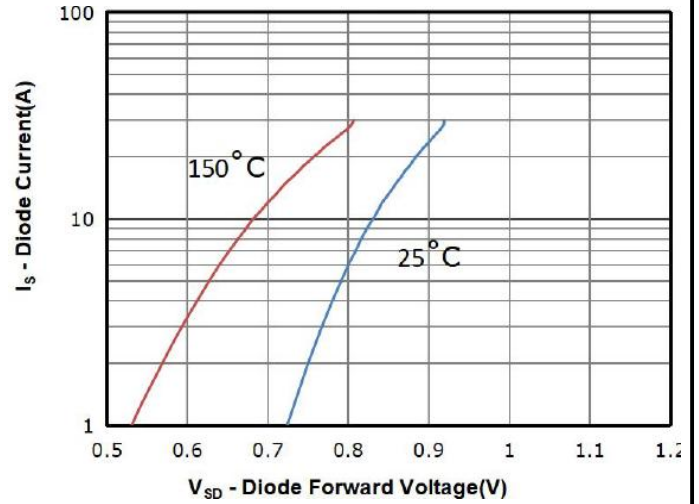


Figure.11 Power Dissipation

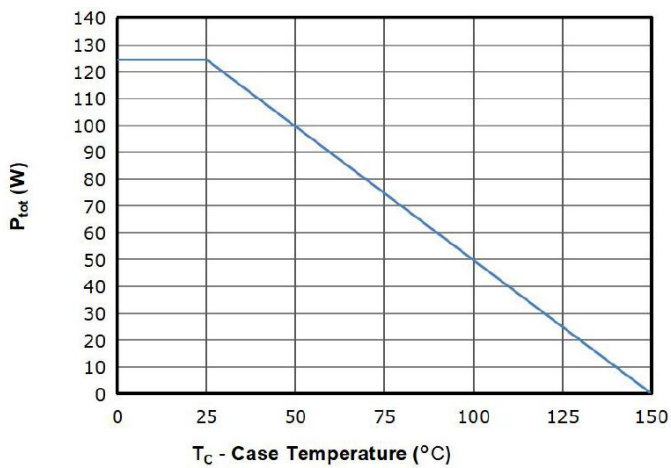


Figure.12 Drain Current Derating

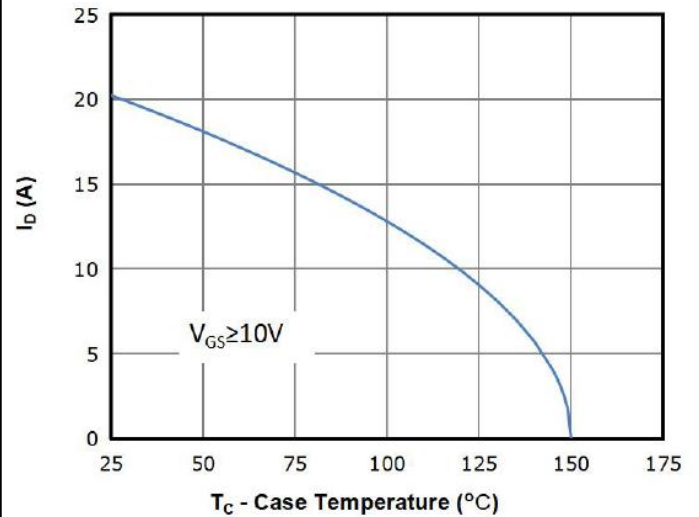


Figure.13 Safe Operating Area

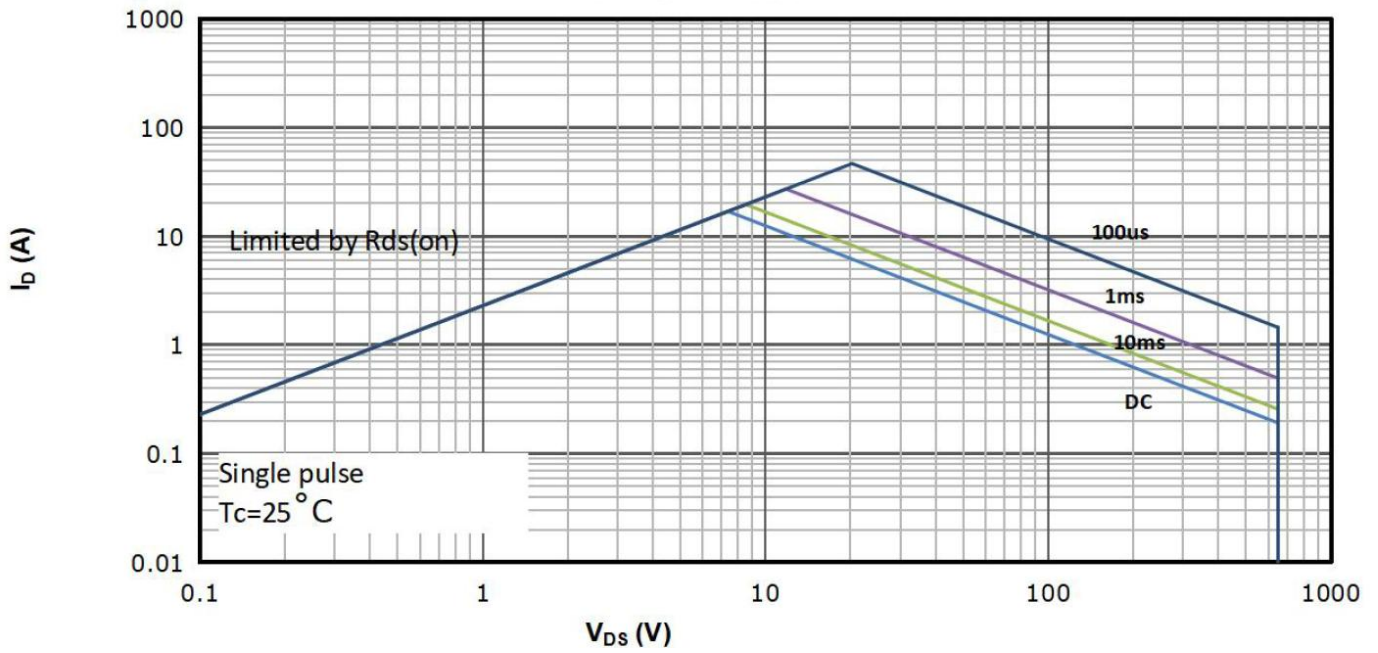
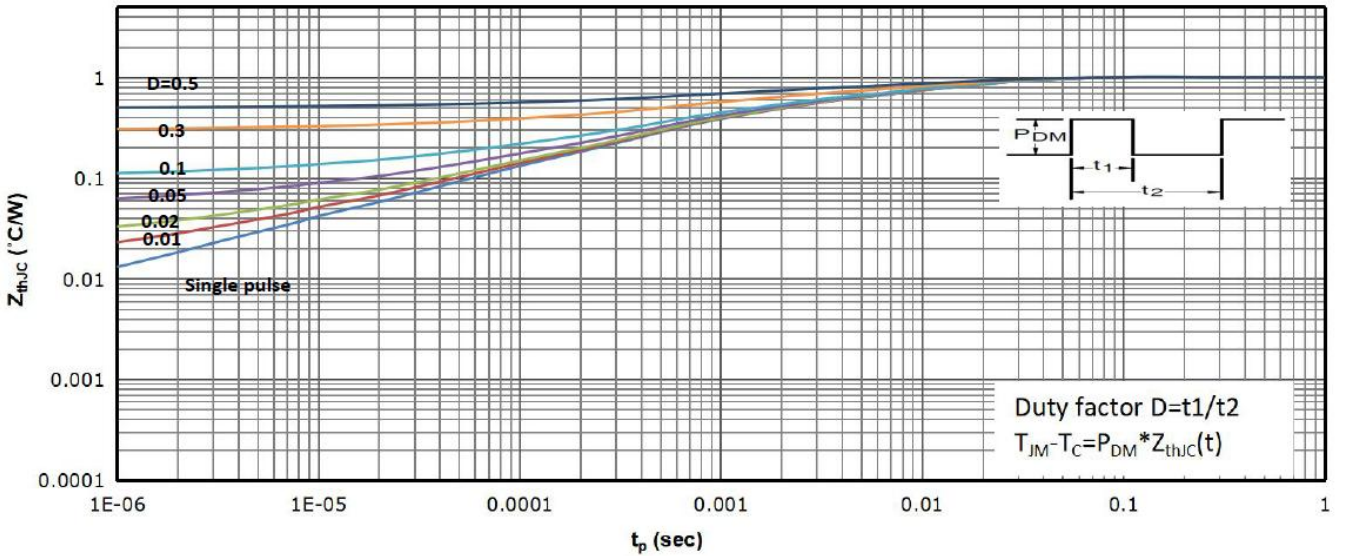
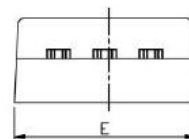
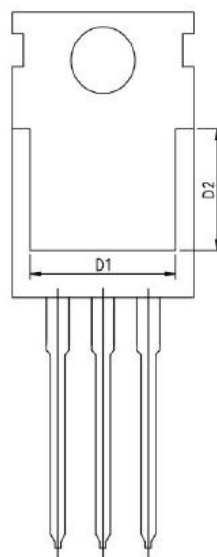
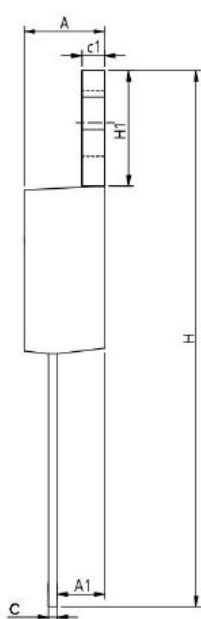
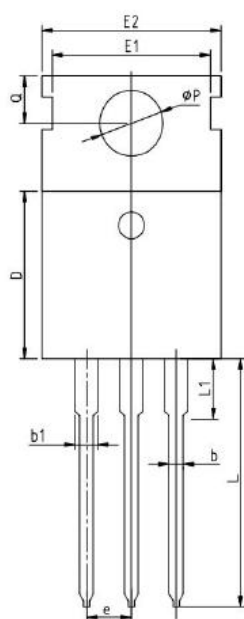
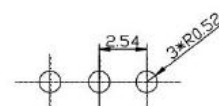


Figure.14 Maximum Effective Thermal Impedance, Junction to Case





RECOMMENDED LAND PATTERN



UNIT: mm

SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.40	4.80	0.173	0.189
A1	2.25	2.55	0.089	0.100
b	0.72	0.92	0.028	0.036
b1	1.12	1.42	0.044	0.056
c	0.40	0.60	0.016	0.024
c1	1.20	1.40	0.047	0.055
D	8.80	9.40	0.346	0.370
D1	7.75	8.15	0.305	0.321
D2	6.55	6.95	0.258	0.274
e	2.54		0.100	
E	9.65	10.35	0.380	0.407
E1	8.70		0.343	
E2	9.70	10.30	0.382	0.406
H	28.70	29.70	1.130	1.169
H1	6.25	6.85	0.246	0.270
L	13.20	13.80	0.520	0.543
L1	2.80	3.40	0.110	0.134
Q	2.60	3.00	0.102	0.118
φP	3.45	3.75	0.136	0.148