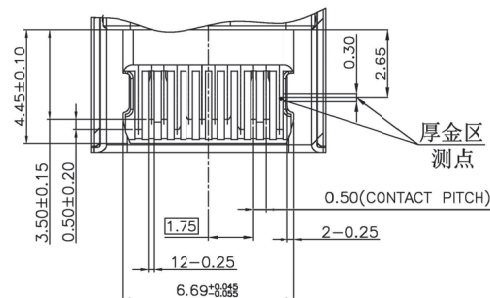
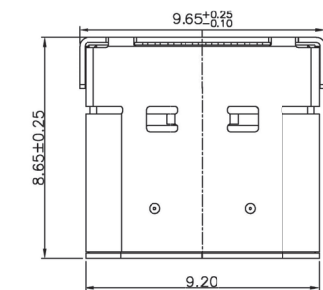
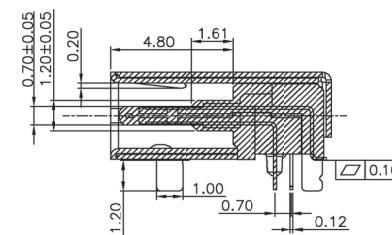
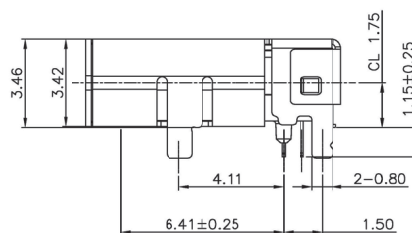
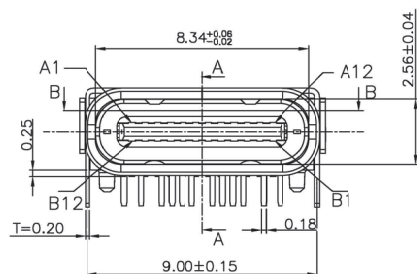


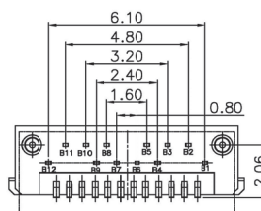
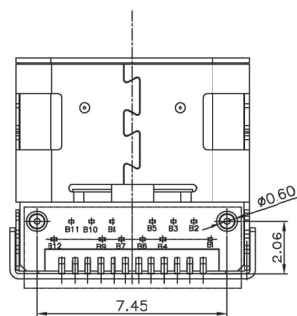
RoHS2.0+REACH



SECTION B-B



Section A-A
Scale 1:1



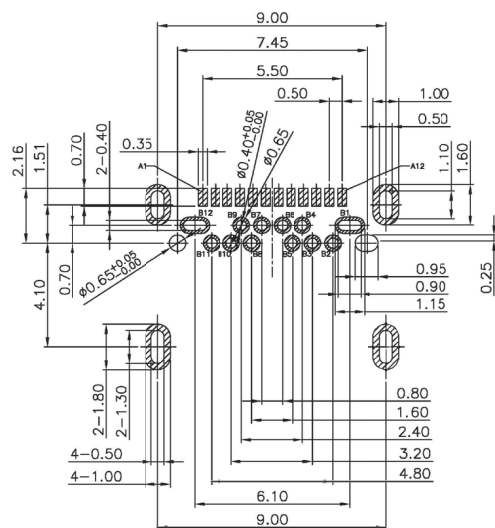
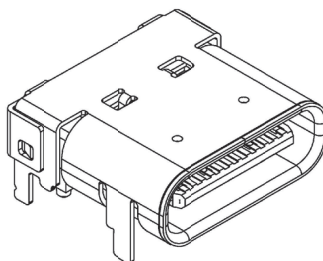
Detail A
Scale 1:1

NOTES:

- MATERIAL:
MOLDING: LCP UL94 V-0
CONTACT: COPPER ALLOY.
PLATED GOLD ON CONTACT AREA,
G/F GOLD ON SOLDER AREA,
50u" NICKEL PLATING OVER ALL.
OUTER SHELL: SUS304-H, T=0.20±0.03mm
50u" NICKEL SOLDER AREA TAIL
INNER SHELL: SUS304, T=0.20±0.03mm
LATCH: SUS301, T=0.15±0.03mm
- MECHANICAL:
INSERTION: 5~20N.
EXTRACTION: 8~20N.
DURABILITY: 10000 CYCLES
- ELECTRICAL:
CURRENT: 5A MAX FOR VBUS;
1.25A FOR GND, 0.25A FOR OTHER OTHER.
VOLTAGE: 20VDC MAX
WITHSTANDING VOLTAGE: 100V AC.
CONTACT RESISTANCE: 40mΩ MAX.
INSULATION RESISTANCE: 100MΩ MIN.
- ENVIRONMENTAL
TEMPERATURE RANGE -40°C ~ +85°C

5.0				DRAWING:	GENERAL TOLERANCE			
4.0				Frank	.X:=±0.30	.X°:=±2°		
3.0				CHECKED:	.XX:=±0.25	.XX°:=±1°	DESCRIPTION: 板上双壳8.65L脚长1.2 CL1.75带弹	
2.0				yangjie	.XXX:=±0.15	.XXX°:=±0.5°		
1.0				APPROVAL:	UNIT: MM	PROJECTION:	PART NO: USB30-AF-012	REV: 1.0
NO.	DATE	DESCRIPTION	DRAWING		SCALE: 1:1		OLD CODE: USBC1-AF-012	SIZE: A4
					SHEET: 1/1		DRAWING NO:	

RoHS2.0+REACH



RECOMMEND P.C.B LAYOUT(COMPONENT SIDE)
TOLERANCE FOR PCB LAYOUT IS ± 0.05

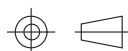
USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	E12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	E11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	E10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	VBUS	Bus Power	E9	VBUS	Bus Power
A5	CC1	Configuration Channel	E8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	E7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	E6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use(SBU)	E5	CC2	Configuraation Channel
A9	VBUS	Bus Power	E4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	E3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	E2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	E1	GND	Ground return

5.0			
4.0			
3.0			
2.0			
1.0			
NO.	DATE	DESCRIPTION	DRAWING

DRAWING:
Frank
CHECKED:
yangjie
APPROVAL:

GENERAL TOLERANCE
.X:= ± 0.30 .X*:= $\pm 2^\circ$
.XX:= ± 0.25 .XX*:= $\pm 1^\circ$
.XXX:= ± 0.15 .XXX*:= $\pm 0.5^\circ$
UNIT: MM PROJECTION:
SCALE: 1:1
SHEET: 1/1



CHIN-BAN
Electronic Technology

DESCRIPTION: 板上双壳8.65L脚长1.2 CL1.75带弹

PART NO: **USB30-AF-012**
OLD CODE: USBC1-AF-012
DRAWING NO:

REV: 1.0
SIZE: A4