

Description

The 16N65 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

D S G

TO-263

General Features

V_{DS} = 650V I_D =16A

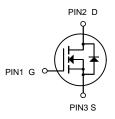
 $R_{DS(ON)} < 0.55 \Omega$ @ V_{GS} =10V

Application

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)	
16N65	TO-263	16N65 XXXX YYYY	800	

Absolute Maximum Ratings

Symbol	Parameter	Limit	Units
V _{DS}	Drain-Source Voltage	650	V
V _{GS}	Gate-Source Voltage	±30	V
I _D	Drain Current-Continuous	16	A
I _{DM}	Drain Current-Pulsed ^a	64	A
В	Maximum Power Dissipation @ T _C = 25°C	180	W
P_{D}	- Derate above 25°C	1.1	W/°C
E _{AS}	Single Pulsed Avalanche Energy d	1000	mJ
I _{AS}	Single Pulsed Avalanche Current d	64	A
T _J ,T _{stg}	Operating and Store Temperature Range	-55 to 175	°C
Rejc	Thermal Resistance, Junction-to-Case	0.69	°C/W
Reja	Thermal Resistance, Junction-to-Ambient	62.5	°C/W



Electrical Characteristics $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	650			V	V _{GS} =0V, I _D =250uA
				1.0	uA	V _{DS} =650V, V _{GS} =0V
I _{DSS}	Drain-to-Source Leakage Current			100		V _{DS} =520V, V _{GS} =0V, T _J =125°C
				+100	nA	V _{GS} =+30V, V _{DS} =0V
I _{GSS}	Gate-to-Source Leakage Current			-100		V _{GS} =-30V, V _{DS} =0V
R _{DS(ON)}	Static Drain-to-Source On-Resistance ^[4]		0.45	0.55	Ω	V _{GS} =10V, I _D =8A
$V_{GS(TH)}$	Gate Threshold Voltage	2.0		4.0	V	$V_{DS}=V_{GS}$, $I_{D}=250uA$
gfs	Forward Transconductance ^[4]		15		S	VDS=15V,ID=8A
C _{iss}	Input Capacitance		2442			V_{GS} =0V, V_{DS} =25V, f =1.0MH $_{Z}$
C _{rss}	Reverse Transfer Capacitance		18.5		рF	
C _{oss}	Output Capacitance		218			
Qg	Total Gate Charge		54			V_{DD} =325V, I_{D} =16A, V_{GS} =0 to 10V
Q _{gs}	Gate-to-Source Charge		12		nC	
Q_{gd}	Gate-to-Drain (Miller) Charge		21			
td(ON)	Turn-on Delay Time		15			V_{DD} =325V, I_{D} =16A, V_{GS} = 10V RG=6.1 Ω
trise	Rise Time		52			
td(OFF)	Turn-Off Delay Time		59		nS	
tfall	Fall Time		72			
I _{SD}	Continuous Source Current ^[4]			16	Λ	Integral PN-diode in MOSFET
I _{SM}	Pulsed Source Current ^[4]			64	Α	
V _{SD}	Diode Forward Voltage			1.5	V	I _S =16A, V _{GS} =0V
trr	Reverse recovery time		380		V	V _{GS} =0V ,I _F =16A,
Qrr	Reverse recovery charge		2.6		uC	dir/dt=100A/µs

^[1] T_J=+25 $^{\circ}$ C to +150 $^{\circ}$ C

^[2] Repetitive rating; pulse width limited by maximum junction temperature.
[3] ISD= 16A di/dt < 100 A/µs, VDD < BVDSS, TJ=+150 °C.
[4] Pulse width≤380µs; duty cycle≤2%.



Typical Characteristics

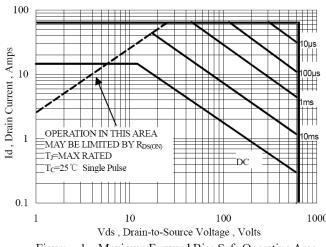
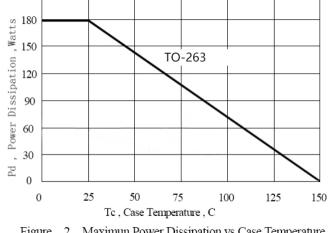


Figure 1 Maximun Forward Bias Safe Operating Area



2 Maximun Power Dissipation vs Case Temperature Figure

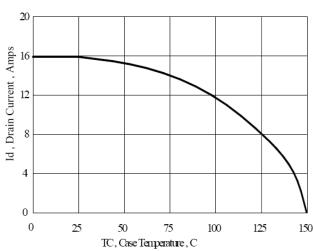


Figure 3 Maximum Continuous Drain Current vs Case Temperature

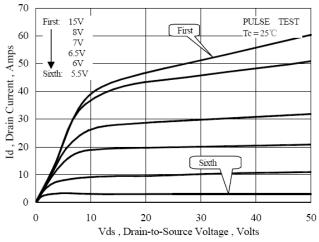
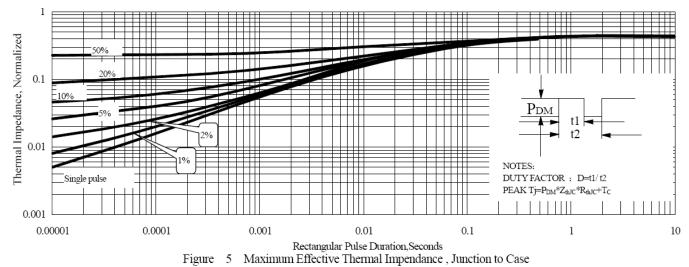


Figure 4 Typical Output Characteristics



210

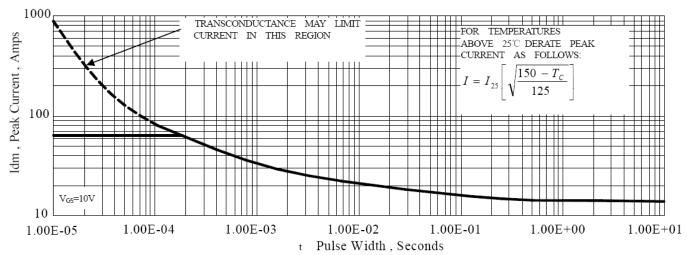
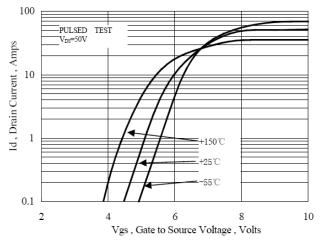


Figure 6 Maximun Peak Current Capability



Typical Transfer Characteristics

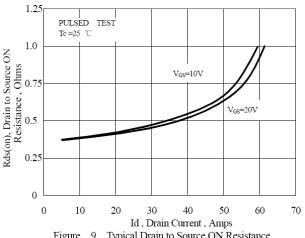
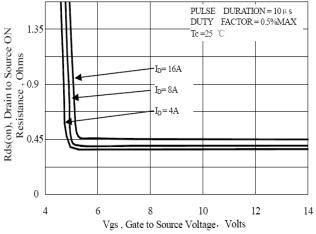
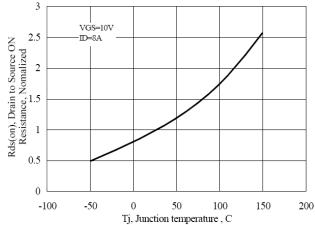


Figure 9 Typical Drain to Source ON Resistance vs Drain Current



8 Typical Drain to Source ON Resistance vs Gate Voltage



Typical Drian to Source on Resistance Figure 10 vs Junction Temperature

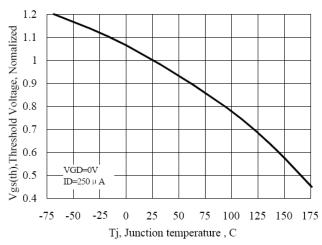


Figure 11 Typical Theshold Voltage vs Junction Temperature

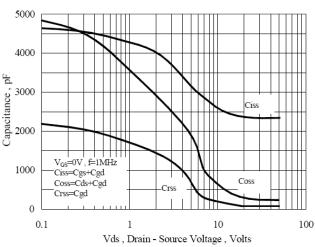


Figure 13 Typical Capacitance vs Drain to Source Voltage

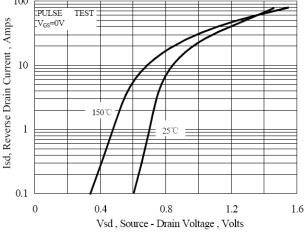


Figure 15 Typical Body Diode Transfer Characteristics

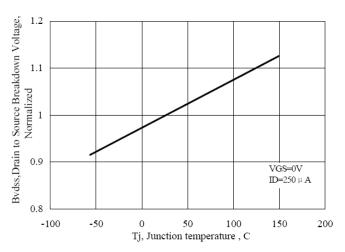


Figure 12 Typical Breakdown Voltage vs Junction Temperature

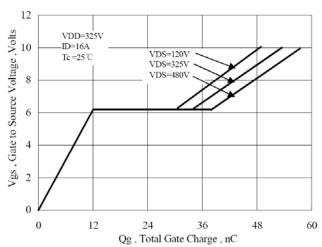


Figure 14 Typical Gate Charge vs Gate to Source Voltage

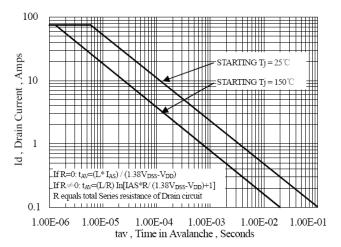


Figure 16 Unclamped Inductive Switching Capability



Test Circuits and Waveforms

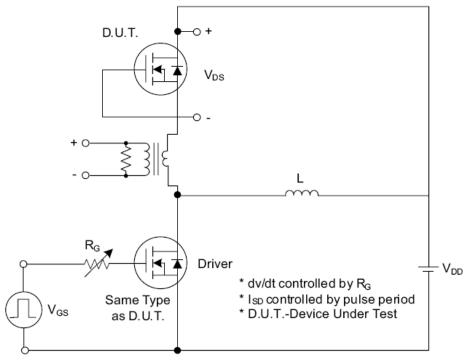


Fig. 1.1 Peak Diode Recovery dv/dt Test Circuit

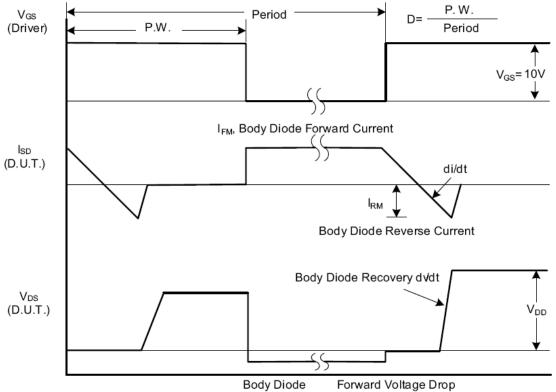


Fig. 1.2 Peak Diode Recovery dv/dt Waveforms

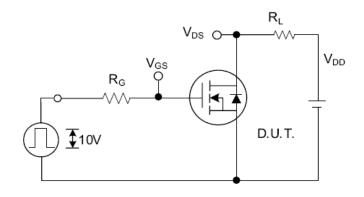


Fig. 2.1 Switching Test Circuit

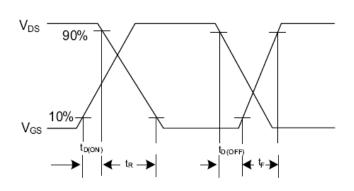


Fig. 2.2 Switching Waveforms

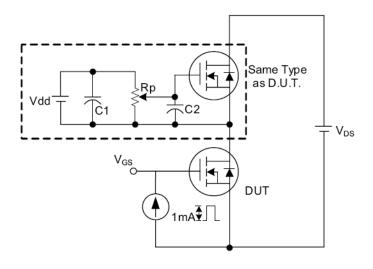


Fig. 3 . 1 Gate Charge Test Circuit

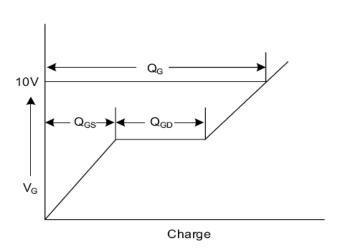


Fig. 3.2 Gate Charge Waveform

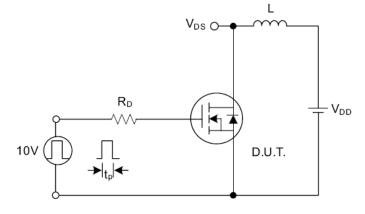


Fig. 4.1 Unclamped Inductive Switching Test Circuit

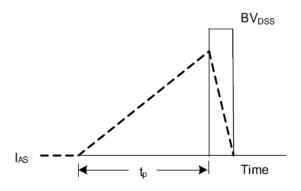
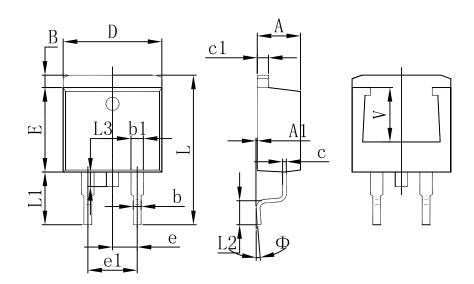


Fig. 4.2 Unclamped Inductive Switching Waveforms



TO-263 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	4.470	4.670	0.176	0.184	
A1	0.000	0.150	0.000	0.006	
В	1.120	1.420	0.044	0.056	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.310	0.530	0.012	0.021	
c1	1.170	1.370	0.046	0.054	
D	10.010	10.310	0.394	0.406	
E	8.500	8.900	0.335	0.350	
е	2.540 TYP.		0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
L	14.940	15.500	0.588	0.610	
L1	4.950	5.450	0.195	0.215	
L2	2.340	2.740	0.092	0.108	
L3	1.300	1.700	0.051	0.067	
Ф	0°	8°	0°	8°	
V	5.600 REF.		0.220REF.		



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