

Description

Available either in through-hole or surface-mount packages, the BTA08-600C is suitable for general purpose AC switching. They can be used as an ON /OFF function in application such as static relays, heating regulation, Induction motor starting circuits or for phase control Operation in light dimmers, motor speed controllers.



Absolute Maximum Ratings (T_a=25°C unless otherwise noted)

Symbol	Parameter	Test condition	Value	Unit
I _{T(RMS)}	RMS on-state current	Tc=110°C	8	Α
l	Non repetitive surge	F=60Hz, t=16.7ms	84	Α
I _{TSM}	peak on-state current	F=50Hz, t=20ms	80	
l ² t	I ² t value	tp=10ms	36	A ² s
dI/dt	Critical rate of rise of on-state current	I _G =2*I _{GT} , tr≤100ns, F=120Hz,T _j =125°C	50	A/µs
I _{GM}	Peak gate current tp=20µs, T _j =125°C		4	Α
P _{G(AV)}	Average gate power	T _j =125°C	1	W
T _{STG}	Storage temperature		-40~+150	
Tj	Operating junction temperature		-40~+125	°C



Electrical Characteristics ($T_j = 25$ °C unless otherwise specified) Snubberless TM and Logic Level (3 quadrants)

Symbol	Parameter	Test condition			Value	Unit
I _{GT} (1)	Gate trigger current	V _D =12V,	I - II -III	Max	25	mA
V _{GT}	Gate trigger voltage	R _L =30Ω,	I - II -III	Max	1.3	V
$V_{\sf GD}$	Non-triggering gate voltage	V _D =V _{DRW} , T _j =125°C R _L =3.3K		Min	0.2	V
I _H (2)	Holding current	I _T =100mA		Max	50	
ال	Latching current	Ι _G =1.2Ι _{GΤ,}	I -III	Max	70	mA
			II	Max	80	
D _V /dt(2)	Critical rate of rise of off-state	V_D =67% V_{DRM} , Gate Open T_j =125°C		Min	1000	V/µs
		(dl/dt)c=0.1V/us, T _j =125°C		Min	-	
(DI/dt)c(2)	Critical rate of rise of	(dl/dt)c=10V/us, T _j =125°C			-	V/µs
	off-state	Without snubber, T _j =	=125°C		7	

Standard (4 quadrants)

Symbol	Parameter	Test condition			Value	Unit
I _{GT} (1)	Gate trigger current	$V_D=12V$, $R_L=30\Omega$,	I - II -III IV	Max	50 100	mA
V_{GT}	Gate trigger voltage		ALL	Max	1.3	V
V_{GD}	Non-triggering gate voltage	$V_D = V_{DRW}, T_j = 125$ °C RL=3.3K ALL		Min	0.2	V
I _H (2)	Holding current	I _T =500mA		Max	50	
IL	Latching current	1 4 01	I -III- IV	Max	50	mA
IL.		I _G =1.2I _{GT} ,	II	Max	100	
D _V /dt(2)	Critical rate of rise of off-state	V _D =67%V _{DRM} , Gate Open T _j =125°C		Min	400	V/µs
(DI/dt)c(2)	Critical rate of rise of off-state	(D _V /dt)c=3.5A/ms,T _j =125°C		Min	10	V/µs



Static Characteristics

Symbol	Test Con	Value	Unit		
V _{TM} (2)	$I_{TM} = 11 \text{ A}, t_p = 380 \mu s$	T _j = 25°C	MAX.	1.55	V
V _{to} (2)	Threshold voltage	T _j = 125°C	MAX.	0.85	V
R _d (2)	Dynamic resistance	T _j = 125°C	MAX.	50	mΩ
I _{DRM}	$V_{DRM} = V_{RRM}$	T _j = 25°C	NAAN	5	μA
I _{RRM}	VDRM — VRRM	T _j = 125°C	MAX.	1	mA
V _{DRM} /V _{RRM}	Voltage	T _j = 25°C		800	mA

Note 1: minimum IGT is guaranted at 5% of IGT max

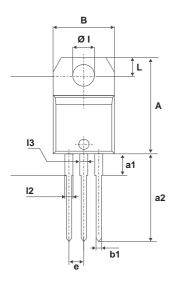
Note 2: for both polarities of A2 referenced to A1

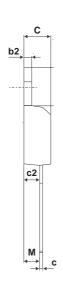
Thermal Resistances

Symbol	Parameter	Value	Unit
Rth (j-c)	Junction to case (AC)	2.5	°C/W
Rth (j-a)	Junction to ambient	60	°C/W



Package Information TO-220A





	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	15.20		15.90	0.598		0.625
a1		3.75			0.147	
a2	13.00		14.00	0.511		0.551
В	10.00		10.40	0.393		0.409
b1	0.61		0.88	0.024		0.034
b2	1.23		1.32	0.048		0.051
С	4.40		4.60	0.173		0.181
c1	0.49		0.70	0.019		0.027
c2	2.40		2.72	0.094		0.107
е	2.40		2.70	0.094		0.106
F	6.20		6.60	0.244		0.259
ØI	3.75		3.85	0.147		0.151
14	15.80	16.40	16.80	0.622	0.646	0.661
L	2.65		2.95	0.104		0.116
12	1.14		1.70	0.044		0.066
13	1.14		1.70	0.044		0.066
М		2.60			0.102	



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