MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

SIS410DN-T1-GE3-MS

Product specification





Description

The SI7106DN-T1-E3-MS uses advanced trench technology to provide excellent R_{DS(ON)}, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

Features

- V_{DS} = 20V I_D = 60A
- $R_{DS(ON)} < 5m\Omega$ @ $V_{GS} = 4.5V$

Application

- Battery protection
- Load switch
- Uninterruptible power supply

Reference News

DFN3X3-8L	N-Channel MOSFET	Marking
GS S G D D D D D D D D D D D D D D D D D	G S	MSKSEMI S410D N20

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	20 V	
Vgs	Gate-Source Voltage	±12	V
lo@Tc=25℃	Continuous Drain Current, V _{GS} @ 10V ¹	60	Α
l b@Tc=100℃	Continuous Drain Current, V _{GS} @ 10V ¹	33	Α
Ірм	Pulsed Drain Current ²	220	Α
EAS	Single Pulse Avalanche Energy ³	46	mJ
las	Avalanche Current	25	Α
P 	Total Power Dissipation ⁴	15	W
Тѕтс	Storage Temperature Range	-55 to 150	${\mathbb C}$
TJ	Operating Junction Temperature Range	-55 to 150	${\mathbb C}$
Rеja	Thermal Resistance Junction-ambient ¹	62	°C/W
Rejc	Thermal Resistance Junction-Case ¹	4.5	°C/W



Electrical Characteristics (TJ=25 ℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V,	-	-	1.0	μA
Igss	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS}=V_{GS},\ I_{D}=250\mu A$	0.4	0.7	1.1	V
_	Static Drain-Source on-Resistance	V _{GS} =4.5V, I _D =30A	-	4.0	5	0
R _{DS(on)}	note3	V _{GS} =2.5V, I _D =20A	-	6.0	9	mΩ
Ciss	Input Capacitance	101111	-	2500	-	pF
Coss	Output Capacitance	V_{DS} =10V, V_{GS} =0V, $f = 1.0MHz$	-	407	-	pF
Crss	Reverse Transfer Capacitance	1 - 1.0WI1Z	-	386	-	pF
Qg	Total Gate Charge	\/ 40\/ L 00A	-	32	-	nC
Qgs	Gate-Source Charge	V_{DS} =10V, I_{D} =30A, V_{GS} =4.5V	-	3	-	nC
Q_{gd}	Gate-Drain("Miller") Charge	V GS-4.5 V	-	11	-	nC
t _{d(on)}	Turn-on Delay Time	\/ -40\/	-	17	-	ns
t r	Turn-on Rise Time	V _{DS} =10V,	-	49	-	ns
t _{d(off)}	Turn-off Delay Time	l _D =30A, R _{GEN} =3Ω, V _{GS} =4.5V	-	74	-	ns
t f	Turn-off Fall Time	VGS -4.3 V	-	26	-	ns
ls	Maximum Continuous Drain to Source Diode Forward Current		-	-	75	А
Іѕм	Maximum Pulsed Drain to Source Diode Forward Current		-	-	300	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S =30A	-	-	1.2	V

Notes:

- 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
- 2. EAS condition: TJ=25 $^{\circ}\text{C}$, Vdd=10V, Vg=4.5V, L=0.5mH, Rg=25 Ω , IAs=15A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



Typical Performance Characteristics

Figure1: Output Characteristics

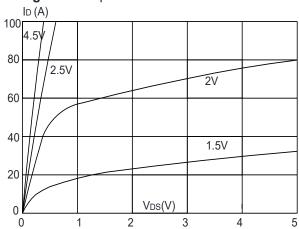


Figure 2: Typical Transfer Characteristics

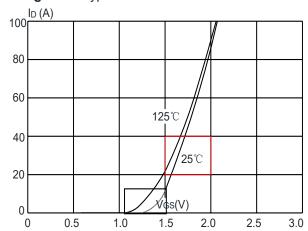


Figure 3:On-resistance vs. Drain Current RDS(ON) (m Ω)

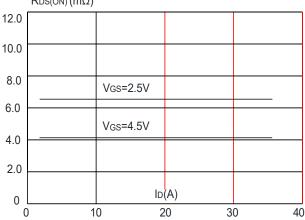


Figure 4: Body Diode Characteristics

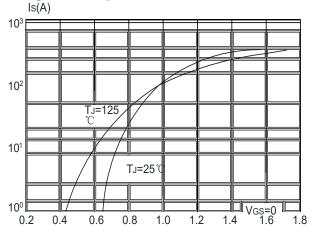


Figure 5: Gate Charge Characteristics

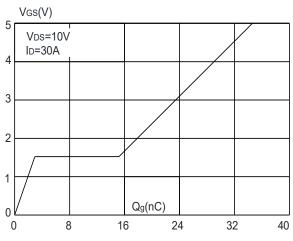


Figure 6: Capacitance Characteristics

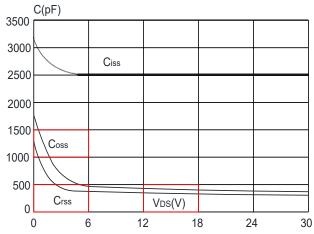




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

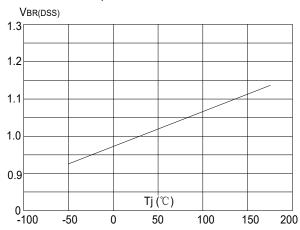


Figure 9: Maximum Safe Operating Area

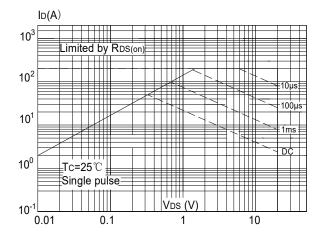


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

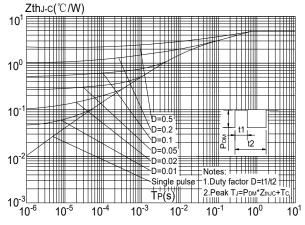


Figure 8: Normalized on Resistance vs. Junction Temperature

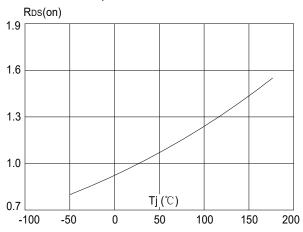
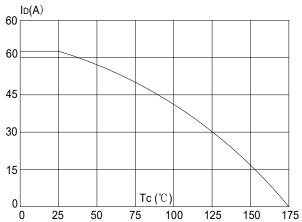
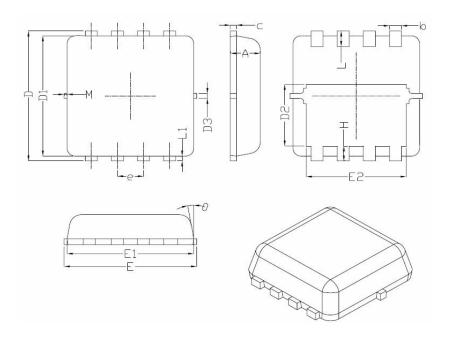


Figure 10: Maximum Continuous Drain Current vs. Case Temperature





DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	_	
Е	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
e		0.65BSC		
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
М	*	*	0.15	
θ		10 °	12 °	

REEL SPECIFICATION

P/N	PKG	QTY
SIS410DN-T1-GE3-MS	DFN3X3-8L	5000



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