

General Description

The WSF50N02 is the highest performance trench N-Channel MOSFET with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSF50N02 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Features

- 100% UIS Tested.
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

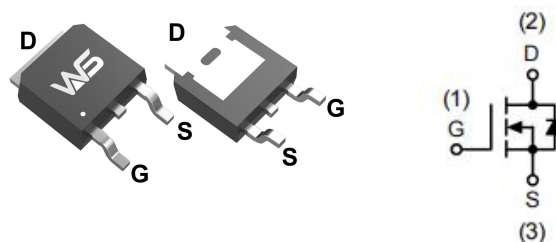
Product Summary

BV_{DSS}	$R_{DS(ON)}$	I_D
20V	6.2m Ω	50A

Applications

- Power Management for Industrial DC/DC Converters
- Ideal for high-frequency switching and synchronous rectification

TO-252-2L Pin Configuration



Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter		Rating	Units
V_{DS}	Drain-Source Voltage		20	V
V_{GS}	Gate-Source Voltage		± 12	
I_D ⁷	Continuous Drain Current	$T_C=25^{\circ}\text{C}$	50	A
		$T_C=100^{\circ}\text{C}$	35	
I_{DM} ³	Pulse Drain Current		120	W
P_D ²	Power Dissipation	$T_C=25^{\circ}\text{C}$	42	
I_{AS} ³	Single pulse Avalanche Current		25	A
E_{AS} ³	Single pulse Avalanche Energy	$L=0.5\text{mH}$	150	mJ
T_{STG}	Storage Temperature Range		-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range		-55 to 150	
$R_{\theta JA}$ ^{1,4}	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	31	$^{\circ}\text{C/W}$
		Steady State	62.5	
$R_{\theta JC}$	Thermal Resistance-Junction to Case		3.6	

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	20	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=4.5V$, $I_D=20A$	---	6.2	8.5	m Ω
		$V_{GS}=2.5V$, $I_D=15A$	---	8.8	13	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	0.4	0.7	1.1	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=20V$, $V_{GS}=0V$	---	---	1.0	μA
		$T_J=55^{\circ}\text{C}$	---	---	5.0	
I_{GSS}	Gate-Source Leakage Current	$V_{DS}=0V$, $V_{GS}=\pm 10V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V$, $I_D=1A$	---	18	---	S
R_G	Gate Resistance	$f=1.0\text{MHz}$	---	6.0	---	Ω
Q_g	Total Gate Charge (10V)	$V_{DS}=10V$, $V_{GS}=4.5V$, $I_D=20A$	---	19	---	nC
Q_{gs}	Gate-Source Charge		---	3	---	
Q_{gd}	Gate-Drain Charge		---	6.6	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=10V$, $V_{GS}=4.5V$, $I_D=10A$ $R_L=1\Omega$, $R_{GEN}=3\Omega$	---	11	---	ns
T_r	Rise Time		---	22	---	
$T_{d(off)}$	Turn-Off Delay Time		---	39	---	
T_f	Fall Time		---	20	---	
C_{iss}	Input Capacitance	$V_{DS}=10V$, $V_{GS}=0V$, $f=1.0\text{MHz}$	---	1500	---	pF
C_{oss}	Output Capacitance		---	240	---	
C_{rss}	Reverse Transfer Capacitance		---	222	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I_S	Continuous Source Current		---	---	50	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=1A$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=20A$, $di/dt=100A/\mu s$	---	35	---	ns
Q_{rr}	Reverse Recovery Charge		---	21	---	nC

Note:

1. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The Power dissipation P_{DSM} is based on $R_{\theta JA} \leq 10s$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.
2. The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
3. Single pulse width limited by junction temperature $T_{J(MAX)}=150^{\circ}\text{C}$.
4. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JC}$ and case to ambient.
5. The static characteristics in Figures 1 to 6 are obtained using $<300\mu s$ pulses, duty cycle 0.5% max.
6. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)}=150^{\circ}\text{C}$. The SOA curve provides a single pulse rating.
7. The maximum current rating is package limited.
8. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$.
9. The maximum current rating is silicon limited

Typical Characteristics

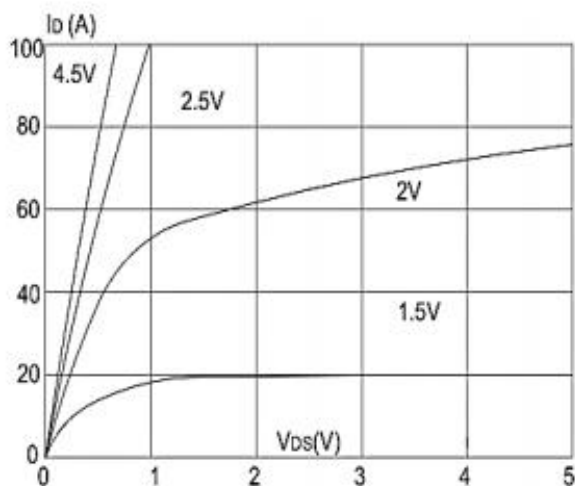


Figure 1: Output Characteristics

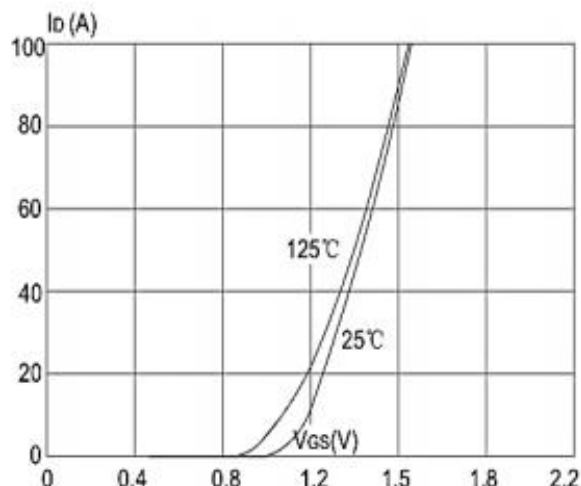


Figure 2: Typical Transfer Characteristics

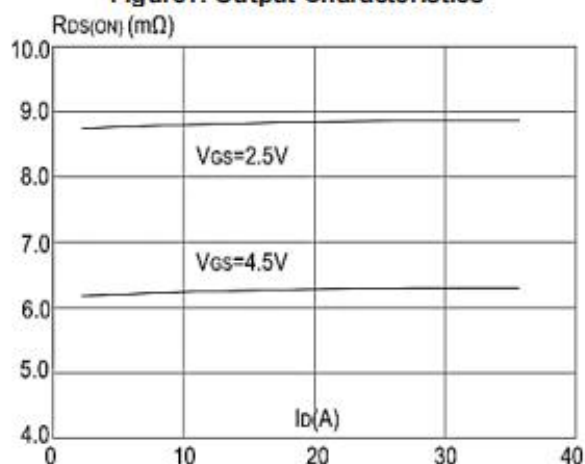


Figure 3: On-resistance vs. Drain Current

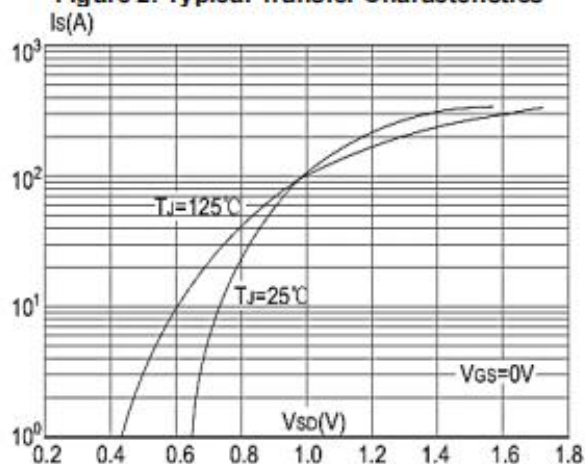


Figure 4: Body Diode Characteristics

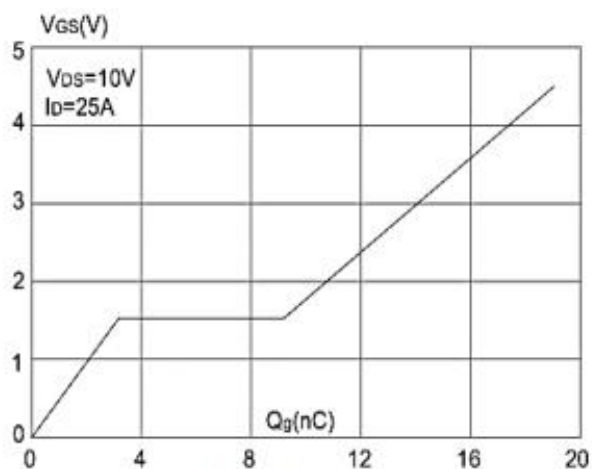


Figure 5: Gate Charge Characteristics

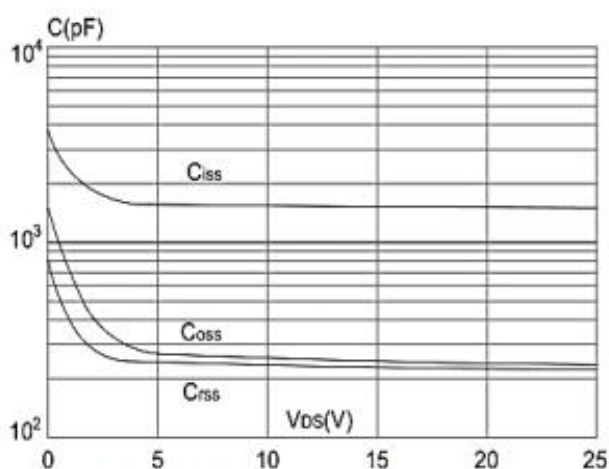


Figure 6: Capacitance Characteristics

Typical Characteristics

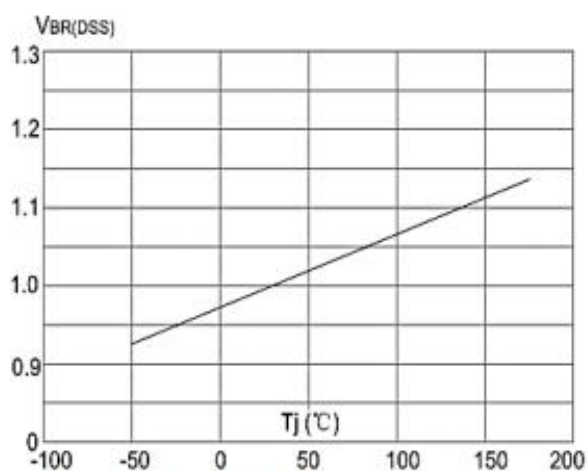


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

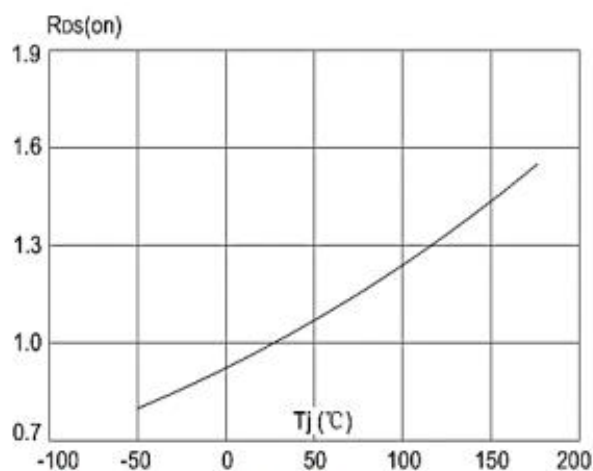


Figure 8: Normalized on Resistance vs. Junction Temperature

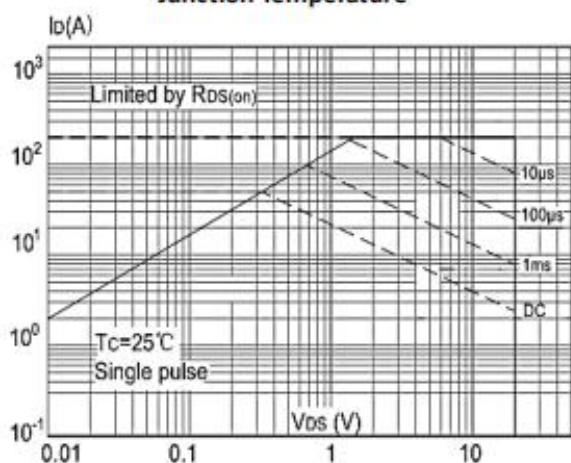


Figure 9: Maximum Safe Operating Area vs. Case Temperature

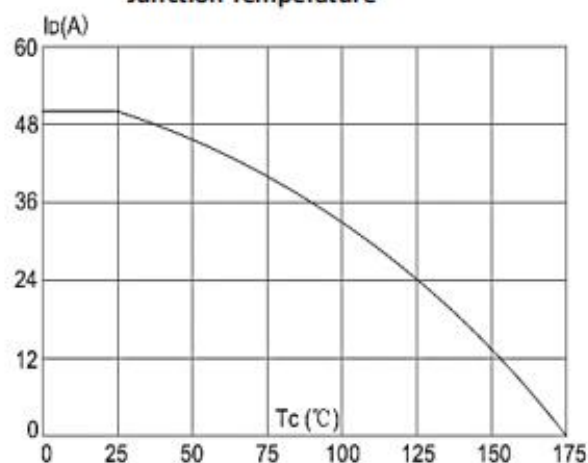


Figure 10: Maximum Continuous Drain Current

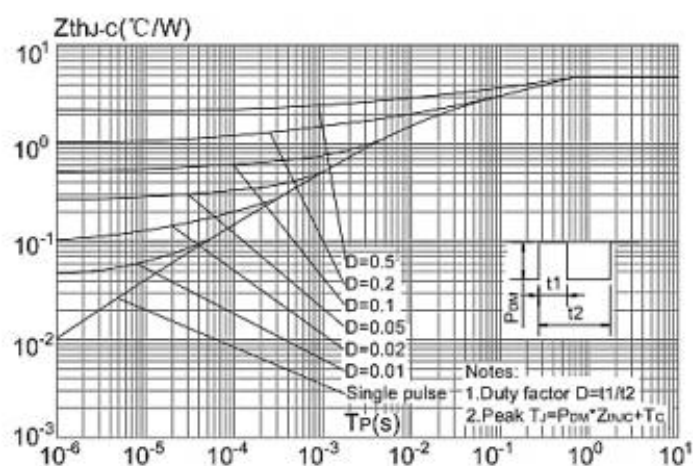
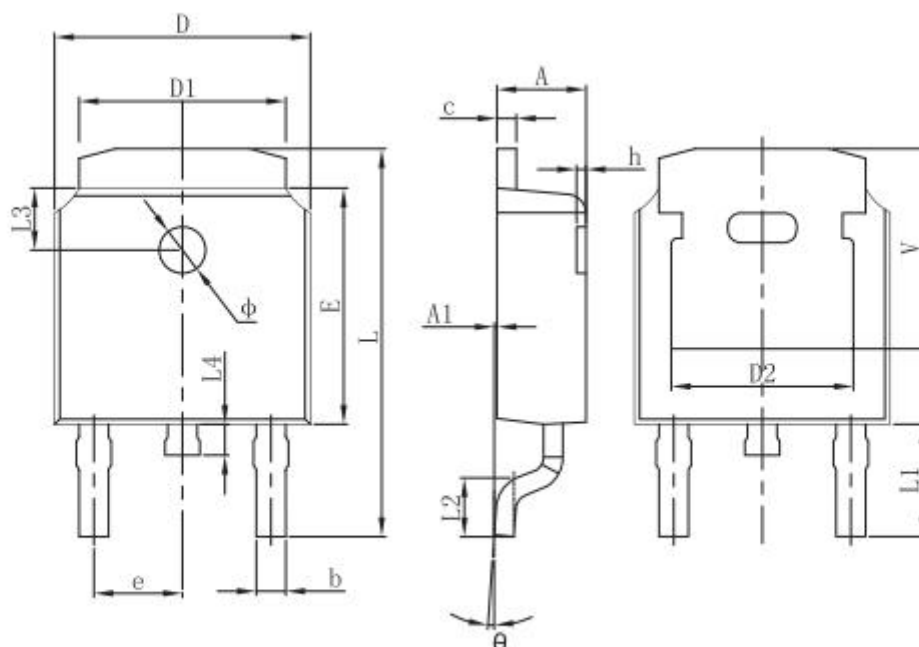


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

Packaging information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250 REF.		0.207 REF.	

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