

6MHz Rail-to-Rail I/O CMOS Operational Amplifier

Features

Single-Supply Operation from +2.1V ~ +5.5V

Rail-to-Rail Input / Output

• Gain-Bandwidth Product: 6MHz (Typ)

Low Input Bias Current: 1pA (Typ)

Low Offset Voltage: 3.5mV (Max)

Quiescent Current: 470µA per Amplifier (Typ)

Operating Temperature: -40°C ~ +125°C

Small Package:

OPA374 Available in SOT23-5,SC70-5and SOP-8 Packages
OPA2374 Available in SOP-8 and MSOP-8 Packages

OPA4374 Available in SOP-14 and TSSOP-14 Packages



Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
OPA374M5/TR	SOT-23-5	A374,A76	REEL	3000pcs/reel
OPA374M7/TR	SC70-5(SOT-353)	A374	REEL	3000pcs/reel
OPA374M/TR	SOP-8	OPA374	REEL	2500pcs/reel
OPA2374M/TR	SOP-8	OPA2374	REEL	2500pcs/reel
OPA2374MM/TR	MSOP-8	A2374	REEL	3000pcs/reel
OPA4374M/TR	SOP-14	OPA4374	REEL	2500pcs/reel
OPA4374MT/TR	TSSOP-14	OPA4374	REEL	2500pcs/reel



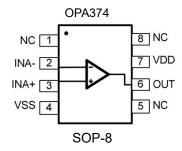
General Description

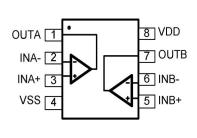
The OPA374/2374/4374 have a high gain-bandwidth product of 6MHz, a slew rate of $4.2V/\mu s$, and a quiescent current of $470\mu A$ per amplifier at 5V. The OPA374/2374/4374 are designed to provide optimal performance in low voltage and low noise systems. They provide rail-to-rail output swing into heavy loads. The input common mode voltage range includes ground, and the maximum input offset voltage is 3.5mV for OPA374/2374/4374. They are specified over the extended industrial temperature range ($-40^{\circ}C$ to $+125^{\circ}C$). The operating range is from 2.1V to 5.5V. The OPA374 single is available in Green SC70-5, SOT23-5 and SOP-8 packages. The OPA2374 dual is available in Green SOP-8 and MSOP-8 packages. The OPA4374 Quad is available in Green SOP-14 and TSSOP-14 packages

Applications

- Sensors
- Active Filters
- Cellular and Cordless Phones
- Laptops and PDAs
- Audio
- Handheld Test Equipment
- Battery-Powered Instrumentation
- A/D Converters

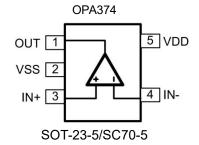
Pin Configuration

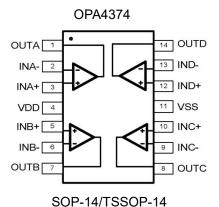




OPA2374

SOP-8/MSOP-8







Absolute Maximum Ratings

CONDITIONS	MIN	MAX				
Power Supply Voltage (VDD to Vss)	-0.5V	+7.5V				
Analog Input Voltage (IN+ or IN-)	Vss-0.5V	VDD+0.5V				
PDB Input Voltage	Vss-0.5V	+7V				
Operating Temperature Range	-40°C	+125°C				
Junction Temperature	+16	0°C				
Storage Temperature Range	-55°C	+150°C				
Lead Temperature (soldering, 10sec)	+260°C					
Package Thermal Resistance (TA=+25℃)						
SOP-8, θ _{JA}	125°	C/W				
MSOP-8, θ _{JA}	216°	C/W				
SOT23-5, θ _{JA}	190°	C/W				
SC70-5, θ _{JA}	333°	C/W				
ESD Susceptibility						
НВМ	8k	(V				
MM	400	0V				

Note: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability



Electrical Characteristics

(At Vs=5V, T_A = +25°C, V_{CM} = $V_S/2$, R_L = 600 Ω , unless otherwise noted.)

				OI	PA374/23	374/4374		
DADAMETED	CONDITIONS	TYP		MIN/I	MAX OVI	ER TEMPI	ERATURE	
PARAMETER	CONDITIONS	+25 ℃	+25 ℃	0℃ to 70℃	-40℃ to 85℃	-40 ℃ to 125℃	UNITS	MIN /MAX
NPUT CHARACTERISTICS								
Input Offset Voltage (Vos)		8.0	3.5	3.9	4.3	4.6	mV	MAX
Input Bias Current (I _B)		1					pА	TYP
Input Offset Current (Ios)		1					pA	TYP
Input Common Mode Voltage Range (V _{CM})	Vs = 5.5V	-0.1 to +5.6					V	TYP
Common Mode Rejection Ratio (CMRR)	$V_S = 5.5V$, $V_{CM} = -0.1V$ to 4V	90	73	70	70	65	dB	MIN
	$V_S = 5.5V$, VCM = -0.1V to 5.6V	83					dB	MIN
Open-Loop Voltage Gain (A _{OL})	$R_L = 600\Omega, VO = 0.15V \text{ to } 4.85V$	97	90	87	86	79	dB	MIN
	$R_L = 10k\Omega, VO = 0.05V \text{ to } 4.95V$	108					dB	MIN
Input Offset Voltage Drift ($\Delta V_{OS}/\Delta_T$)		2.4					μV/°C	TYP
OUTPUT CHARACTERISTICS								
Output Voltage Swing from Rail	$R_L = 600\Omega$	0.1					V	TYP
	$R_L = 10k\Omega$	0.015					V	TYP
Output Current (I _{OUT})		53	49	45	40	35	mA	MIN
Closed-Loop Output Impedance	f = 200kHz, G = 1	3					Ω	TYP
POWER-DOWN DISABLE								
Turn-On Time		4					μs	TYP
Turn-Off Time		1.2					μs	TYP
POWER SUPPLY								
Operating Voltage Range			2.1	2.1	2.1	2.1	V	MIN
Power Supply Rejection Ratio (PSRR)	$V_S = +2.5V \text{ to } +5.5V$ $V_{CM} = (-VS) + 0.5V$	91	5.5 74	5.5 72	5.5 72	5.5 68	V dB	MAX MIN
Quiescent Current/Amplifier (IQ)	$I_{OUT} = 0$	470	650	750	750	815	μA	MAX



Electrical Characteristics

(At Vs=5V, TA = +25°C, VCM = VS/2, RL = 600Ω , unless otherwise noted.)

			OPA374/2374/4374									
DADAMETED	CONDITIONS	TYP	MIN	MIN/MAX OVER TEMPERATURE								
PARAMETER	CONDITIONS	+25°C-		0℃to 70℃	-40℃to 85℃	-40℃to 125℃	UNITS	MIN / MAX				
DYNAMIC PERFORMANCE												
Gain-Bandwidth Product (GBP)	$R_L = 10k\Omega$, $C_L = 100pF$	6					MHz	TYP				
Phase Margin (φ ₀)	$R_L = 10k\Omega$, $C_L = 100pF$	53					Degrees	TYP				
Full Power Bandwidth (BWP)	$<$ 1% distortion, R _L = 600 Ω	250					kHz	TYP				
Slew Rate (SR)	$G = +1$, 2V Step, $R_L = 10$ kΩ	4.2					V/µs	TYP				
Settling Time to 0.1% (ts)	G = +1, 2V Step, R _L = 600Ω	0.4					μs	TYP				
Overload Recovery Time	V _{IN} ·Gain = VS, R _L = 600Ω	2.5					μs	TYP				
NOISE PERFORMANCE												
Voltage Noise Density (e _n)	f = 1kHz	13					nV /√Hz	TYP				
	f = 10kHz	9.5					nV /√Hz	TYP				



Typical Performance characteristics

(At Vs=5V, $T_A = +25^{\circ}\text{C}$, $V_{CM} = V_S/2$, $R_L = 600\Omega$, unless otherwise noted.) Large-Signal Step Response Large-Signal Step Response Voltage (500mV/div) Vs=5V Voltage (1V/div) Vs=2.5V G=+1 G=+1 CL=100pF CL=100pF RL=10KΩ RL=10 KΩ Time (1µs/div) Time (1µs/div) Small-Signal Step Response Small-Signal Step Response Voltage (50mV/div) Voltage (50mV/div) Vs=5V Vs=2.5V G=+1 CL=100pF G=+1 CL=100pF RL=10 KΩ RL=10 KΩ Time (1µs/div) Time (1µs/div) Negative Overload Recov Positive Overload Recovery 50mV 50mV VIN VIN /div /div $Vs = \pm 2.5V$ VIN=-50mVp-p RL=600Ω G=-100 RL=600Ω 1V Vout G=-100 $Vs = \pm 2.5V$ 500mV /div VIN=50mVp-p /div Vout

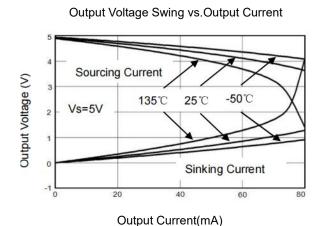
Time (2µs/div)

Time (2µs/div)



Typical Performance characteristics

(At Vs=5V, T_A = +25°C, V_{CM} = $V_S/2$, R_L = 600 Ω , unless otherwise noted.)



Supply Current vs. Temperature

(VI)

500

450

450

Vs=2.5

Vs=3

350

350

25

0

25

50

75

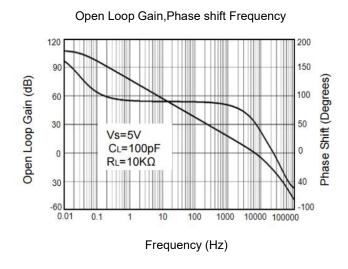
100

125

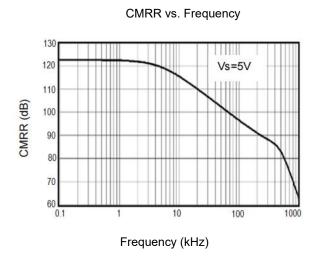
Input Voltage Noise Spectral Density vs. Frequency

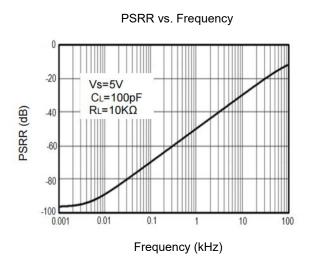
125
100
75
Vs=5V
CL=100pF
RL=10KΩ

Frequency (Hz)



Temperature (°C)







Application Note

Size

OPA374/2374/4374 series op amps are unity-gain stable and suitable for a wide range of general-purpose applications. The small footprints of the OPA374/2374/4374 series packages save space on printed circuit boards and enable the design of smaller electronic products.

Power Supply Bypassing and Board Layout

OPA374/2374/4374 series operates from a single 2.1V to 5.5V supply or dual ± 1.05 V to ± 2.75 V supplies. For best performance, a 0.1μ F ceramic capacitor should be placed close to the VDD pin in single supply operation. For dual supply operation, both VDD and VSS supplies should be bypassed to ground with separate 0.1μ F ceramic capacitors.

Low Supply Current

The low supply current (typical 470µA per channel) of OPA374/2374/4374 series will help to maximize battery life. They are ideal for battery powered systems.

Operating Voltage

OPA374/2374/4374 series operate under wide input supply voltage (2.1V to 5.5V). In addition, all temperature specifications apply from -40°C to +125°C. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-lon battery lifetime.

Rail-to-Rail Input

The input common-mode range of OPA374/2374/4374 series extends 100mV beyond the supply rails (V_{SS} -0.1V to V_{DD} +0.1V). This is achieved by using complementary input stage. For normal operation, inputs should be limited to this range.

Rail-to-Rail Output

Rail-to-Rail output swing provides maximum possible dynamic range at the output. This is particularly important when operating in low supply voltages. The output voltage of OPA374/2374/4374 series can typically swing to less than 2mV from supply rail in light resistive loads (>100k Ω), and 60mV of supply rail in moderate resistive loads (10k Ω).

Capacitive Load Tolerance

The OPA374/2374/4374 family is optimized for bandwidth and speed, not for driving capacitive loads. Output capacitance will create a pole in the amplifier's feedback path, leading to excessive peaking and potential oscillation. If dealing with load capacitance is a requirement of the application, the two strategies to consider are (1) using a small resistor in series with the amplifier's output and the load capacitance and (2) reducing the bandwidth of the amplifier's feedback loop by increasing the overall noise gain. Figure 2. shows a unity gain follower using the series resistor strategy. The resistor isolates the output from the capacitance and, more importantly, creates a zero in the feedback path that compensates for the pole created by the output capacitance.



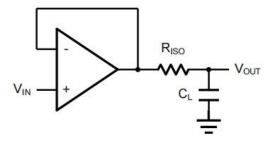


Figure 2. Indirectly Driving a Capacitive Load Using Isolation Resistor

The bigger the R_{ISO} resistor value, the more stable V_{OUT} will be. However, if there is a resistive load R_L in parallel with the capacitive load, a voltage divider (proportional to R_{ISO}/R_L) is formed, this will result in a gain error.

The circuit in Figure 3 is an improvement to the one in Figure 2. R_F provides the DC accuracy by feed-forward the V_{IN} to R_L . C_F and R_{ISO} serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving the phase margin in the overall feedback loop. Capacitive drive can be increased by increasing the value of C_F . This in turn will slow down the pulse response.

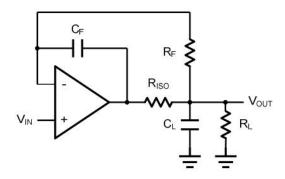


Figure 3. Indirectly Driving a Capacitive Load with DC Accuracy



Typical Application Circuits

Differential amplifier

The differential amplifier allows the subtraction of two input voltages or cancellation of a signal common the two inputs. It is useful as a computational amplifier in making a differential to single-end conversion or in rejecting a common mode signal. Figure 4. shown the differential amplifier using OPA374/2374/4374.

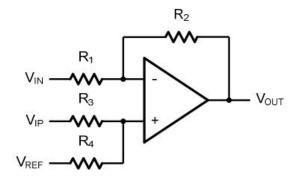


Figure 4. Differential Amplifier

$$V_{OUT} \text{--} (\frac{R_1 + R_2}{R_3 + R_4}) \frac{R_4}{R_1} V_{IN} \text{--} \frac{R2}{R1} V_{IP} \text{--} (\frac{R_1 + R_2}{R_3 + R_4}) \frac{R_3}{R_1} V_{REF}$$

If the resistor ratios are equal (i.e. $R_1 = R_3$ and $R_{2=}R_4$), then

$$V_{OUT} = \frac{R2}{R1} (V_{IP} - V_{IN}) + V_{REF}$$

Low Pass Active Filter

The low pass active filter is shown in Figure 5. The DC gain is defined by -R2/R1. The filter has a -20dB/decade roll-off after its corner frequency $fC=1/(2\pi R3C1)$

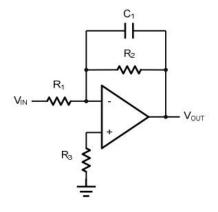


Figure 5. Low Pass Active Filter



Instrumentation Amplifier

The triple OPA374/2374/4374 can be used to build a three-op-amp instrumentation amplifier as shown in Figure 6. The amplifier in Figure 6 is a high input impedance differential amplifier with gain of R_2/R_1 . The two differential voltage followers assure the high input impedance of the amplifier.

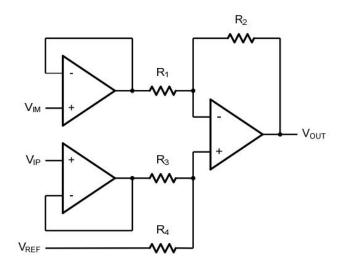
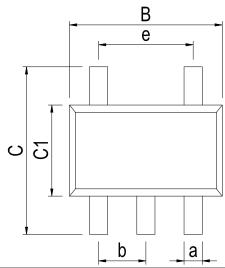


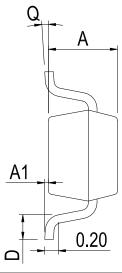
Figure 6. Instrument Amplifier



Physical Dimensions

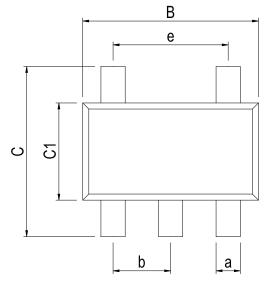
SOT-23-5

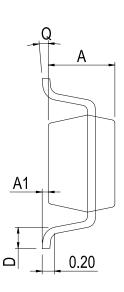




Dimensions In	Dimensions In Millimeters(SOT-23-5)											
Symbol:	Α	A1	В	С	C1	D	Q	а	b	е		
Min:	1.00	0.00	2.82	2.65	1.50	0.30	0°	0.30	0.05 BCC	1.90 BSC		
Max:	1.15	0.15	3.02	2.95	1.70	0.60	8°	0.50	0.95 BSC			

SC70-5



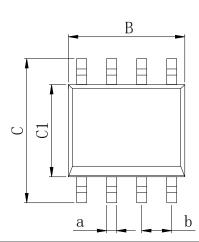


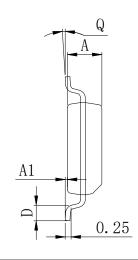
Dimensions In Millimeters(SC70-5)										
Symbol:	А	A1	В	С	C1	D	Q	а	b	е
Min:	0.90	0.00	2.00	2.15	1.15	0.26	0°	0.15	0.65	4 20 DCC
Max:	1.00	0.15	2.20	2.45	1.35	0.46	8°	0.35	BSC	1.30 BSC



Physical Dimensions

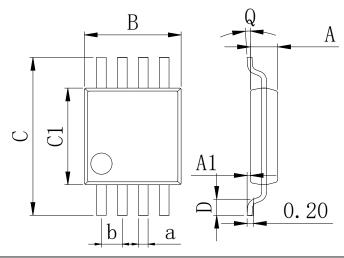
SOP-8 (150mil)





Dimensions In Millimeters(SOP-8)										
Symbol:	Α	A1	В	С	C1	D	Q	а	b	
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1 27 DCC	
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	1.27 BSC	

MSOP-8

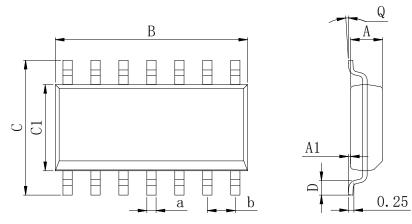


Dimensions In Millimeters(MSOP-8)										
Symbol:	Α	A1	В	С	C1	D	Q	а	b	
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65.000	
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	0.65 BSC	



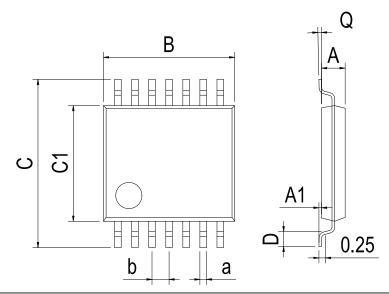
Physical Dimensions

SOP-14



Dimensions In Millimeters(SOP-14)										
Symbol:	Α	A1	В	С	C1	D	Q	а	b	
Min:	1.35	0.05	8.55	5.80	3.80	0.40	0°	0.35	4 27 DCC	
Max:	1.55	0.20	8.75	6.20	4.00	0.80	8°	0.45	- 1.27 BSC	

TSSOP-14



Dimensions In Millimeters(TSSOP-14)										
Symbol:	Α	A1	В	С	C1	D	Q	а	b	
Min:	0.85	0.05	4.90	6.20	4.30	0.40	0°	0.20	0.65.050	
Max:	0.95	0.20	5.10	6.60	4.50	0.80	8°	0.25	0.65 BSC	



Revision History

DATE	REVISION	PAGE
2018-6-5	New	1-16
2024-10-25	Update SOT-23-5 Physical dimension and Lead Temperature	12、3



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