

**N-Channel MOSFET** 

#### **General Description**

The WSR20N10 uses advanced Ternch MOSFET to provide excellent  $R_{DS(ON)}$ , low gate charge

This device is suitable for use as a Battery protection or in other Switching application.

#### **Features**

- 100% UIS + R<sub>g</sub> Tested.
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- Moisture Sensitivity Level MSL1 (per JEDEC J-STD-020D)

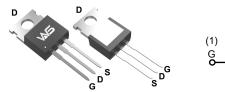
#### **Product Summery**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	l <sub>D</sub>
100V	72mΩ	20A

### **Applications**

Power Management in Notebook Computer,
 Portable Equipment and Battery Powered
 Systems.

# **TO-220-3L Pin Configuration**





## **Absolute Maximum Ratings** (T<sub>A</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter		Rating	Units	
$V_{DS}$	Drain-Source Voltage	Drain-Source Voltage Gate-Source Voltage		V	
V <sub>GS</sub>	Gate-Source Voltage			V	
I <sub>S</sub>	Diode Continuous Forward Current	T <sub>C</sub> =25°C	20		
	Cantinuous Dunin Cumant	T <sub>C</sub> =25°C	20		
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> =100°C	12	Α	
I <sub>DM</sub> <sup>2</sup>	Pulse Drain Current	T <sub>C</sub> =25°C	45		
D	Maximum Davier Dissination	T <sub>C</sub> =25°C	30	W	
$P_{D}$	Maximum Power Dissipation	T <sub>C</sub> =100°C	17		
R <sub>0JA</sub> <sup>4</sup>	Thermal Resistance-Junction to Ambient	Steady State	62.5	20044	
$R_{ heta JC}$	Thermal Resistance-Junction to Case		5	°C/W	
I <sub>AS</sub> <sup>3</sup>	Avalanche Current, Single pulse	L=0.5mH	8	Α	
E <sub>AS</sub> <sup>3</sup>	Avalanche Energy, Single pulse	L=0.5mH	90	mJ	
T <sub>STG</sub>	Storage Temperature Range	ange -55 to 150		00	
TJ	Maximum Junction Temperature		-55 to 150	°C	



**N-Channel MOSFET** 

# **Electrical Characteristics** (T<sub>A</sub>=25°C, Unless Otherwise Noted)

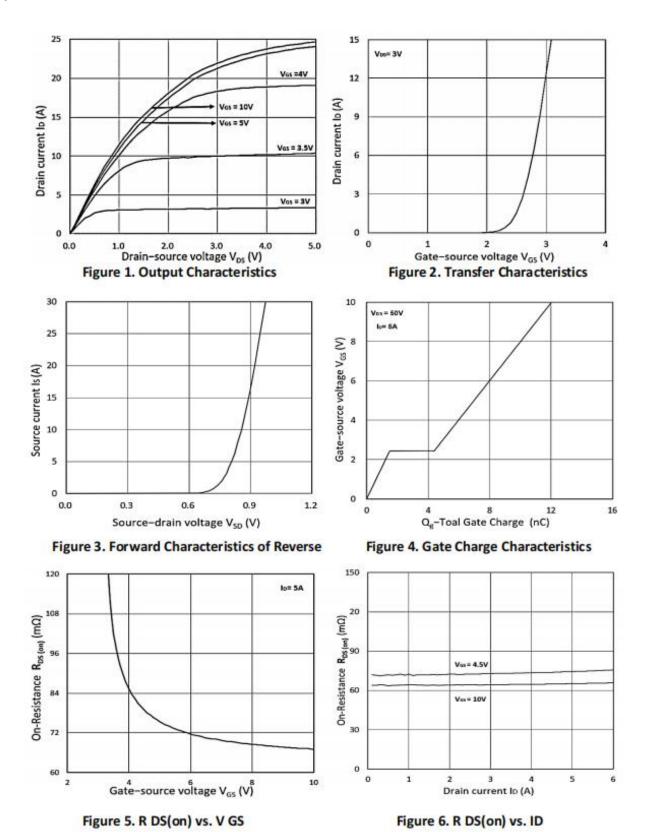
Symbol	Parameter	Conditions		Min.	Тур.	Max.	Units
Static Chara	cteristics						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250μA		100			V
		V <sub>DS</sub> =48V , V <sub>GS</sub> =0V				1.0	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current		T <sub>J</sub> =85°C			30	μA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>DS</sub> =250μ	A	1.2	2.0	2.5	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V				±100	nA
<b>D</b> 5	Drain-Source On-state Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =2A			72	100	mΩ
R <sub>DS(ON)</sub> <sup>5</sup>	Drain-Source On-state Resistance	$V_{GS}$ =4.5 $V$ , $I_{D}$ =1 $A$			88	110	
Diode Chara	cteristics						
V <sub>SD</sub> <sup>5</sup>	Diode Forward Voltage	I <sub>SD</sub> =1A , V <sub>GS</sub> =0V			0.8	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	- I <sub>DS</sub> =60A , di <sub>SD</sub> /dt=100A/μs			50		ns
Q <sub>rr</sub>	Reverse Recovery Charge				70		nC
Dynamic Ch	aracteristics <sup>6</sup>						
$R_g$	Gate Resistance	V <sub>GS</sub> =0V , V <sub>DS</sub> =0V , f	=1.0MHz		3.0		Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V , V <sub>DS</sub> =20V ,			1215		
C <sub>oss</sub>	Output Capacitance	Frequency=1.0MHz			56		pF
C <sub>rss</sub>	Reverse Transfer Capacitance				45		
T <sub>d(on)</sub>	Turn-on Delay Time	$V_{DD}$ =30V , $R_L$ =30 $\Omega$ , $I_{DS}$ =5A , $V_{GEN}$ =10V , $R_G$ =1.8 $\Omega$			3.9		
T <sub>r</sub>	Turn-on Rise Time				26		ns
$T_{d(off)}$	Turn-off Delay Time				17		
T <sub>f</sub>	Turn-off Fall Time				9		
Gate Charge	Characteristics <sup>6</sup>						
Qg	Total Gate Charge	V <sub>DS</sub> =50V , V <sub>GS</sub> =10V , I <sub>DS</sub> =6A			12		
Q <sub>gs</sub>	Gate-Source Charge				3		nC
$Q_gd$	Gate-Drain Charge				2		

#### Note:

- 1. Calculated continuous current based on maximum allowable junction temperature. Bonding wire limitation current is 8A.
- 2. Pulse width limited by max. junction temperature.
- 3. UIS tested and pulse width limited by maximum junction temperature  $150^{\circ}$ C (initial temperature  $T_J=25^{\circ}$ C).
- 4. Surface Mounted on 1in<sup>2</sup> pad area.
- 5. Pulse test ; pulse width≤300µs, duty cycle≤2%.
- 6. Guaranteed by design, not subject to production testing.



## **Typical Characteristics**



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### **Typical Characteristics (Cont.)**

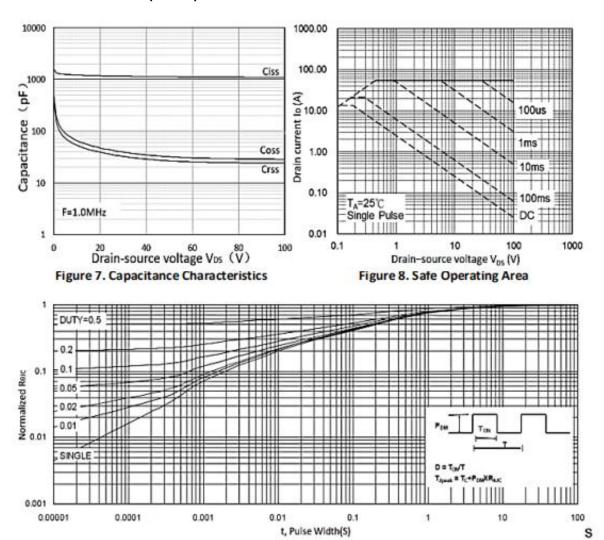


Figure 9. Normalized Maximum Transient Thermal Impedance

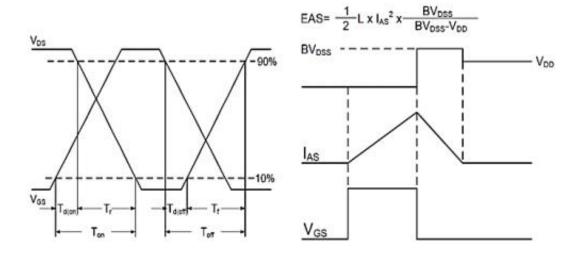
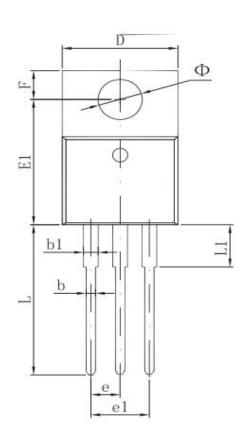


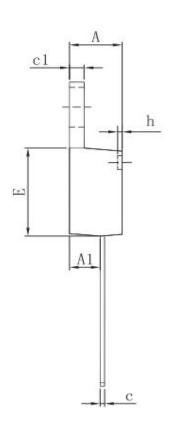
Figure 10. Switching Time Waveform

Figure 11. Unclamped Inductive Switching Waveform



# **Packaging information**





O b - 1	Dimensions In Millimeters		Dimensions In Inche	
Symbol	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
С	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
Е	8.500	8.900	0.335	0.350
E1	12.060	12.460	0.475	0.491
е	2.540	) TYP	0.100	) TYP
e1	4.980	5. 180	0.196	0.204
F	2.590	2.890	0.102	0.114
h	0.000	0.300	0.000	0.012
L	13.400	13.800	0.528	0.543
L1	3.560	3.960	0.140	0.156
Φ	3.735	3.935	0.147	0.155





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