

IMW120R030M1H-VB Datasheet

N-Channel 1200V (D-S) SiC Power MOSFET

PRODUCT SUMMARY

V_{DS} (V) at T_J max.	1200	
$R_{DS(on)}$ at 25 °C (Ω)	$V_{GS} = 18V$	0.040
Q_g (nC)	101	

FEATURES

- Low figure-of-merit (FOM) $R_{on} \times Q_g$
- Low input capacitance (C_{iss})
- Reduced switching and conduction losses
- Ultra low gate charge (Q_g)
- Avalanche energy rated (UIS)

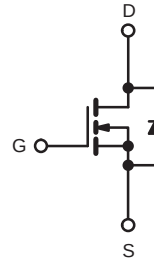


RoHS

APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- DC/DC converter

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N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

PARAMETER			SYMBOL	LIMIT	UNIT
Drain-Source Voltage			V_{DS}	1200	V
Gate-Source Voltage			V_{GS}	-10 / +22	
Continuous Drain Current ($T_J = 150\text{ }^{\circ}\text{C}$)	V_{GS} at 18V	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	60	A
		$T_C = 100\text{ }^{\circ}\text{C}$		42	
Pulsed Drain Current ^a			I_{DM}	160	
Linear Derating Factor				2.1	W/ $^{\circ}\text{C}$
Single Pulse Avalanche Energy ^b			E_{AS}	1200	mJ
Maximum Power Dissipation			P_D	320	W
Operating Junction and Storage Temperature Range			T_J, T_{stg}	-55 to +175	$^{\circ}\text{C}$
Drain-Source Voltage Slope	$T_J = 125\text{ }^{\circ}\text{C}$		dV/dt	50	V/ns
Reverse Diode dV/dt ^d		15			
Soldering Recommendations (Peak Temperature) ^c	for 10 s			260	$^{\circ}\text{C}$

Notes

- Repetitive rating; pulse width limited by maximum junction temperature.
- $V_{DD} = 100\text{ V}$, starting $T_J = 25\text{ }^{\circ}\text{C}$, $L = 30\text{ mH}$, $R_g = 25\text{ }\Omega$, $I_{AS} = 9\text{ A}$.
- 1.6 mm from case.
- $I_{SD} \leq I_D$, $dI/dt = 100\text{ A}/\mu\text{s}$, starting $T_J = 25\text{ }^{\circ}\text{C}$.

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R_{thJA}	-	40	°C/W
Maximum Junction-to-Case (Drain)	R_{thJC}	-	0.47	

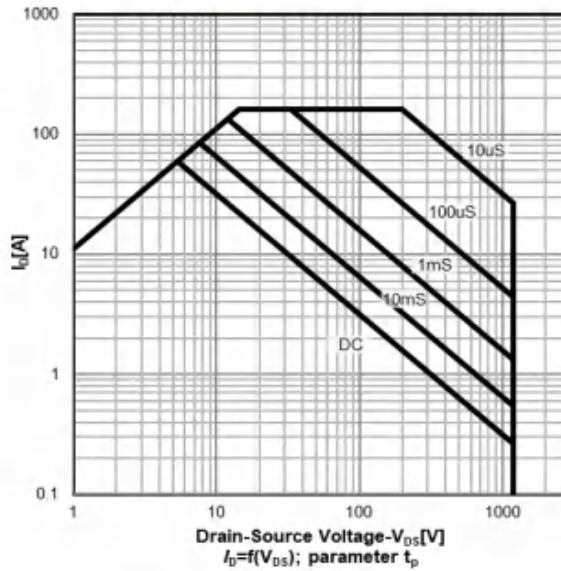
SPECIFICATIONS ($T_J = 25\text{ °C}$, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 1 mA		1200	-	-	V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	Reference to 25 °C, I _D = 1 mA		-	0.70	-	V/°C
Gate-Source Threshold Voltage (N)	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 10 mA		2.5	-	4.5	V
Gate-Source Leakage	I _{GSS}	V _{GS} = +22 V		-	-	100	nA
		V _{GS} = -10 V		-	-	100	μA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 1200 V, V _{GS} = 0 V		-	10	-	μA
		V _{DS} = 1200 V, V _{GS} = 0 V, T _J = 125 °C		-	-	100	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 18 V	I _D = 30A	-	0.040	-	Ω
Forward Transconductance	g _{fs}	V _{DS} = 0 V, I _D = 30 A		-	16	-	S
Dynamic							
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 800 V, f = 1 MHz		-	2200	-	pF
Output Capacitance	C _{oss}			-	123	-	
Reverse Transfer Capacitance	C _{rss}			-	10	-	
Effective Output Capacitance, Energy Related ^a	C _{o(er)}	V _{DS} = 0 V to 800 V, V _{GS} = 0 V		-	156	-	
Effective Output Capacitance, Time Related ^b	C _{o(tr)}			-	268	-	
Total Gate Charge	Q _g	V _{GS} = -5/18 V	I _D = 20 A, V _{DS} = 800 V	-	101	-	nC
Gate-Source Charge	Q _{gs}			-	29	-	
Gate-Drain Charge	Q _{gd}			-	33	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 800 V, I _D = 20A, V _{GS} = -5/18 V , R _g = 2 Ω		-	18	25	ns
Rise Time	t _r			-	24	55	
Turn-Off Delay Time	t _{d(off)}			-	8 0	-	
Fall Time	t _f			-	1 2	-	
Gate Input Resistance	R _g	f = 1 MHz, open drain		-	3.2	-	Ω
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode 		-	-	60	A
Pulsed Diode Forward Current	I _{SM}			-	-	160	
Diode Forward Voltage	V _{SD}	T _J = 25 °C, I _S = 30 A, V _{GS} = 0		-	-	4.1	V
Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = I _S = 30 A, dI/dt = 1000 A/μs, V _R = 800 V		-	47	-	ns
Reverse Recovery Charge	Q _{rr}			-	220	-	μC
Reverse Recovery Current	I _{RRM}			-	60	-	A

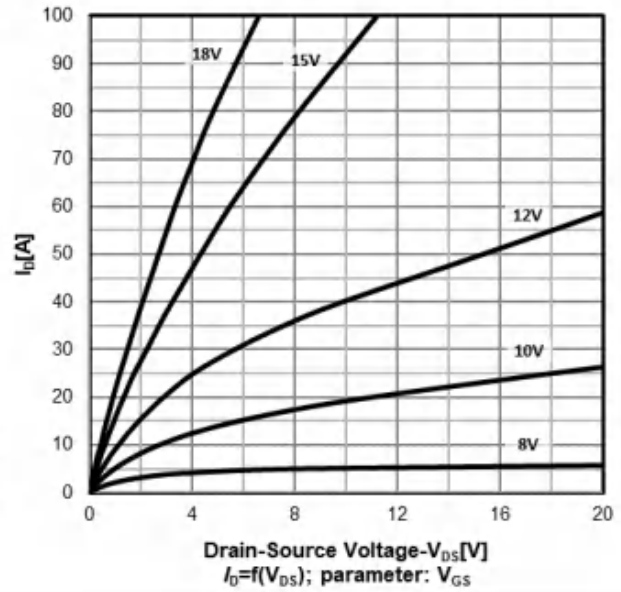
Notes

- a. $C_{oss(er)}$ is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} .
 b. $C_{oss(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} .

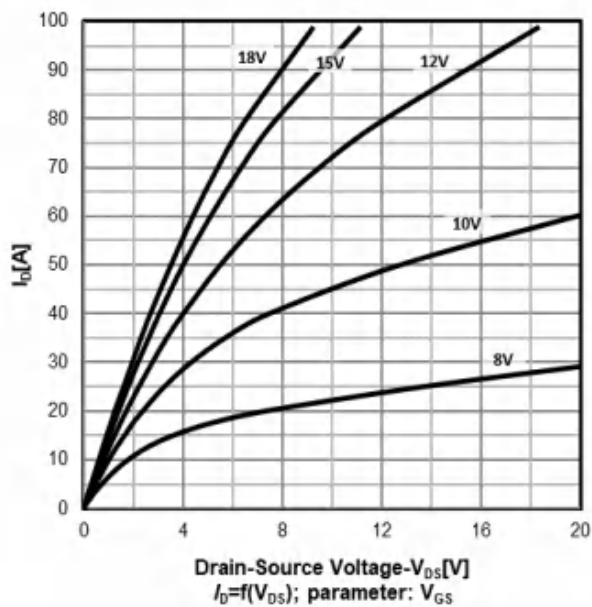
Safe operating area $T_c=25^\circ\text{C}$
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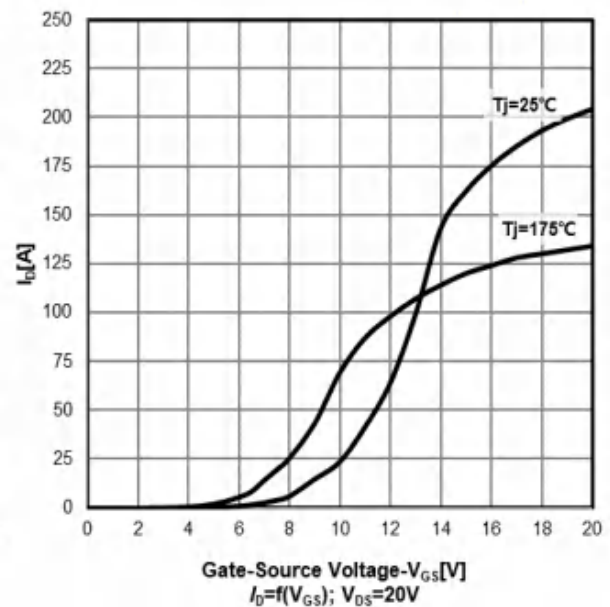
On-Region characteristics $T_J=25^\circ\text{C}$



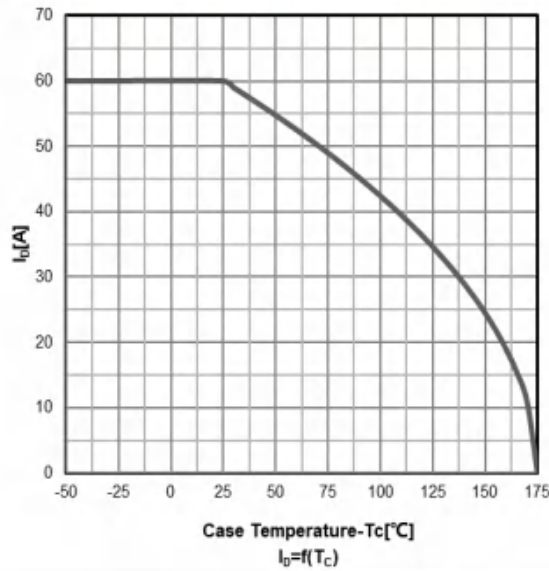
On-Region characteristics $T_J=175^\circ\text{C}$



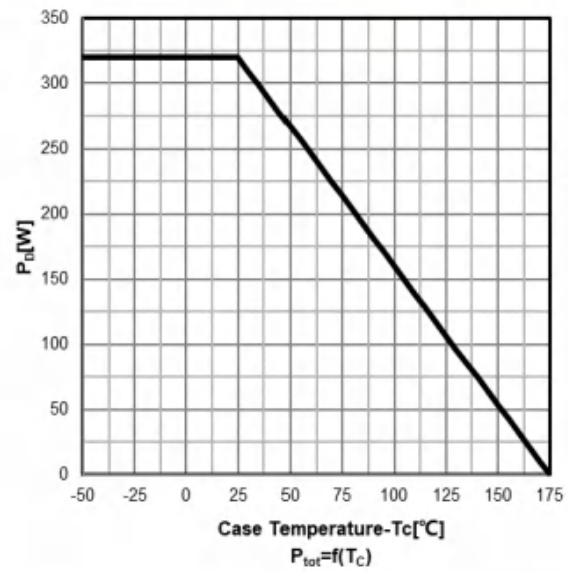
Transfer characteristics



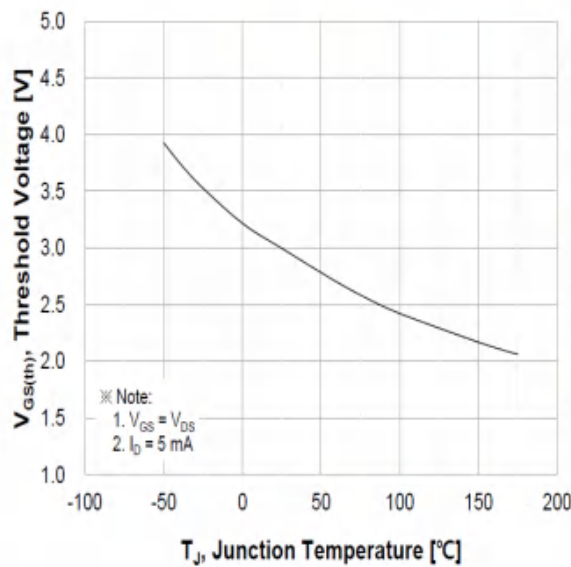
Drain current vs temperature



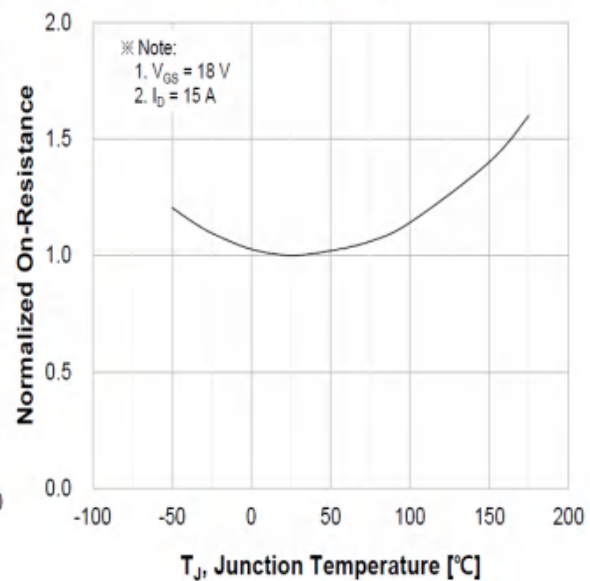
Power dissipation



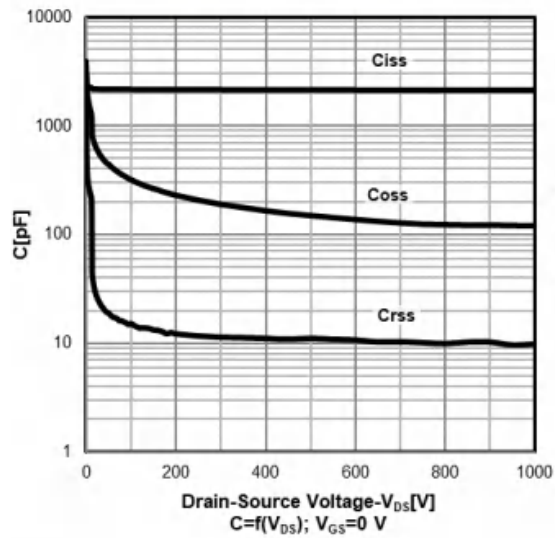
Threshold voltage vs temperature



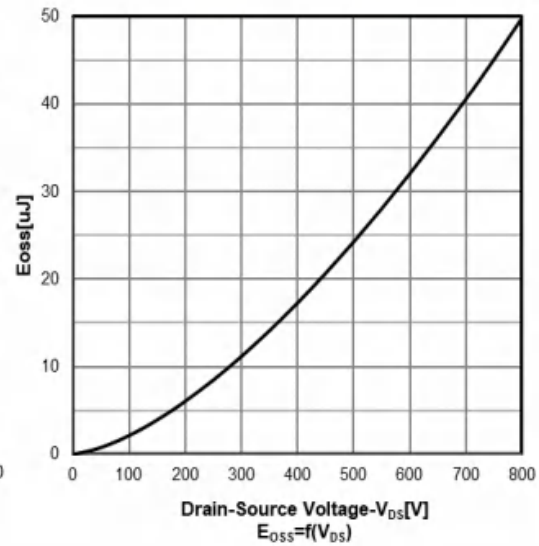
Normalized On-resistance vs temperature



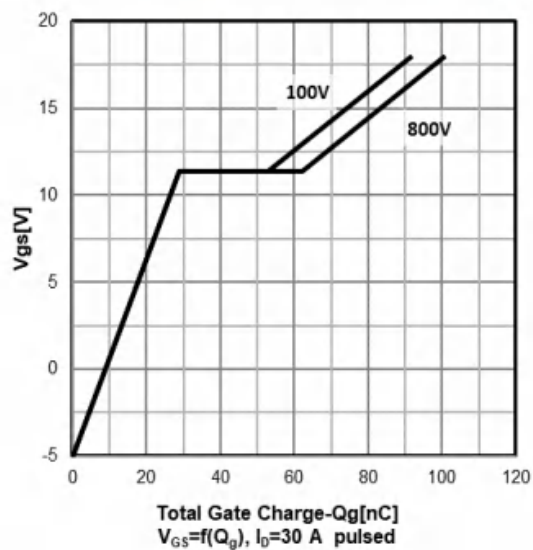
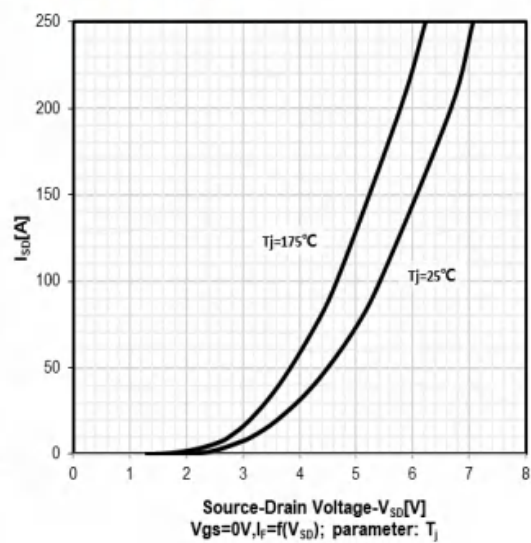
Typ. capacitances



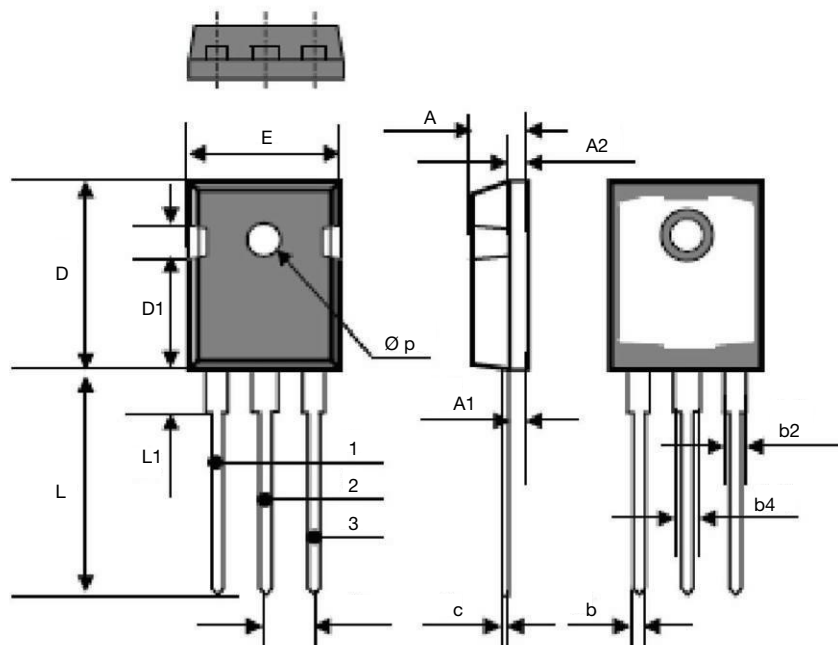
Coss stored energy



Typ. gate charge characteristics

Diode forward voltage characteristics
 $T_J=25^\circ\text{C}/175^\circ\text{C}$ 

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DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.70	5.31	0.185	0.209
A1	2.21	2.59	0.087	0.102
A2	1.50	2.49	0.059	0.098
b	0.99	1.40	0.039	0.055
b2	1.65	2.41	0.065	0.095
b4	2.59	3.43	0.102	0.135
c	0.61 BSC		0.024 BSC	
D	20.80	21.46	0.819	0.845
D1	3.68	5.49	0.145	0.216
(e)	5.46 BSC		0.215 BSC	
E	15.49	16.26	0.610	0.640
L	19.81	20.32	0.780	0.800
L1	4.06	4.50	0.160	0.177
$\varnothing p$	3.51	3.66	0.138	0.144

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