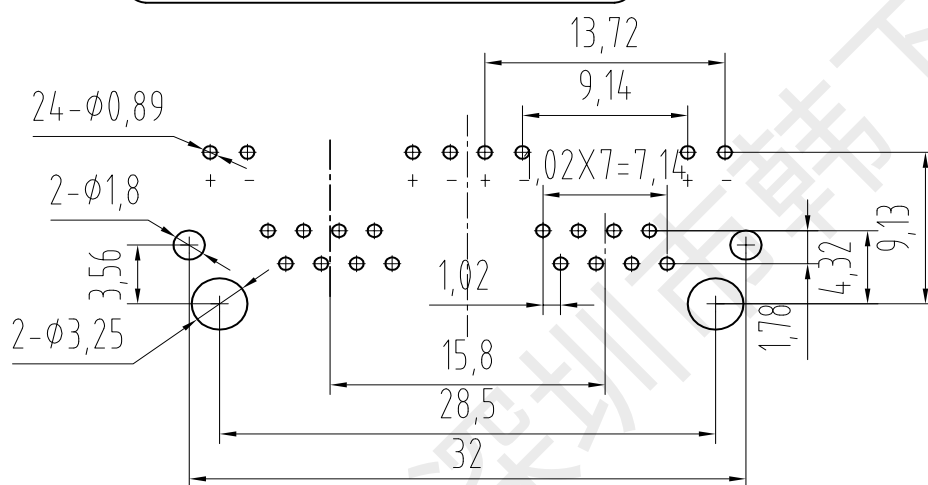
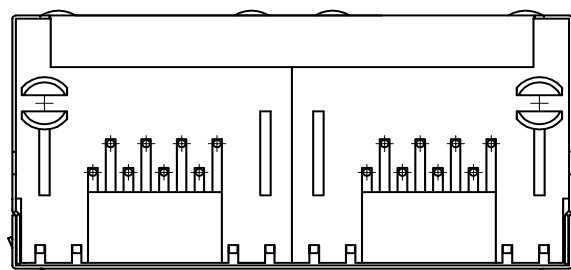
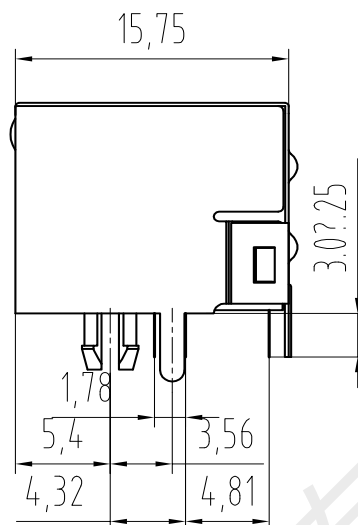
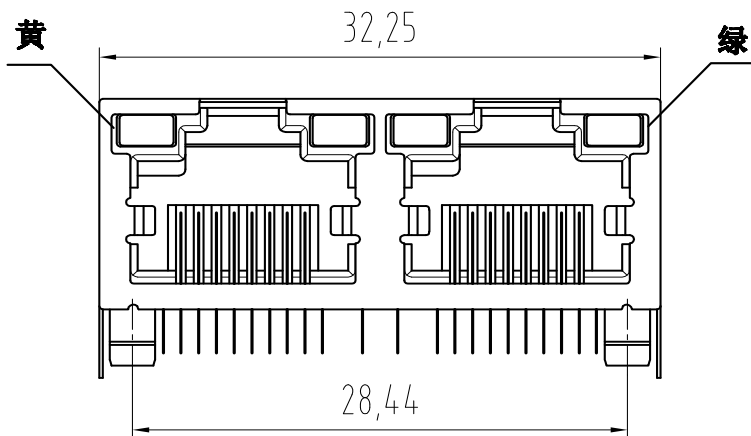


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PC Board Layout
Component Side Shown

Specifications:

1. Electrical:

Rated voltage and current: 125V 1.5A Max

Contact resistance: 50mΩ Max Initial state
100mΩ Max End state

Insulation resistance: 500MΩ Min

Withstand Voltage: AC 1000V

2. Mechanical:

Durability: 750 Cycles 20-30Cycles/Min

3. Material:

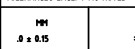
Shell: Glass Filled Polyester UL94V-0

Contact: Phosphor Bronze

Plating: Gold Plating Fu "50u" Over Nickel

4. Temperature Range: -25°C to +85°C.

5. Welding temperature: 260±5°C 3±0.5S

DETAILED LISTS			DFTD 黄彬	DATE 2020.5.18	 深圳市韩下电子有限公司			
	TOLERANCES EXCEPT AS NOTED		CHKD 张伟	DATE 2020.5.18				
			MFO	DATE	TITLE			
			APPVL 罗小睿	DATE 2020.5.18				
			ANGLES ± 0.5		MATERIAL :		HX-RJ45 90 5631-1x2 LED	
			Q.T'Y :		DRAWING NO. TESJA1006			
			FINISH :		/PART NO.			
THIRD ANGLE PROJECTION		SCALE :		DO NOT SCALE DRAWING		SHEET X OF Y		