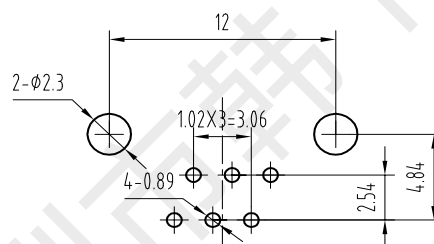
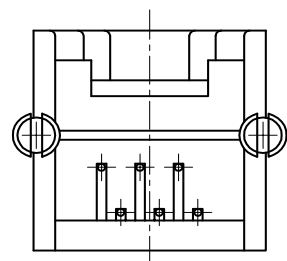
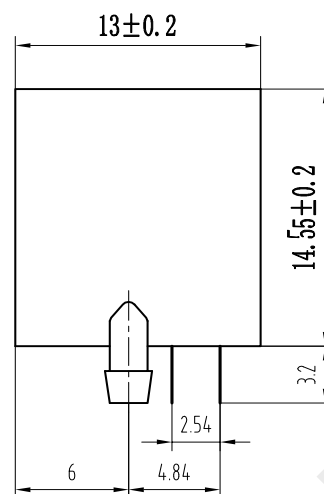
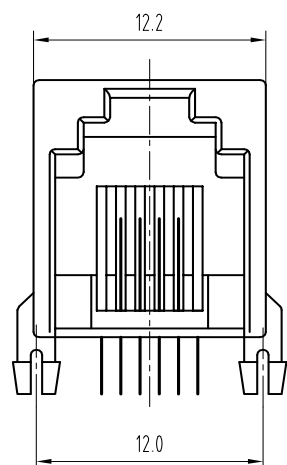


REVISION RECORD				
REV	ECO	DESCRIPTION	DFT	CHKD



PC Board Layout
Component Side Shown

Specifications:

1. Electrical:

Rated voltage and current: 125V 1.5A Max
Contact resistance: 50mΩ Max Initial state
100mΩ Max End state

Insulation resistance: 500MΩ Min

Withstand Voltage: AC 1000V

2. Mechanical:

Durability: 750 Cycles 20-30Cycles/Min

3. Material:

Shell: Glass Filled Polyester UL94V-0

Contact: Phosphor Bronze

Plating: Gold Plating Fu " 50u " Over Nickel

4. Temperature Range: -25°C to +85°C.

5. Welding temperature: 260±5°C 3±0.5S

DETAILED LISTS	MM (INCH)		DFTO 黄彬	DATE 2017.05.03	 深圳市韩下电子有限公司	
	TOLERANCES EXCEPT AS NOTED		CHKD 张伟	DATE 2017.05.03		
	MM J0 ± 0.15 J00 ± 0.075 J000 ± 0.05	± ± ±	MFO	DATE		
			APPVL 罗小馨	DATE 2017.05.03		
			MATERIAL :			
	ANGLES ± 0.5		QTY :		TITLE HX-RJ11 90 5513-1x1 6P6C S	
			FINISH :		DRAWING NO. TESSA1001	
			SCALE :		/PART NO.	
			DO NOT SCALE DRAWING		SIZE A3	
	THIRD ANGLE PROJECTION				REV 0	
				SHEET X OF Y		