



S5A THRU S5M

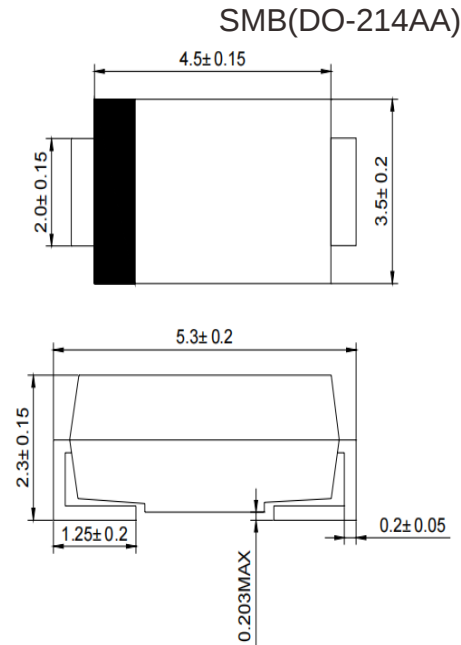
Surface Mount Standard Rectifiers

Features

- Low profile package
- Ideal for automated placement
- Glass passivated chip junctions
- Fast switching for high efficiency
- High forward surge capability
- High temperature soldering:
260°C/10 seconds at terminals
- Component in accordance to
RoHS 2002/95/1 and WEEE 2002/96/EC

Mechanical Data

- **Case:** JEDEC DO-214AA molded plastic body over glass passivated chip
- **Terminals:** Solder plated, solderable per
J-STD-002B and JESD22-B102D
- **Polarity:** Laser band denotes cathode end



Dimensions in millimeters

Maximum Ratings and Electrical Characteristics

Rating at 25 °C

ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%

Parameter	Symbols	S5A	S5B	S5D	S5G	S5J	S5K	S5M	Units
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current	$I_{F(AV)}$	5							A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I_{FSM}	150							A
Maximum Instantaneous Forward Voltage at 5 A	V_F	1.1							V
Maximum DC Reverse Current $T_a = 25\text{ }^{\circ}\text{C}$ at Rated DC Blocking Voltage $T_a = 125\text{ }^{\circ}\text{C}$	I_R	5 100							μA
Typical Junction Capacitance ⁽¹⁾	C_j	40							pF
Typical Thermal Resistance ⁽²⁾	$R_{\theta JA}$ $R_{\theta JC}$	48 16							$^{\circ}\text{C/W}$
Operating and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150							$^{\circ}\text{C}$

(1) Measured at 1 MHz and applied reverse voltage of 4 V D.C

(2) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



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Characteristic Curves ($T_A=25^\circ\text{C}$ unless otherwise noted)

Fig.1 Forward Current Derating Curve

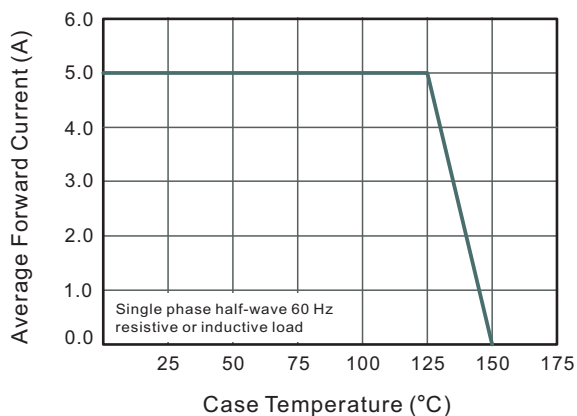


Fig.2 Typical Instantaneous Reverse Characteristics

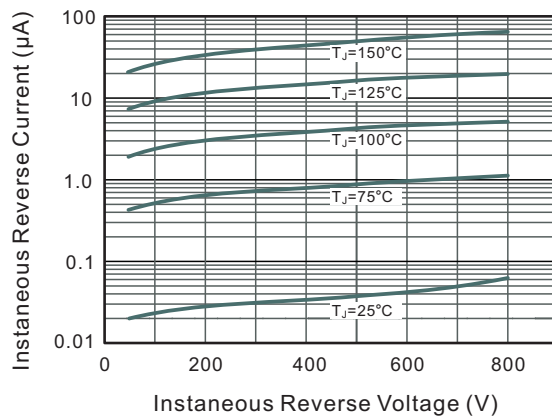


Fig.3 Typical Forward Characteristic

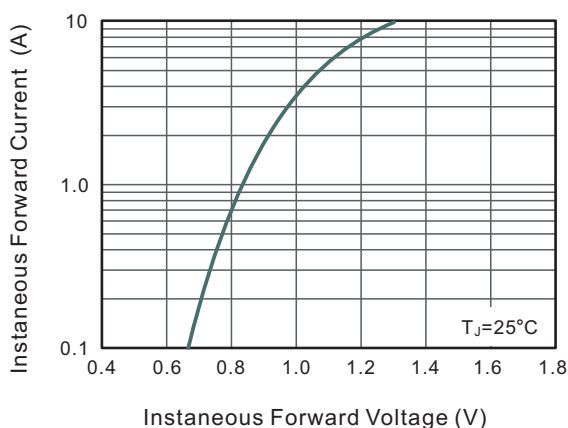


Fig.4 Typical Junction Capacitance

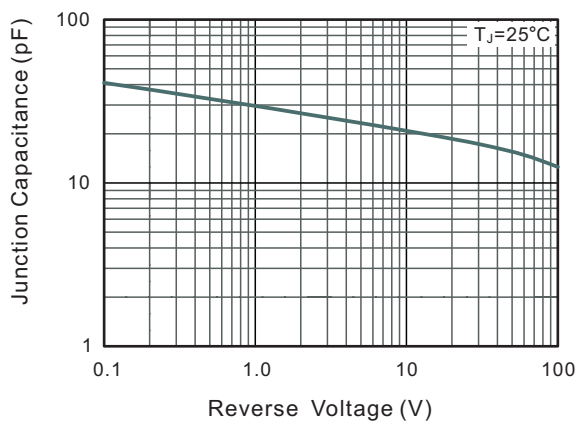


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

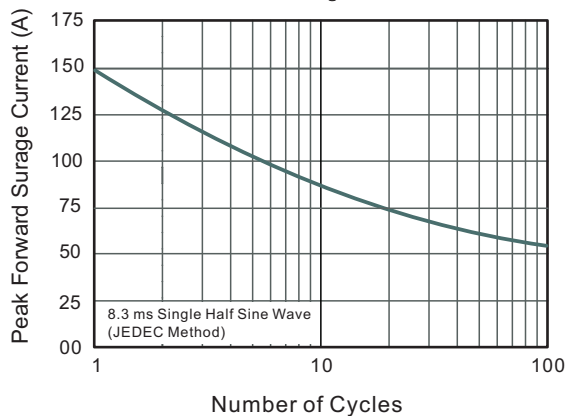


FIG.6 MOUNTING PAD LAYOUT

