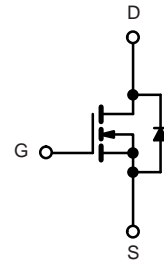
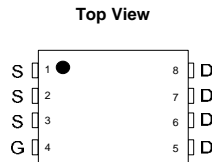
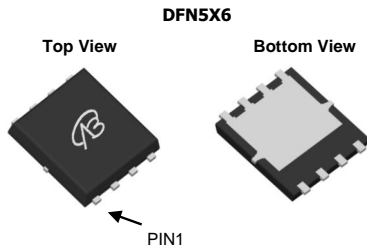


## SiR638ADP-VB Datasheet N-Channel 40V (D-S) MOSFET

PRODUCT SUMMARY		
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>
40	0.0008 at V <sub>GS</sub> = 10 V	250
	0.0009 at V <sub>GS</sub> = 7.5 V	235

### FEATURES

- 175 °C Junction Temperature
- SGT technology Power MOSFET
- Material categorization:



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)				
Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V <sub>GS</sub>	±20	V
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 25 °C	I <sub>D</sub>	250	A
	T <sub>C</sub> = 100 °C		150 <sup>a</sup>	
Pulsed Drain Current		I <sub>DM</sub>	750	
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	250 <sup>a</sup>	
Avalanche Current		I <sub>AS</sub>	125	
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E <sub>AS</sub>	134	mJ
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	136	W
	T <sub>A</sub> = 25 °C		3 <sup>b</sup> , 8.3 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 sec	R <sub>thJA</sub>	15	18	°C/W
	Steady State		40	50	
Maximum Junction-to-Case		R <sub>thJC</sub>	0.85	1.1	

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t ≤ 10 s.

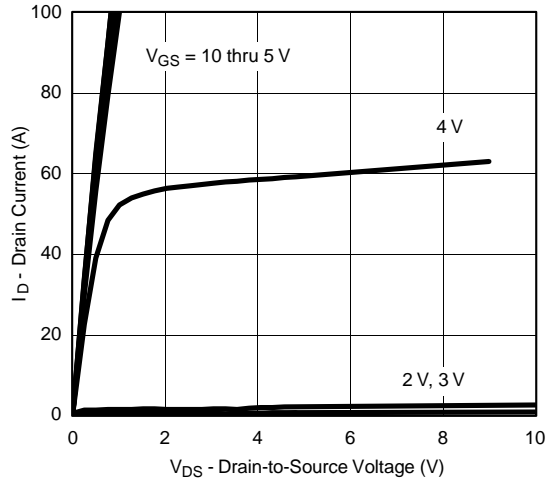
<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	40			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$		2.5		
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			50	
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 175\text{ }^\circ\text{C}$			250	
On-State Drain Current <sup>b</sup>	$I_{D(on)}$	$V_{DS} = 5\text{ V}, V_{GS} = 10\text{ V}$	60			A
Drain-Source On-State Resistance <sup>b</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 40\text{ A}$		0.0008		$\Omega$
		$V_{GS} = 10\text{ V}, I_D = 30\text{ A}, T_J = 125\text{ }^\circ\text{C}$		0.0012		
		$V_{GS} = 10\text{ V}, I_D = 30\text{ A}, T_J = 175\text{ }^\circ\text{C}$		0.0020		
		$V_{GS} = 7.5\text{ V}, I_D = 30\text{ A}$		0.0009		
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 20\text{ A}$		60		S
<b>Dynamic</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 40\text{ V}, f = 1\text{ MHz}$		8800		pF
Output Capacitance	$C_{oss}$			470		
Reverse Transfer Capacitance	$C_{rss}$			225		
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = 40\text{ V}, V_{GS} = 10\text{ V}, I_D = 50\text{ A}$		96	70	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			17		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			15		
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = 40\text{ V}, R_L = 0.6\text{ }\Omega$ $I_D \cong 50\text{ A}, V_{GEN} = 10\text{ V}, R_g = 2.5\text{ }\Omega$		20	23	ns
Rise Time <sup>c</sup>	$t_r$			15	25	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			35	50	
Fall Time <sup>c</sup>	$t_f$			20	30	
<b>Source-Drain Diode Ratings and Characteristics</b> ( $T_C = 25\text{ }^\circ\text{C}$ )						
Pulsed Current	$I_{SM}$				750	A
Diode Forward Voltage	$V_{SD}$	$I_F = 20\text{ A}, V_{GS} = 0\text{ V}$		1	1.5	V
Reverse Recovery Time	$t_{rr}$	$I_F = 20\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		4	135	ns

## Notes:

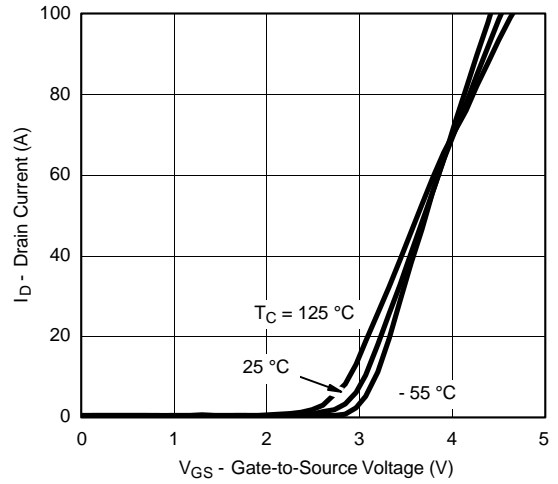
- a. For design aid only; not subject to production testing.  
 b. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

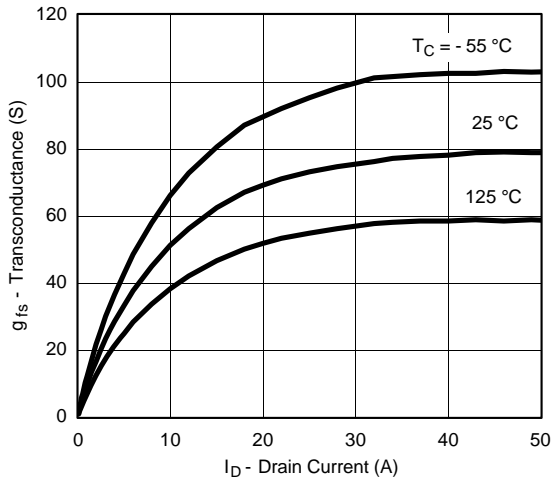
**TYPICAL CHARACTERISTICS** (25 °C unless noted)



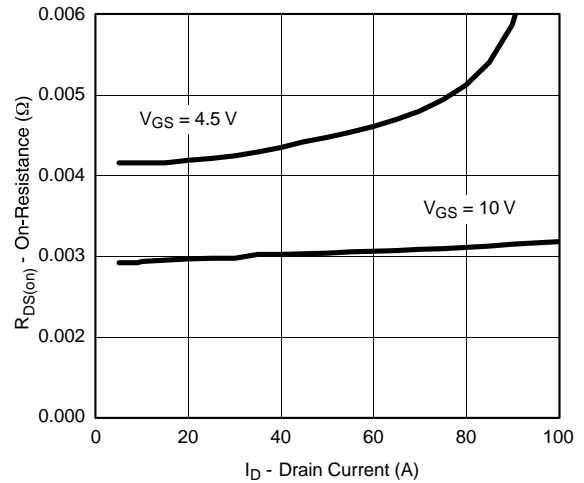
**Output Characteristics**



**Transfer Characteristics**



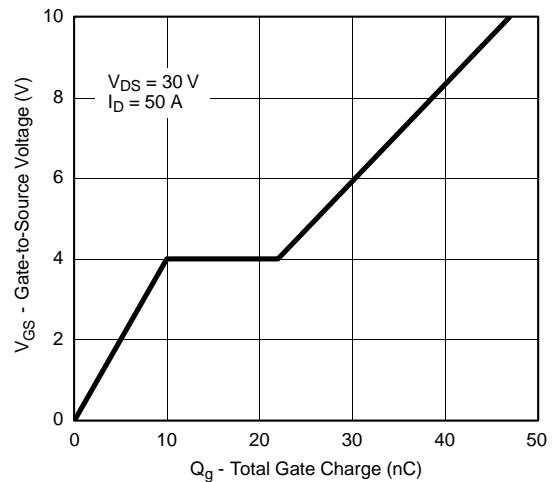
**Transconductance**



**On-Resistance vs. Drain Current**

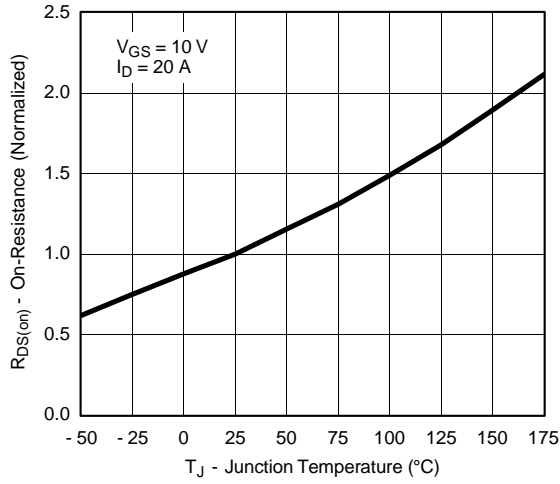


**Capacitance**

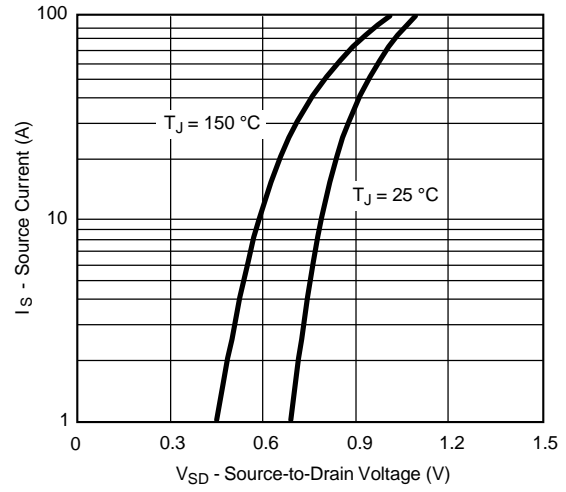


**Gate Charge**

**TYPICAL CHARACTERISTICS** (25 °C unless noted)

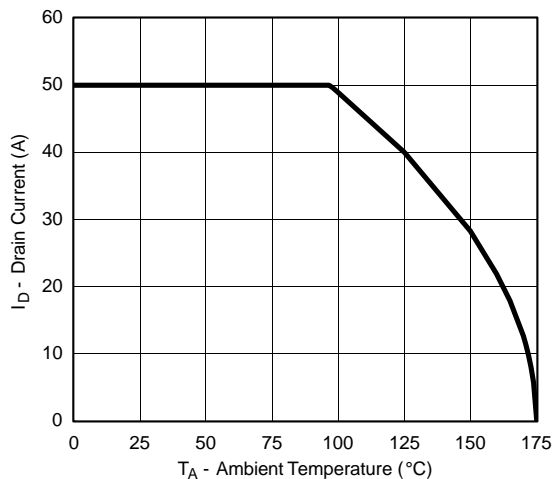


**On-Resistance vs. Junction Temperature**

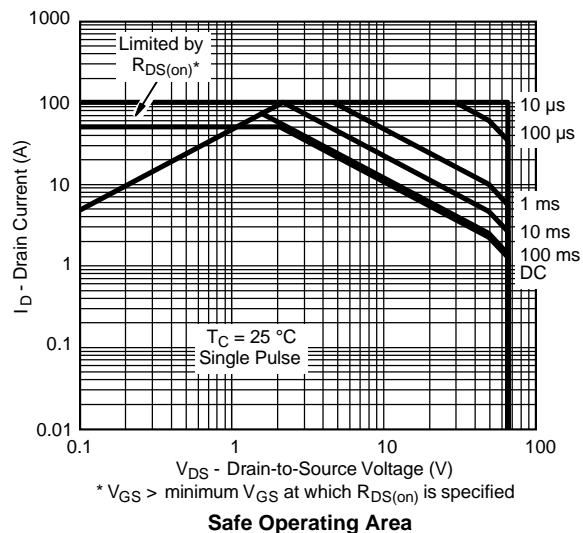


**Source-Drain Diode Forward Voltage**

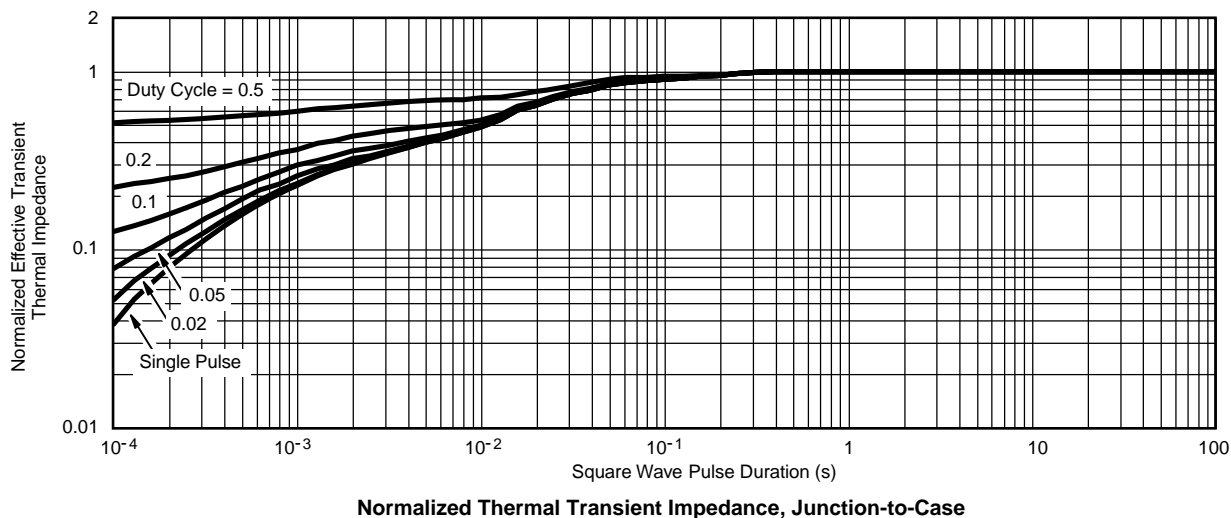
**THERMAL RATINGS**



Maximum Drain Current vs. Ambient Temperature

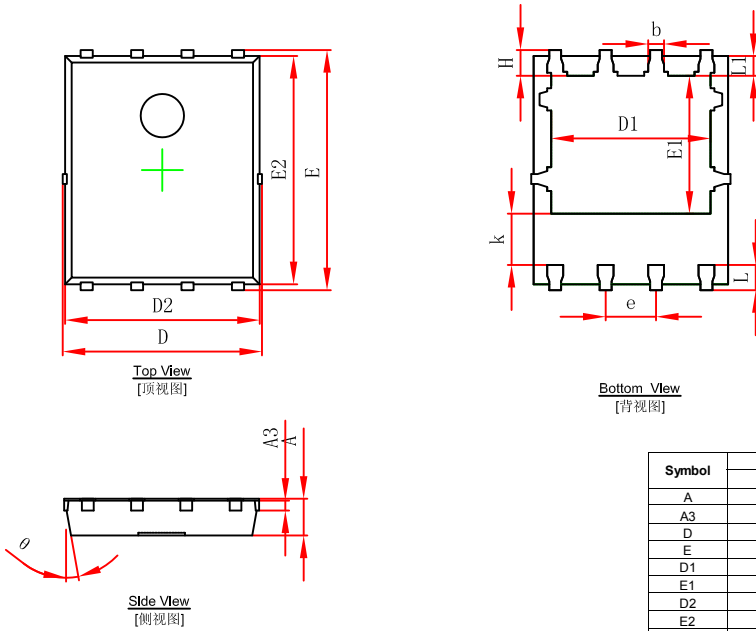


Safe Operating Area



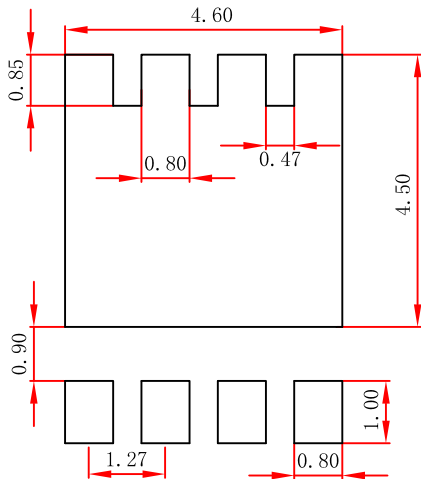
Normalized Thermal Transient Impedance, Junction-to-Case

PDFNWB5x6-8L Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
theta	10°	12°	10°	12°

PDFNWB5x6-8L Suggested Pad Layout



Note:  
 1. Controlling dimension: in millimeters.  
 2. General tolerance: ±0.05mm.  
 3. The pad layout is for reference purposes only.

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