

General Description

This product family offers state of the art performance. It is designed for high frequency applications where high efficiency and high reliability are required.

Features

- Low conduction loss due to low V _F
- Extremely low switching loss by tiny Q_C
- Highly rugged due to better surge current
- Industrial standard quality and reliability

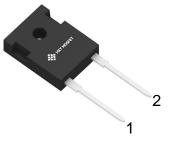
Applications

- UPS
- Power Inverter
- High performance SMPS
- Power factor correction









TO-247-2L



Maximum Ratings

Symbol	Parameter	Value	Unit	Test Conditions
V_{RRM}	Repetitive Peak Reverse Voltage	650	V	
V _{RSM}	Surge Peak Reverse Voltage	650	V	
V_R	DC Peak Reverse Voltage	650	V	
I _F	Continuous Forward Current	26	А	T _c =135°C
I _{FRM}	Repetitive Peak Forward Surge Current	102 63	А	T _c =25°C, t _p =10 ms, Half Sine Wave, D=1 T _c =110°C, t _p =10 ms, Half Sine Wave, D=1
I _{FSM}	Non-Repetitive Peak Forward Surge Current	150 120	А	T _c =25°C, t _p =10ms, Half Sine Wave, D=1 T _c =110°C, t _p =10 ms, Half Sine Wave, D=1
P _{tot}	Power Dissipation	150 65	w	T _c =25°C T _c =110°C
∫i ² dt	i ² dt value	112 72	A ² s	T _C = 25°C,t _p =10ms,Half Sine Pulse T _C = 110°C,t _p =10ms,Half Sine Pulse
T _J	Operating Junction Range	-55 to +175	°C	
T_{stg}	Storage Temperature Range	-55 to +150	°C	

Electrical Characteristics

Parameter	Symbol	Value			Unit	Test Condition
raiailletei		min.	typ.	max.	Oilit	Test Condition
	V _F					I _F =20A
Forward Voltage		-	1.35	1.5	V	T _j =25°C
		-	1.7	1.8		T _j =175°C
					μΑ	V _R =650V
Reverse Current	I_R	-	2	40		T _j =25°C
		-	10	100		T _j =175°C
	e Q _C	-	52	-	nC	V _R =400V,T _j =25℃
Total Capacitive Charge						$V_{R}=400V, T_{j}=25^{\circ}C$ $Q_{C}=\int_{0}^{V_{R}}C(V)dV$
	С				pF	T _j =25℃, f=1MHz
Total Canasitanas		-	1018	-		V _R =0V
Total Capacitance		-	104	-		V _R =200V
		-	89	-		V _R =400V

Thermal Characteristics

Symbol	Parameter	Тур.	Unit
R _{eJC}	Thermal Resistance from Junction to Case	1.00	°C/W

Typical Performance

Fig 1: Forward Characteristics

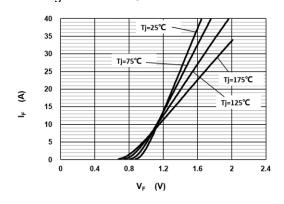
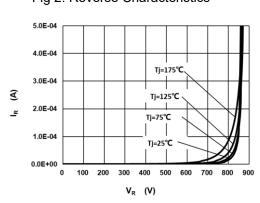
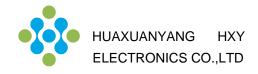


Fig 2: Reverse Characteristics





10% Duty

160 140 120 IF(peak) (A) 100 80 40 20 150 175 T_C (°C)

Fig 3: Current Derating

Fig 5: Capacitance vs. Reverse Voltage

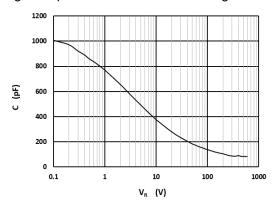


Fig 7: Typical Capacitance Stored Energy

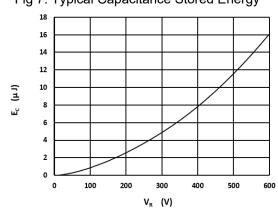


Fig 4: Power Derating

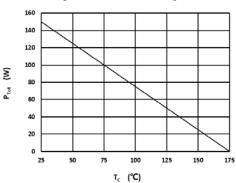


Fig 6: Reverse Charge vs. Reverse Voltage

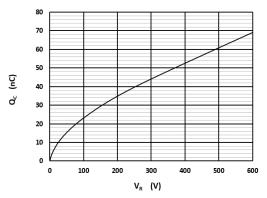
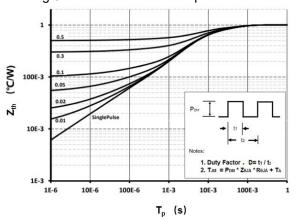
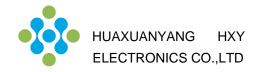


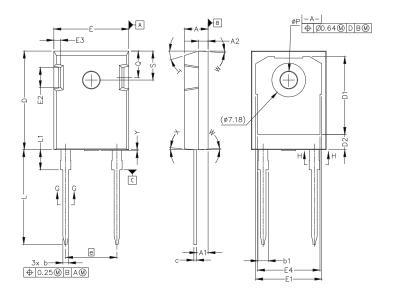
Fig 8: Transient Thermal Impandance





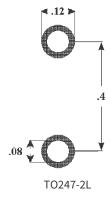
Package Dimensions

Package: TO247-2L All dimensions in mm.

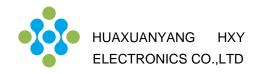


0.44	MILLIM	ETERS	INCHES		
SYM	MIN	MAX	MIN	MAX	
A	4.83	5.21	.190	.205	
A1	2.29	2.54	.090	.100	
A2	1.91	2.16	.075	.085	
b'	1.07	1.28	.042	.050	
b	1.07	1.33	.042	.052	
b1	1.91	2.41	.075	.095	
b2	1.91	2.16	.075	.085	
c'	0.55	0.65	.022	.026	
c	0.55	0.68	.022	.027	
D	20.80	21.10	.819	.831	
D1	16.25	17.35	.640	.683	
D2	2.86	3.16	.112	.124	
E	15.75	16.13	.620	.635	
E1	13.10	14.15	.516	.557	
E2	3.68	5.10	.145	.201	
E3	1.00	1.90	.039	.075	
E4	12.38	13.43	.487	.529	
e	10.88 BSC		.428 BSC		
L	19.81	20.32	.780	.800	
L1	4.10	4.40	.161	.173	
φP	3.51	3.65	.138	.144	
Q	5.49	6.00	.216	.236	
S	6.04	6.30	.238	.248	
T	17.5° REF.				
W	3.5° REF.				
X	4° REF.				
Y	0	0.50	0	0.020	

Recommended Solder Pad Layout



all units are in inches



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