

# **P-Channel Enhancement Mode MOSFET**

### **Features**

• -25V/-4.2A,  $R_{DS(ON)}=130\text{m}\Omega(\text{Max})$ @ $V_{GS}=-10\text{V}$   $R_{DS(ON)}=150\text{m}\Omega(\text{Max})$ @ $V_{GS}=-4.5\text{V}$  $R_{DS(ON)}=180\text{m}\Omega(\text{Max})$ @ $V_{GS}=-2.5\text{V}$ 

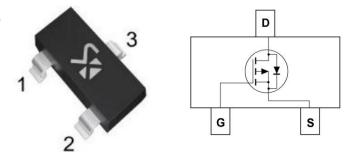
$BV_{DSS}$ , $T_A = 25$ °C	$R_{DS(ON)}$ , max@-4.5V	$I_D,T_A=25$ °C
-25V	$150 \mathrm{m}\Omega$	-4.2A

- Super high dense cell design for extremely low R<sub>DS(ON)</sub>
- Reliable and rugged
- SC-59 for surface mount package

# **Marking:AFKA**

## **Applications**

- Power Management
- Portable Equipment and Battery Powered Systems



**SC-59** 

## **Absolute Maximum Ratings**(T<sub>A</sub>=25°C,unless otherwise noted.)

Parameter	Symbol	Limit	Unit
Drain-source Voltage	$V_{ m DS}$	-25	V
Gate-source Voltage	$V_{GS}$	±12	V
Drain Current-Continuous	$I_D$	-4.2	A
Maximum Junction Temperature	$T_{J}$	150	°C
Storage Temperature Range	$T_{STG}$	-55~+150	°C

## Electrical Characteristics (T<sub>A</sub>=25°C, unless otherwise noted.)

Parameter	Symbol	Condition	Min	Тур	Max	Unit				
Off Characteristics										
Drain-source breakdown voltage	$BV_{DSS}$	$V_{GS}=0V,I_{D}=-250\mu A$	-25			V				
Drain sauraa laakaga aurrant	$I_{ m DSS}$	$V_{DS}$ =-25 $V_{,}V_{GS}$ =0 $V$			-1					
Drain-source leakage current		T <sub>J</sub> =85°C			-30	μΑ				
Gate-source leakage current	$I_{GSS}$	V <sub>DS</sub> =0V,V <sub>GS</sub> =±12V			±100	nA				
On Characteristics <sup>(3)</sup>										
Gate Threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$	-0.7		-1.3	V				
	R <sub>DS(on)</sub>	$V_{GS}$ =-10V, $I_{D}$ =-4.2A			130	mΩ				
Prain-source on resistance		$V_{GS}$ =-4.5V, $I_{D}$ =-4.0A			150					
		$V_{GS}$ =-2.5V, $I_{D}$ =-1.0A			180					
Drain-Source Diode Characteristics										
Diode Forward Voltage	$V_{\mathrm{SD}}$	$V_{GS}=0V,I_{D}=-1.0A$			-1.0	V				

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# **Typical Characteristics**

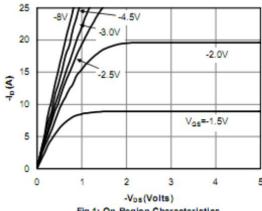


Fig 1: On-Region Characteristics

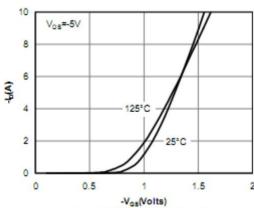


Figure 2: Transfer Characteristics

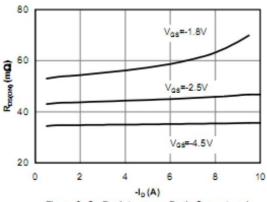


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

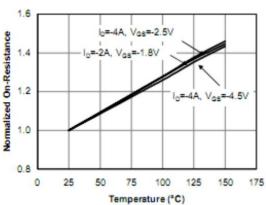


Figure 4: On-Resistance vs. Junction Temperature

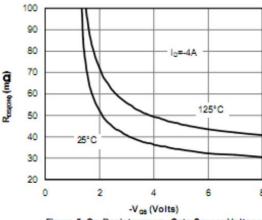


Figure 5: On-Resistance vs. Gate-Source Voltage

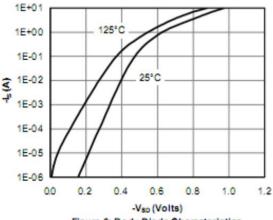


Figure 6: Body-Diode Characteristics

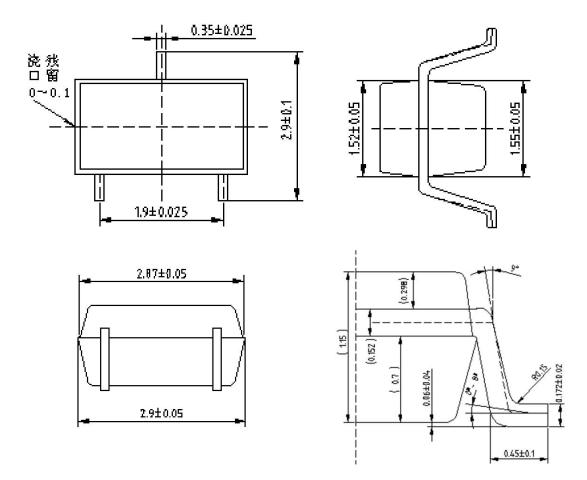
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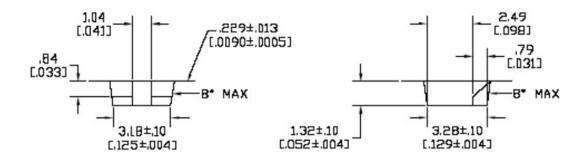
# **Package Information**

## **SC-59**

## **Dimensions in mm**



# SC-59 Carrier Tape



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