

PNP Silicon Epitaxial Planar Transistor

Features

- For switching and amplifier applications
- Peculiarly suitable for AF-driver stages and low power Output stages

Marking:2TY

SOT-23



- 1. BASE
- 2. EMITTER
- 3. COLLECTOR

Maximum Ratings(T_A=25°C,unless otherwise noted.)

Parameter	Symbol	Limit	Unit	
Collector-Base Voltage	V_{CBO}	-50	V	
Collector-Emitter Voltage	$ m V_{CEO}$	-30	V V mA	
Emitter-Base Voltage	V_{EBO}	-8		
Collector Current Continuous	I_{C}	-500		
Collector Power Dissipation	Pc	300	mW	
Junction Temperature	TJ	150	°C	
Storage Temperature	T_{STG}	-55~+150	°C	

Electrical Characteristics(T_A=25°C,unless otherwise noted.)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I_{C} =-0.05mA, I_{E} =0	-50			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =-1mA,I _B =0	-30			V
Emitter-base breakdown voltage	V _{(BR)EBO}	-8			V	
Collector cut-off current	I_{CBO}	$V_{CB} = -35V, I_{E} = 0$			-0.1	μΑ
Emitter cut-off current	I _{EBO}	V _{EB} =-4V,I _C =0			-0.1	μΑ
DC current gain	hFE	V _{CE} =-1V,I _C =-50mA	200		300	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =-500mA,I _B =-50mA			-0.6	V
Base-emitter saturation voltage	V _{BE(sat)}	I _C =-500mA,I _B =-50mA			-1.2	V
Transition frequency	f_T	V _{CE} =-6V,I _C =-20mA, f=30MHz	150			MHz

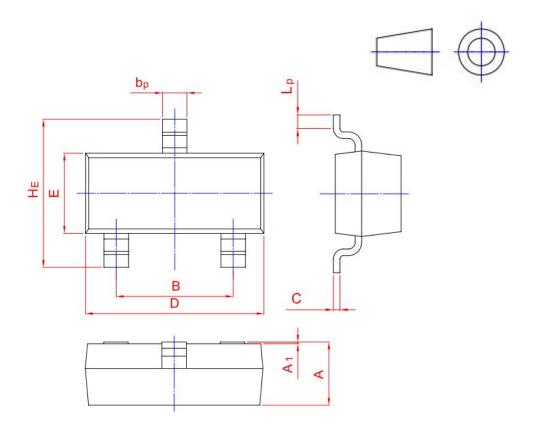
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Package Information

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Dimensions in mm



Uint	А	В	b _p	С	D	E	H _E	A ₁	Lp
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20

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