MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

AON6380-MS

Product specification





Description

The AON6380-MS uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V.

This device is suitable for use as a Battery protection or in other Switching application

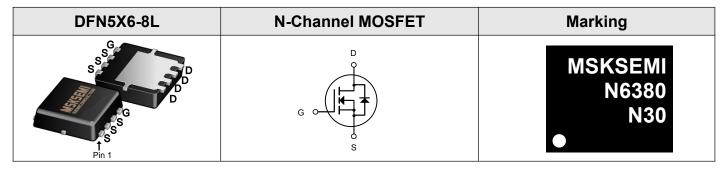
Features

- VDS = 30V ID=80A
- RDS(ON) < 6mΩ VGS=10V

Application

- Battery protection
- Load switch
- Uninterruptible power supply

Reference News



Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|-----------------------------|---|--------|------------|
| Vos | Drain-Source Voltage | 30 | V |
| Vgs | Gate-Source Voltage | ±20 | V |
| b@Tc=25℃ | Continuous Drain Current, Ves @ 10V | 80 | А |
| l o@Tc=70℃ | Continuous Drain Current, Vos @ 10V | 45 | Α |
| Ірм | Pulsed Drain Current ² | 280 | Α |
| EAS | Single Pulse Avalanche Energy ³ | 56 | mJ |
| P o@T c =25 ℃ | Total Power Dissipation⁴ | 37 | W |
| Тѕтс | Storage Temperature Range -55 to 15 | | $^{\circ}$ |
| TJ | Operating Junction Temperature Range -55 to 150 | | $^{\circ}$ |
| Rеja | Thermal Resistance Junction-Ambient ¹ 30 | | °C/W |



Electrical Characteristics @T_i=25°C(unless otherwise specified)

| Symbol | Parameter | Condition | Min. | Тур. | Max. | Unit |
|-----------------|--|----------------------------|------|------|----------|------|
| Static Elec | ctrical Characteristics @ T _j =25°C (unless o | therwise stated) | | | | |
| V(BR)DSS | Drain-Source Breakdown Voltage | VGS=0V ID=250µA | 30 | | | V |
| Inoc | Zero Gate Voltage Drain Current | VDS=30V,VGS=0V | | | 0.1 | μΑ |
| loss | Zero Gate Voltage Drain Current(Tj=125℃) | VDS=30V,VGS=0V | | | 100 | μΑ |
| Igss | Gate-Body Leakage Current | VGS=±20V,VDS=0V | | | ±100 | nA |
| VGS(TH) | Gate Threshold Voltage | VDS=VGS,ID=250µA | 1.0 | 1.7 | 2.5 | V |
| RDS(ON) | Drain-Source On-State Resistance ³ | Vgs=10V, ID=20A | | 4.7 | 6 | mΩ |
| RDS(ON) | Drain-Source On-State Resistance ³ | VGS=4.5V, ID=16A | | 5.4 | 8 | mΩ |
| Dynamic | Electrical Characteristics @ T _j = 25°C | (unless otherwise state | ed) | | | |
| Ciss | Input Capacitance | | | 1930 | | pF |
| Coss | Output Capacitance | VDS=15V,VGS=0 V, f=1MHz | | 310 | | pF |
| Crss | Reverse Transfer Capacitance | | | 260 | | pF |
| Rg | Gate Resistance | f=1MHz | | 0.85 | | |
| Qg | Total Gate Charge | | | 38 | | nC |
| Qgs | Gate-Source Charge | VDS=15V,ID=20A, VGS=10V | | 5.1 | | nC |
| Q _{gd} | Gate-Drain Charge | | | 12 | | nC |
| Switching | g Characteristics | | | | | |
| t d(on) | Turn-on Delay Time | | | 8.5 | | nS |
| t _r | Turn-on Rise Time | VDD=15V, ID=20A, | | 9 | | nS |
| t d(off) | Turn-Off Delay Time | RG=3, VGS=10V | | 31 | | nS |
| t _f | Turn-Off Fall Time | V G S = 1 U V | | 9 | | nS |
| Source- D | Drain Diode Characteristics@ T _j = 25°C | (unless otherwise state | ed) | | <u> </u> | |
| VsD | Forward on voltage | IsD=20A,VGS=0V | | 0.8 | 1.2 | V |
| t _{rr} | Reverse Recovery Time | Tj=25℃,Isd=20A | | 16 | | nS |
| Qrr | Reverse Recovery Charge | , VGS=0V di/dt=500A/µs | | 42 | | nC |

NOTE:

 $^{{\}bf 1.} Repetitive\ rating;\ pulse\ width\ limited\ by\ max.\ junction\ temperature.$

 $^{2 \}text{ Limited by T}_{Jmax}, \text{ starting T}_{J} = 25^{\circ}\text{C}, \text{ L} = 0.5 \text{mH}, \text{Rg} = 25 \text{ , las} = 15 \text{A}, \text{Vgs} = 10 \text{V}. \text{ Part not recommended for use above this value}$

^{3.} Pulse width \leq 300 µs; duty cycle \leq 2%.



Typical Electrical and Thermal Characteristics (Curves)

Normalized On Resistance

D-Drain Current (A)

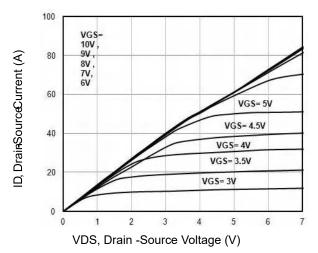


Fig1. Typical Output Characteristics

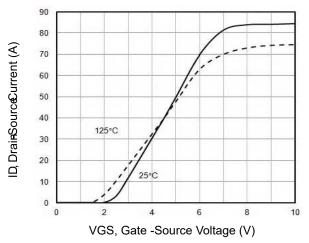


Fig3. Typical Transfer Characteristics

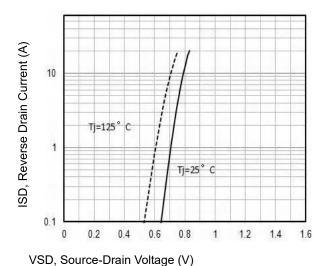


Fig6. Maximum Safe Operating Area Voltage

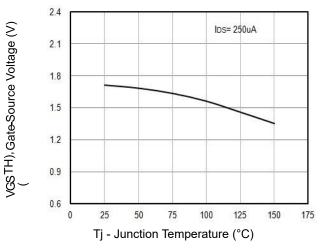


Fig2. V_{GS(TH)} Gate -Source Voltage Vs.Tj

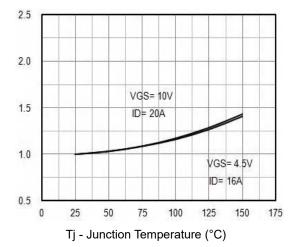


Fig4. Normalized On-Resistance Vs. Tj

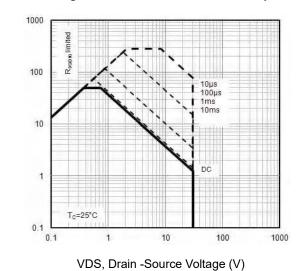


Fig5. Typical Source-Drain Diode Forward



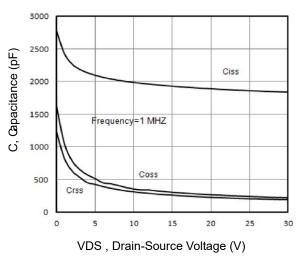


Fig7. Typical Capacitance Vs.Drain-Source Voltage

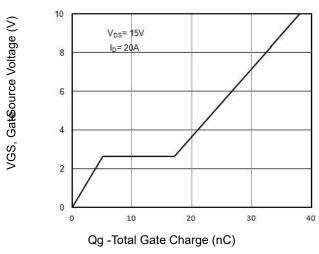


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

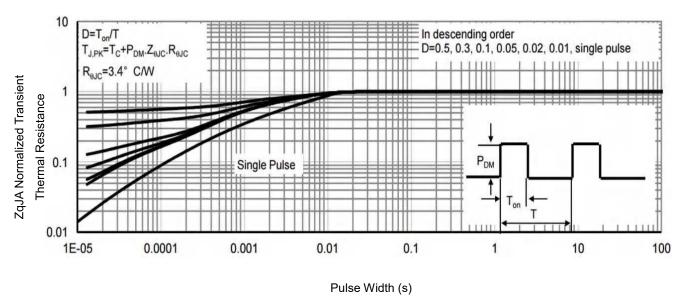


Fig9. Normalized Maximum Transient Thermal Impedance

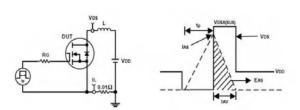


Fig10. Unclamped Inductive Test Circuit and waveforms

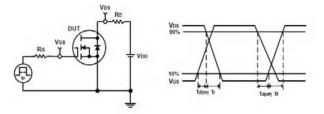
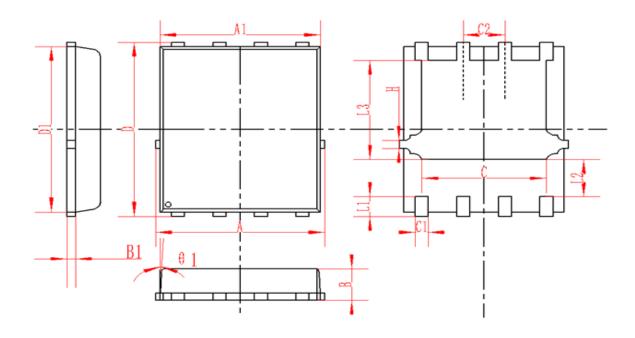


Fig11. Switching Time Test Circuit and waveforms



DFN5X6-8L Package Information



| SYMBOL | | MM | | | INCH | |
|----------|-------|----------|-------|-------|----------|-------|
| STIVIDOL | MIN | NOM | MAX | MIN | NOM | MAX |
| А | 4.95 | 5 | 5.05 | 0.195 | 0.197 | 0.199 |
| A1 | 4.82 | 4.9 | 4.98 | 0.190 | 0.193 | 0.196 |
| D | 5.98 | 6 | 6.02 | 0.235 | 0.236 | 0.237 |
| D1 | 5.67 | 5.75 | 5.83 | 0.223 | 0.226 | 0.230 |
| В | 0.9 | 0.95 | 1 | 0.035 | 0.037 | 0.039 |
| B1 | | 0.254REF | | | 0.010REF | |
| С | 3.95 | 4 | 4.05 | 0.156 | 0.157 | 0.159 |
| C1 | 0.35 | 0.4 | 0.45 | 0.014 | 0.016 | 0.018 |
| C2 | | 1.27TYP | | | 0.5TYP | |
| θ1 | 8° | 10° | 12° | 8° | 10° | 12° |
| L1 | 0.63 | 0.64 | 0.65 | 0.025 | 0.025 | 0.026 |
| L2 | 1.2 | 1.3 | 1.4 | 0.047 | 0.051 | 0.055 |
| L3 | 3.415 | 3.42 | 3.425 | 0.134 | 0.135 | 0.135 |
| Н | 0.24 | 0.25 | 0.26 | 0.009 | 0.010 | 0.010 |

REEL SPECIFICATION

| P/N | PKG | QTY |
|------------|-----------|------|
| AON6380-MS | DFN5X6-8L | 5000 |



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