# MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

AON6226-MS
Product specification





## **Description**

The AON6226-MS use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness and suitable.

### **Features**

- VDS = 100V ID= 75A
- RDS(ON) <  $9.2m\Omega$  VGS= 10V

## **Application**

- Consumer electronic power supply Motor control
- Synchronous-rectification Isolated DC
- Synchronous-rectification applications

#### **Reference News**

DFN5X6-8L	N-Channel MOSFET	Marking
S S S S S S S S S S S S S S S S S S S	G S	MSKSEMI N6226 N100

## Absolute Maximum Ratings at Tj=25 ℃ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	VDS	100	V
Gate source voltage	VGS	±20	V
Continuous drain current <sup>1)</sup>	ID	75	А
Pulsed drain current <sup>2)</sup>	ID, pulse	300	А
Power dissipation <sup>3)</sup>	Po	97	W
Single pulsed avalanche energy <sup>5)</sup>	EAS	90	mJ
Operation and storage temperature	Tstg , Tj	-55 to 150	${\mathbb C}$
Thermal rseistance, junction-case	RθJC	1.3	°C/W



## **Electrical Characteristics** (TJ=25°C unless otherwise specified)

Symbol	Parameter Test Condition		Min.	Тур.	Max.	Units
Off Charac	cteristic					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage V <sub>GS</sub> =0V, I <sub>D</sub> =250µA		100	-	_	V
Ipss	Zero Gate Voltage Drain Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V,	-	-	1.0	μA
Igss	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
On Charac	cteristics			•		
V <sub>GS(th)</sub>	Gate Threshold Voltage V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA		1.0	1.6	2.5	V
	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	7.3	9.2	mΩ
$R_{DS(on)}$	note3	V <sub>GS</sub> =4.5V, I <sub>D</sub> =8A	-	9	13.5	mΩ
Dynamic (	Characteristics					
Ciss	Input Capacitance		_	2046	-	pF
Coss	Output Capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1.0MHz	-	865	-	pF
Crss	Reverse Transfer Capacitance	1-1.0IVIDZ	_	25	-	pF
Qg	Total Gate Charge		-	39.4	-	nC
Qgs	Gate-Source Charge	V <sub>DS</sub> =50V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V	-	5.2	-	nC
$Q_gd$	Gate-Drain("Miller") Charge	VGS-10V	-	9.8	-	nC
Switching	Characteristics					
t <sub>d(on)</sub>	Turn-on Delay Time		_	20	-	ns
tr	Turn-on Rise Time	V <sub>DD</sub> =50V, I <sub>D</sub> =25A,	_	5.2	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =6Ω, V <sub>GS</sub> =10V	-	49	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	12	-	ns
Drain-Sou	rce Diode Characteristics and Max	imum Ratings				
ls	Maximum Continuous Drain to Source Diode Forward Current			-	75	Α
Ism	Maximum Pulsed Drain to Source Diode Forward Current			-	300	Α
VsD	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =30A	-	-	1	V
trr	Body Diode Reverse Recovery Time  Body Diode Reverse Recovery Charge  TJ=25℃, I=12A,dI/dt=100A/µs		-	49	-	ns
Qrr			-	85	-	nC

#### Notes:

- 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



## **Typical Performance Characteristics**

Figure1: Output Characteristics

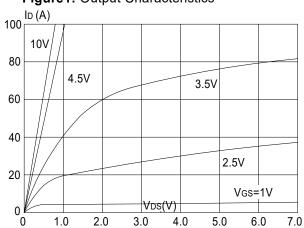


Figure 2: Typical Transfer Characteristics

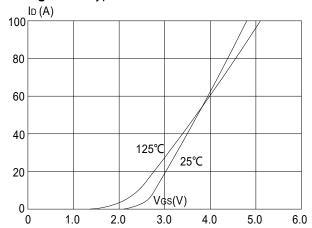


Figure 3:On-resistance vs. Drain Current

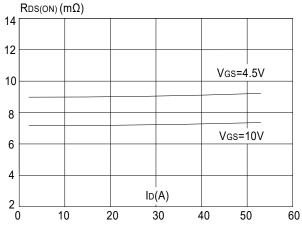


Figure 4: Body Diode Characteristics

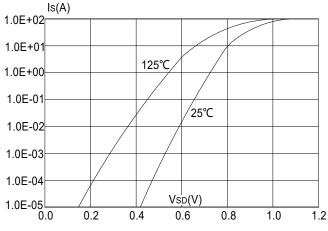


Figure 5: Gate Charge Characteristics

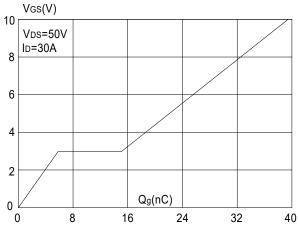
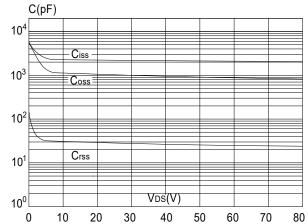


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

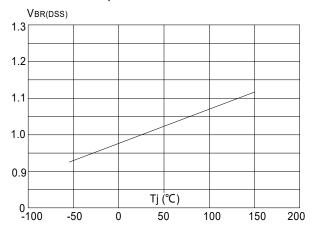
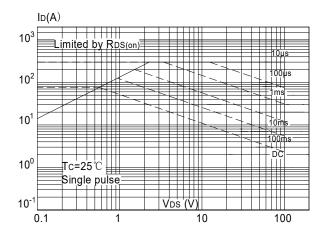
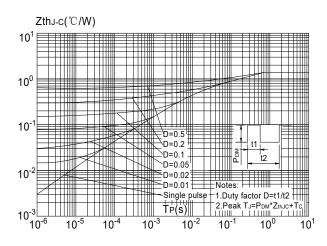


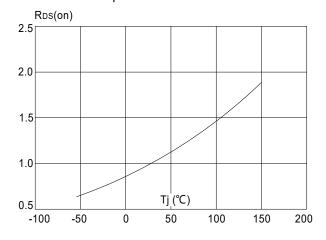
Figure 9: Maximum Safe Operating Area



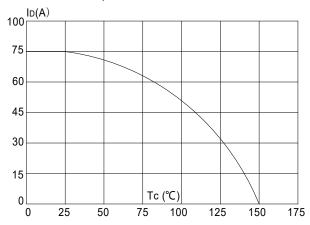
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature





## **Test Circuit**

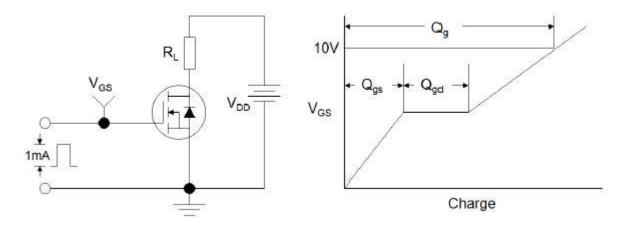


Figure1:Gate Charge Test Circuit & Waveform

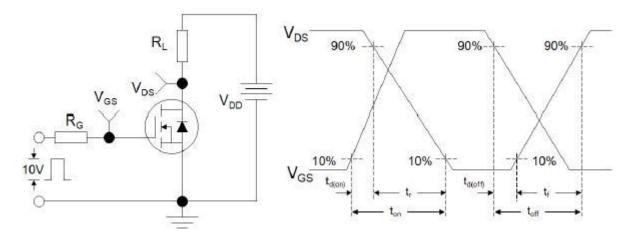


Figure 2: Resistive Switching Test Circuit & Waveforms

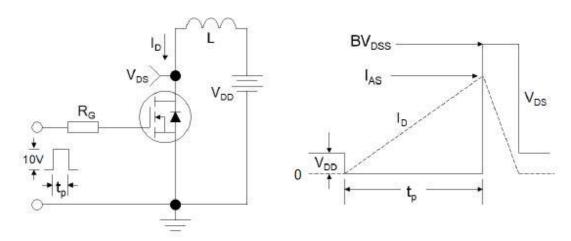
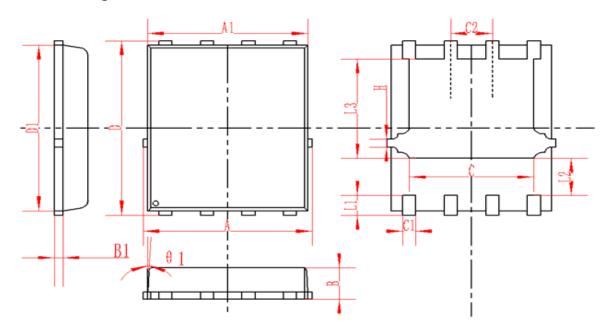


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



# **DFN5X6-8L Package Information**



SYMBOL	MM			INCH		
STIVIDOL	MIN	NOM	MAX	MIN	NOM	MAX
А	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP		0.5TYP			
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010

## **REEL SPECIFICATION**

P/N	PKG	QTY
AON6226-MS	DFN5X6-8L	5000



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