

IGBT Modules

Power Module (V series) 600V / 600A / 2-in-1 package

■ Features

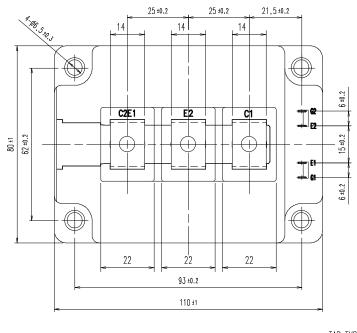
AC-switch
High speed switching
Voltage drive
Low Inductance module structure

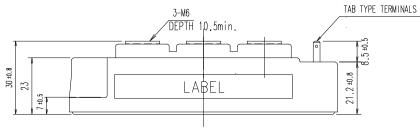
■ Applications

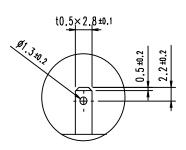
AC-switch for UPS,PCS and etc.

■ Outline drawing (Unit: mm)





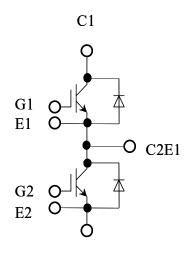




DETAIL TAB TYPE TERMINALS

Weight: 470g (typ.)

■ Equivalent circuit



IGBT Modules

■ Absolute maximum ratings (at T_C= 25°C unless otherwise specified)

Items		Symbols	Conditions	Maximum ratings	Units
Collector-E	mitter voltage	V _{CES}		600	V
Gate-Emitte	er voltage	V _{GES}		±20	V
		I _C	Continuous $\frac{T_{\rm C}=100^{\circ}\text{C}}{T_{\rm C}=25^{\circ}\text{C}}$	600 520	
Collector current		I _C pulse	1ms	1200	Α
				600	1
		-/ c pulse	1ms	1200	1
Collector po	ower dissipation	Pc	1 device	2940	W
Junction temperature		Ti		175	
Operating junction temperature (under switching conditions)		T_{jop}		150	°C
Case temperature		T _c		125	
Storage ten	nperature	T _{stg}		-40 ~ 125	
Isolation voltage	Between terminal and copper base (*1)	V _{iso}	AC: 1min.	2500	VAC
Screw	Mounting	-	M5 or M6	3.0~6.0	N m
torque	Terminals	-	M6	2.5~5.0	IN III

^(*1) All terminals should be connected together during the test.

IGBT Modules

■ Electrical characteristics (at T_j= 25°C unless otherwise specified)

NOTICE:

The external gate resistance ($R_{\rm g}$) shown below is one of our recommend value for the purpose of minimum switching loss. However the optimum $R_{\rm g}$ depends on circuit configuration and/or environment. We recommend that the $R_{\rm g}$ has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

ltomo	Cymhala	Conditions		Characteristics			I I selfe
Items	Symbols			min.	typ.	max.	Units
Zero gate voltage collector current	I _{CES}	V _{GE} =0V,V _{CE} =600V		-	-	2.0	mA
Gate-Emitter leakage current	I _{GES}	V_{CE} =0V, V_{GE} = ± 20 V		-	-	800	nA
Gate-Emitter threshold voltage	$V_{GE(th)}$	V _{CE} =20V,I _C =600mA		6.2	6.7	7.2	V
	V		T _j =25°C	-	1.85	2.40	
	V _{CE(sat)}	$V_{\rm GE}$ =15V, $I_{\rm C}$ =600A	T _j =125°C	-	2.15	-	
Collector-Emitter	(terminal)		T _j =150°C	-	2.35	-	
saturation voltage	1/	<i>T</i> _j =25°C	-	1.60	1.85	V	
	V _{CE(sat)}	V_{GE} =15V, I_{C} =600A	T _i =125°C	-	1.90	-	
	(chip)		T _i =150°C	-	2.10	-	
Internal gate resistance	R _{g(int)}	-		-	1.5	-	Ω
Input capacitance	C ies	V _{CE} =10V, V _{GE} =0V, j	f=1MHz	-	38.8	-	nF
	t on			-	750	-	
Turn-on time	$t_{\rm r}$	$V_{\rm CC}$ =300V, $I_{\rm C}$ =600A, $V_{\rm GE}$ =±15V, $R_{\rm g}$ =2.2 Ω , $T_{\rm j}$ =150°C, $L_{\rm s}$ =30nH		-	400	-	nsec
	t _{r(i)}			-	150	-	
Turn-off time	t off			-	750	-	
Turn-on time	t_{f}			-	70	-	
	V _F		T _i =25°C	-	1.75	2.35	
		$V_{GE} = 0V, I_{F} = 600A$	T _j =125°C	-	1.65	-	
Commend on voltone	(terminal)		T _i =150°C	-	1.62	-	V
Forward on voltage	V _F	V _{GE} =0V,I _F =600A	T _i =25°C	-	1.60	1.85	V
			T _i =125°C	-	1.50	-	
	(chip)		T _j =150°C	-	1.47	-	
Reverse recovery time	t _{rr}	/ _F =600A		-	250	-	nsec

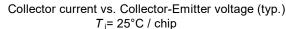
■ Thermal resistance characteristics

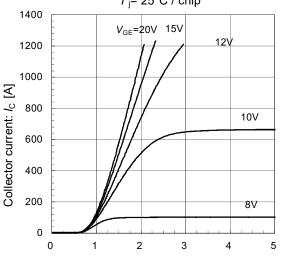
Symbols	Conditions	Ch	Characteristics		
Symbols	Conditions	min.	typ.	max.	Units
D	IGBT	-	-	0.051	
th(j-c)	FWD	-	-	0.088	°C/W
R _{th(c-f)}	with thermal compound	-	0.0125	-	- C/VV
	Symbols R _{th(j-c)} R _{th(c-f)}	R _{th(j-c)} IGBT FWD	Symbols Conditions R th(j-c) IGBT - FWD - FWD	R	R Gooditions min. typ. max.

^(*1) This is the value which is defined mounting on the additional cooling fin with thermal compound.



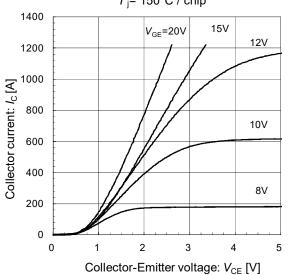
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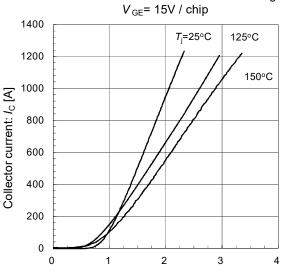


Collector-Emitter voltage: V_{CE} [V]

Collector current vs. Collector-Emitter voltage (typ.) T_i = 150°C / chip

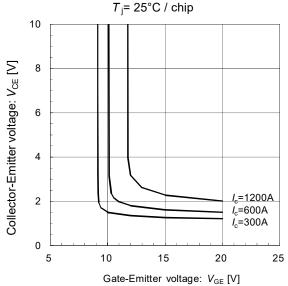


Collector current vs. Collector-Emitter voltage (typ.)

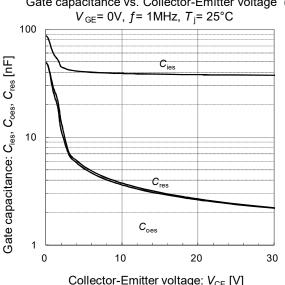


Collector-Emitter voltage: V_{CE} [V]

Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)

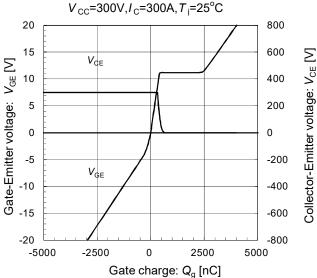


Gate capacitance vs. Collector-Emitter voltage (typ.)



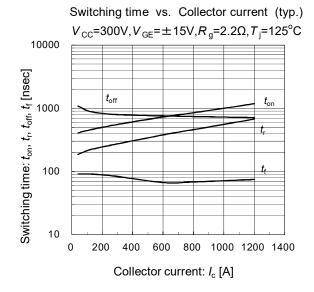
Collector-Emitter voltage: V_{CE} [V]

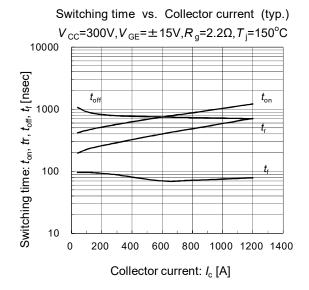
Dynamic gate charge (typ.) $V_{\rm CC}$ =300V, $I_{\rm C}$ =300A, $T_{\rm i}$ =25°C

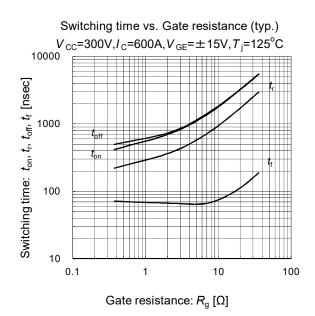


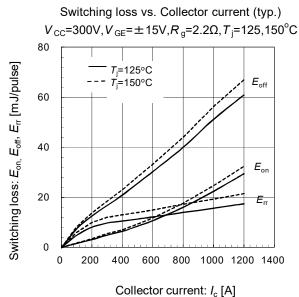


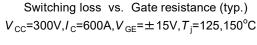
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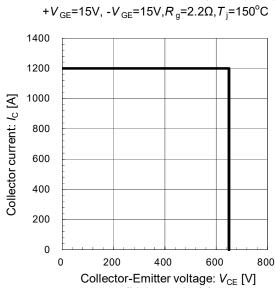




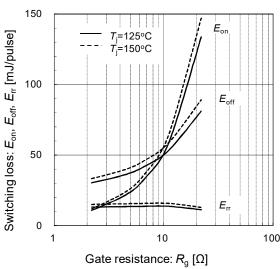






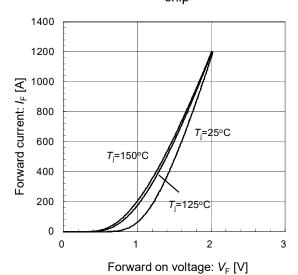


Reverse bias safe operating area (max.)

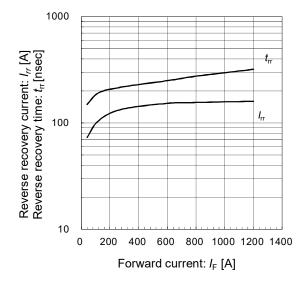


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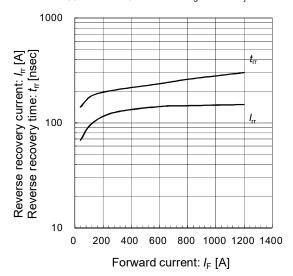
Forward current vs. Forward voltage (typ.) chip



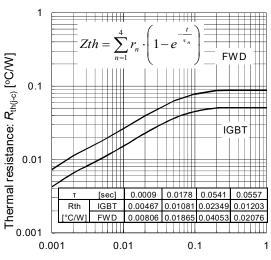
Reverse recovery characteristics (typ.) V_{CC} =300V, V_{GE} =±15V, R_g =2.2 Ω , T_j =150°C



Reverse recovery characteristics (typ.) V_{CC} =300V, V_{GE} =±15V, R_g =2.2 Ω , T_i =125°C



Transient thermal resistance (max.)



Pulse width : Pw [sec]



IGBT Modules

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