



深圳市佑驰电子有限公司

Specification for Approval

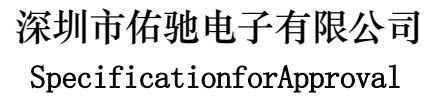
产品名称: 磁胶电感
规格型号: HNRT3015系列
产品编号:
日期: 2023-6-5

1、本承认书的内容更改需经过双方确认，任一方单独的修改均视为无效。

2、本承认书在送达客户后，请给予承认并即签回，如无签回下订单的，我司有权以此承认书标准生产，并表示购买方默认许可。

	审核	制作

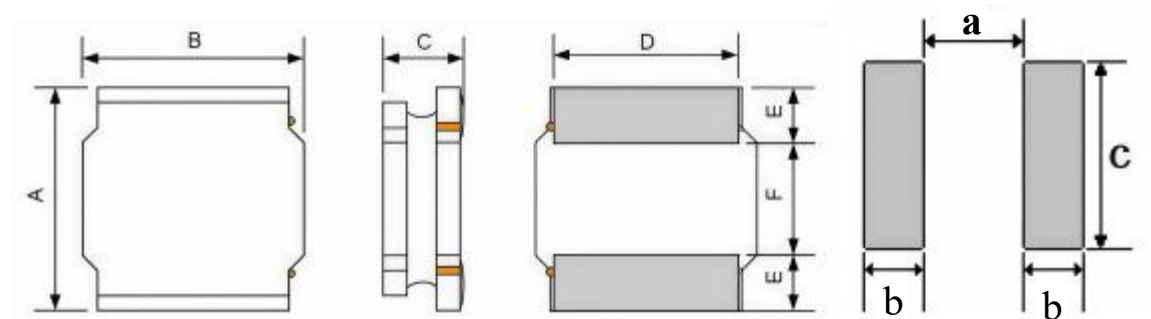
批准	审核	检验



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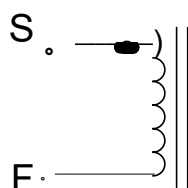
1. SHAPE & DIMENSION (UNIT:mm)



Recommended patterns

A	B	C	D	E	F	a	b	c
3.0±0.2	3.0±0.2	1.5 Max.	2.5±0.2	0.9 Typ.	1.2 Typ.	1.0 Typ.	1.1 Typ.	2.7 Typ.

2. SCHEMATIC



3. PART NUMBERING SYSTEM

HNRT 3015 - 1R0 N
① ② ③ ④

- ① PRODUCT SYMBOL (产品代号: HNRT合金)
- ② DIMENSIONS (规格尺寸)
- ③ Inductance Value: (电感标称值)
- ④ TOLERANCE: (公差) : K±10%; M±20%; N±30%

APPROVED	CHECKED	PREPARED
曾凡强	朱跃红	薛小平

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4. ELECTRICAL CHARACTERISTICS

No.	Part Number	Inductance	DC Resistance		Isat(A)		Irms(A)		Marking
		1MHz/0.1V	Max.	Typ.	Max.	Typ.	Max..	Typ.	
	Units	(uH)	Ω	Ω	A	A	A	A	
1	HNRT3015-R33N	0.33±30%	0.035	0.026	9.00	10.00	4.00	4.50	N/A
2	HNRT3015-1R0N	1.0±30%	0.048	0.041	5.80	7.00	3.30	3.80	N/A
3	HNRT3015-1R5N	1.5±30%	0.072	0.065	4.60	5.50	2.20	2.70	N/A
4	HNRT3015-2R2M	2.2±20%	0.115	0.085	4.00	4.50	2.00	2.30	N/A
5	HNRT3015-3R3M	3.3±20%	0.175	0.144	3.40	4.00	2.00	2.50	N/A
6	HNRT3015-4R7M	4.7±20%	0.215	0.176	3.00	3.30	1.80	2.40	N/A
7	HNRT3015-6R8M	6.8±20%	0.290	0.259	2.00	2.50	1.50	2.00	N/A
8	HNRT3015-100M	10.0±20%	0.460	0.357	1.50	2.00	1.50	2.00	N/A
9	HNRT3015-150M	15.0±20%	0.850	0.695	1.40	1.80	1.00	1.20	N/A
10	HNRT3015-220M	22.0±20%	0.975	0.762	1.15	1.40	0.75	0.90	N/A

Test remarks

- (1). All test data is referenced to 25 °C ambient.
- (2). Isat: DC current at which inductance drops approximate 30% from its value without current.
- (3). Irms: DC current that causes the temperature rise ($\Delta T \leq 40^{\circ}\text{C}$) from 25°C ambient.
- (4). Absolute maximum voltage: DC 25V;

5. GENERAL SPECIFICATION

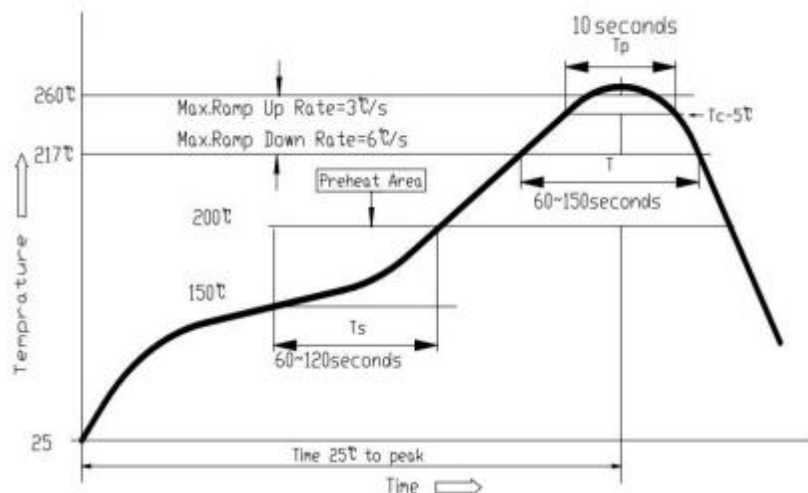
- (1). Operating temp.: -40°C ~ +125°C (Including self-generated heat).
- (2). Storage temp.: -40°C ~ +85°C R.H.:70% Max.
- (3). Recommended products should be used within 12 months from the date of delivery
- (4). The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

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6. SOLDERING CONDITIONS

Figure 1. Re-flow Soldering (Lead Free)



Note:

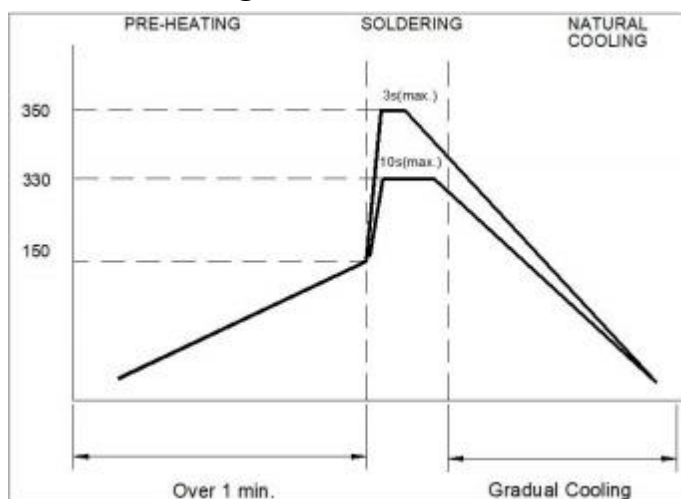
Preheat circuit and products to 150°C

260°C tip temperature (Max)

Reflow times: no more than 2 times

Solder paste thickness: the best 0.08mm is, but max is 0.1mm

Figure 2. Hand Soldering



Note:

Use a 20 watt soldering iron with tip diameter of 1.0mm

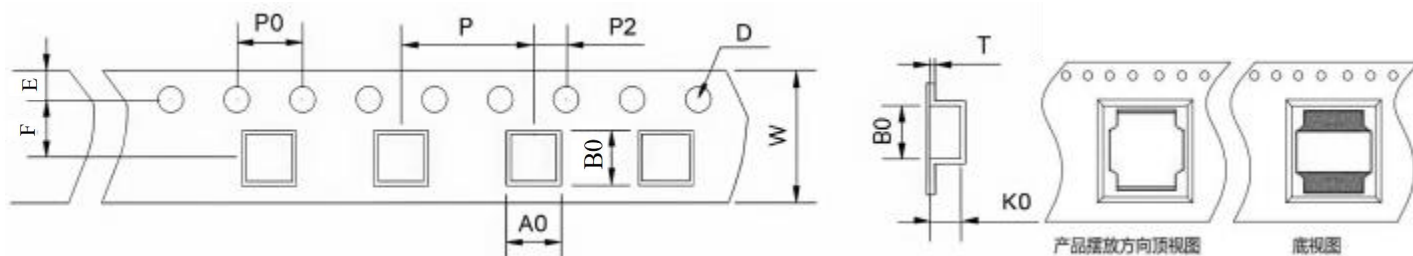
Limit soldering time to 3 sec.

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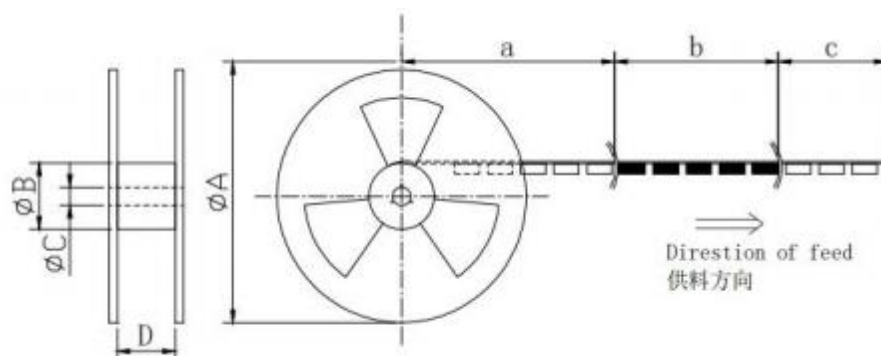
7. PACKAGE SPECIFICATION

(1). Tape Dimensions (Unit:mm)



W	A0	B0	K0	D	E	F	P	P0	P2	T
8.0 ±0.3	3.30 ±0.15	3.30 ±0.15	2.0 ±0.1	1.5 +0.1/-0.1	1.75 ±0.1	3.5 ±0.1	4.0 ±0.1	4.0 ±0.2	2.0 ±0.1	0.30 ±0.1

(2). Reel Dimensions (Unit:mm)



Item	Dimension
A	178
B	58
C	13
D	8.5
a	Blank portions
b	Chip cavity
c	Leader

(3). Packaging Quantity

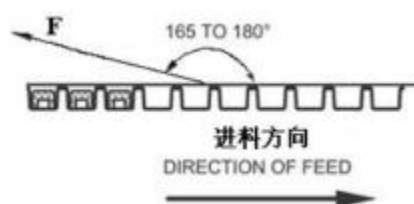
Reel (pcs)	Box (pcs)	Carton (pcs)
2000	8000	48,000

(4). Packaging -Cover Tape

F force: 10~130g

Peeling speed: 300mm/min±10%

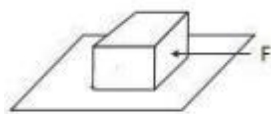
Peeling angle: 165°~180°



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8. RELIABILITY

No.	Item	Requirements	Test Methods and Remarks
1	High temperature Storage test	1. No significant defects in appearance. 2. $\Delta L/L \leq 10\%$ 3. $\Delta DCR/DCR \leq 10\%$	Temperature: $125^{\circ}\text{C} \pm 5^{\circ}\text{C}$ (N: Follow the product specification for the setting.) Time : 96 ± 2 hours Place the samples for one hour at room temperature and test them within two hours
2	Low temperature Storage test	1. No significant defects in appearance. 2. $\Delta L/L \leq 10\%$ 3. $\Delta DCR/DCR \leq 10\%$	Temperature: $-40^{\circ}\text{C} \pm 5^{\circ}\text{C}$ (M: Follow the product specification for the setting) Time : 96 ± 2 hours Place the samples for one hour at room temperature and test them within two hours.
3	Humidity test	1. No significant defects in appearance. 2. $\Delta L/L \leq 10\%$ 3. $\Delta DCR/DCR \leq 10\%$	Temperature: $40 \pm 2^{\circ}\text{C}$, Humidity: $93 \pm 3\% \text{RH}$ Time : 96 ± 2 hours Place the samples for one hour at room temperature and test them within two hours
4	Solderability test	Terminals must have 95% minimum solder coverage	1. Dip pads in flux then dip in solder pot at $245 \pm 5^{\circ}\text{C}$ for 5 second. 2. Solder: lead free 3. Flux: rosin flux
5	Heat endurance of flow soldering	1. No significant defects in appearance. 2. $\Delta L/L \leq 10\%$ 3. $\Delta DCR/DCR \leq 10\%$	1. Refer to the above reflow curve and go through the reflow for twice. 2. The peak temperature : $260 \pm 0/-5^{\circ}\text{C}$
6	Vibration test	1. No significant defects in appearance. 2. No short and no open.	Apply frequency $10 \sim 55 \sim 10\text{Hz}$ and amplitude 1.5mm, 1 min/cycle in X Y and Z direction for 2 hours each. (total 6 hours)
7	Terminal strength push tes	1. Applied force: 10N Duration: 10sec 2. Solder paste thickness: 0.12mm 3. Meet the above requirements without any loose termina	Solder the test samples to the PCB through 245°C reflow, apply a standard force on the side of the test samples for 10 seconds. 

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