

PRODUCT CHARACTERISTICS

N-CHANNEL		P-CHANNEL	
V_{DSS}	40V	V_{DSS}	-40V
$R_{DS(ON)}$ Typ(@ $V_{GS}=10V$)	24m Ω	$R_{DS(ON)}$ Typ(@ $V_{GS}=-10V$)	54m Ω
$R_{DS(ON)}$ Typ(@ $V_{GS}=4.5V$)	32m Ω	$R_{DS(ON)}$ Typ(@ $V_{GS}=-4.5V$)	72m Ω
I_D	7A	I_D	-5A

APPLICATIONS

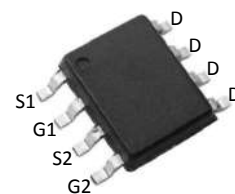
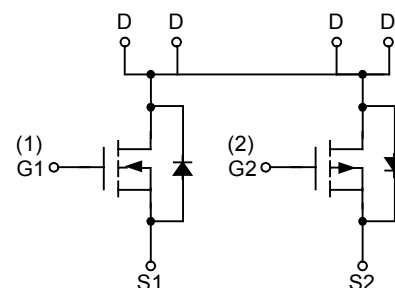
- * Electronic lamp ballasts based on half bridge
- * Load Switching, Quick/Wireless Charge.
- * Motor Driving

FEATURE

- * Low Gate Charge
- * Pb-Free Lead Plating



Symbol



SOP8

ORDER INFORMATION

Order Codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT4674S	SOP8	5000 pieces/Reel

ABSOLUTE MAXIMUM RATINGS($T_A=25^{\circ}C$, unless otherwise specified)

Parameter	Symbol	N-channel	P-channel	Unit
Drain-Source Voltage	V_{DSS}	40	-40	V
Gate-Source Voltage	V_{GSS}	± 20	± 20	V
Drain Current Continuous(@ $V_{GS}=10V, T_A=25^{\circ}C$)	I_D	7	-5	A
Drain Current Pulsed	I_{DM}	28	-20	A
Avalanche Energy	E_{AS}	36	36	mJ
Power Dissipation	P_D	2		W
Junction Temperature	T_J	+150		$^{\circ}C$
Storage Temperature	T_{STG}	-55~ +150		$^{\circ}C$

THERMAL CHARACTERISTICS

Parameter	Symbol	Typ	Unit
Junction to Ambient	R_{thJA}	62.5	$^{\circ}C/W$

■ N-MOS ELECTRICAL CHARACTERISTICS ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off characteristics						
Drain to Source Breakdown Voltage	V _{DSS}	V _{GS} =0V,I _D =250μA	40	-	-	V
Drain to Source Leakage Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	-1	μA
Gate to Source Forward Leakage	I _{GSS(F)}	V _{GS} =+20V,V _{DS} =0V	-	-	100	nA
Gate to Source Reverse Leakage	I _{GSS(R)}	V _{GS} =-20V ,V _{DS} =0V	-	-	-100	nA
On characteristics						
Drain to Source On-Resistance	R _{DS(ON)}	V _{GS} =10V,I _D =5 A	-	24	30	mΩ
		V _{GS} =4.5V,I _D =5 A	-	32	38	mΩ
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} ,I _D =250μA	1	1.5	2.5	V
Dynamic characteristics						
Gate Resistance	R _g	V _{GS} =0V,V _{DS} =0V ,f=1.0MHz	-	1.9	-	Ω
Forward Transconductance	g _{fs}	V _{DS} =5V,I _D =5A	-	7	-	S
Input Capacitance	C _{iss}	V _{DS} =20V,V _{GS} =0V f=1.0MHz	-	450	-	pF
Output Capacitance	C _{oss}		-	58	-	pF
Reverse Transfer Capacitance	C _{rss}		-	43	-	pF
Resistive Switching Characteristics						
Turn-on Delay Time	t _{d(ON)}	V _{GS} =10V, V _{DS} =20V, I _D =6A ,R _G =3Ω	-	4.5	-	ns
Rise Time	t _r		-	2.5	-	ns
Turn-off Delay Time	t _{d(OFF)}		-	14.5	-	ns
Fall Time	t _f		-	3.5	-	ns
Total Gate Charge	Q _g	I _D =6 A, V _{DS} =20V V _{GS} =10V	-	8.9	-	nC
Gate to Source Charge	Q _{gs}		-	2.4	-	nC
Gate to Drain("Miller") Charge	Q _{gd}		-	1.4	-	nC
Source-Drain Diode Characteristics						
Continuous Source Current(Body Diode)	I _s		-	-	7	A
Maximum Pulsed Current(Body Diode)	I _{SM}		-	-	28	A
Diode Forward Voltage	V _{SD}	I _{SD} =1A, V _{GS} =0V	-	0.77	1.2	V
Reverse Recovery Time	t _{rr}	I _{SD} =6A, T _J =25 °C dI/dt=100A/μs	-	25	-	ns
Reverse Recovery Charge	Q _{rr}		-	14	-	nC

■ P-MOS ELECTRICAL CHARACTERISTICS ($T_c=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off characteristics						
Drain to Source Breakdown Voltage	V _{DSS}	V _{GS} =0V,I _D =-250μA	-40	-	-	V
Drain to Source Leakage Current	I _{DSS}	V _{DS} =-40V,V _{GS} =0V	-	-	-1	μA
Gate to Source Forward Leakage	I _{GSS(F)}	V _{GS} =+20V,V _{DS} =0V	-	-	100	nA
Gate to Source Reverse Leakage	I _{GSS(R)}	V _{GS} =-20V ,V _{DS} =0V	-	-	-100	nA
On characteristics						
Drain to Source On-Resistance	R _{DS(ON)}	V _{GS} =-10V,I _D =-5A	-	54	74	mΩ
		V _{GS} =-4.5V,I _D =-5A	-	72	98	mΩ
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} ,I _D =-250μA	-1	-1.6	-2.5	V
Dynamic characteristics						
Gate Resistance	R _g	V _{GS} =0V,V _{DS} =0V ,f=1.0MHz	-	16	-	Ω
Forward Transconductance	g _{fs}	V _{DS} =-5V,I _D =-5A	-	8.5	-	S
Input Capacitance	C _{iss}	V _{DS} =-20V,V _{GS} =0V f=1.0MHz	-	406	-	pF
Output Capacitance	C _{oss}		-	53	-	pF
Reverse Transfer Capacitance	C _{rss}		-	40	-	pF
Resistive Switching Characteristics						
Turn-on Delay Time	t _{d(ON)}	V _{GS} =-10V,V _{DS} =-20V, I _D =-5 A,R _G =6Ω	-	6.2	-	ns
Rise Time	t _r		-	8.4	-	ns
Turn-off Delay Time	t _{d(OFF)}		-	44.8	-	ns
Fall Time	t _f		-	16	-	ns
Total Gate Charge	Q _g	I _D =-5 A, V _{DS} =-20V V _{GS} =-10V	-	17	-	nC
Gate to Source Charge	Q _{gs}		-	3.4	-	nC
Gate to Drain("Miller") Charge	Q _{gd}		-	3.2	-	nC
Source-Drain Diode Characteristics						
Continuous Source Current(Body Diode)	I _s		-	-	-5	A
Maximum Pulsed Current(Body Diode)	I _{SM}		-	-	- 20	A
Diode Forward Voltage	V _{SD}	I _{SD} =-1A, V _{GS} =0V	-	-0.79	-1.2	V
Reverse Recovery Time	t _{rr}	I _{SD} =-5 A, T _J =25 °C dI/dt=100A/μs	-	16	-	ns
Reverse Recovery Charge	Q _{rr}		-	8	-	nC

■ N-MOS TYPICAL CHARACTERISTICS

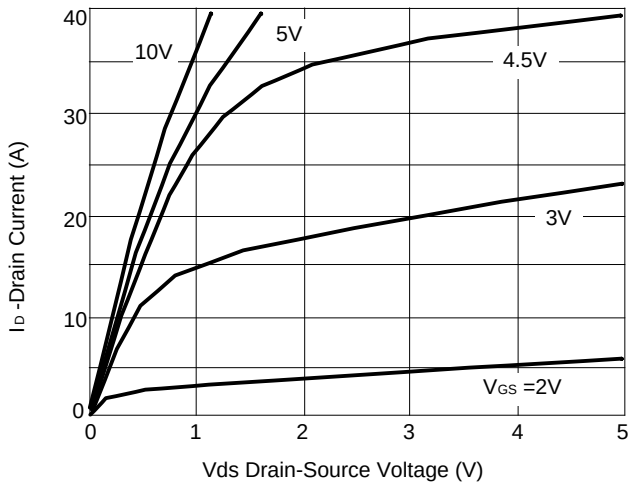


Figure 1: Output Characteristics

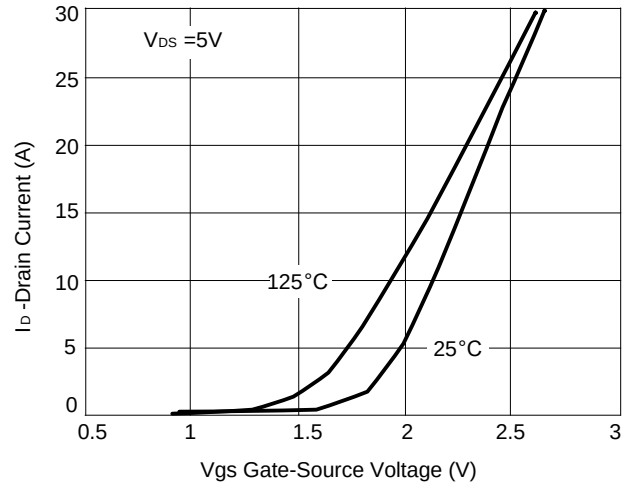


Figure 2: Transfer Characteristics

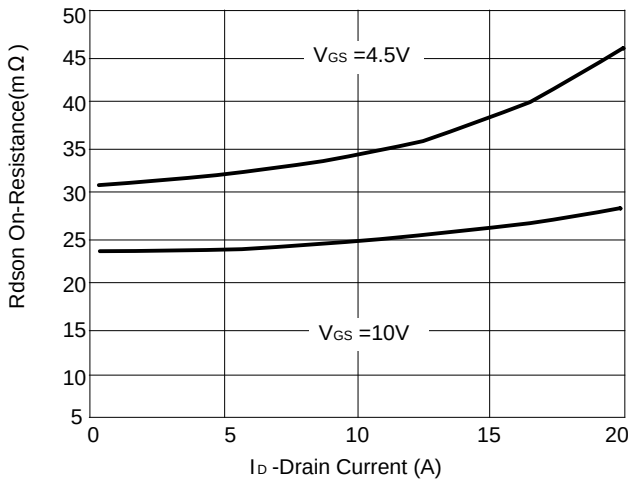


Figure 3: Drain-Source On-Resistance

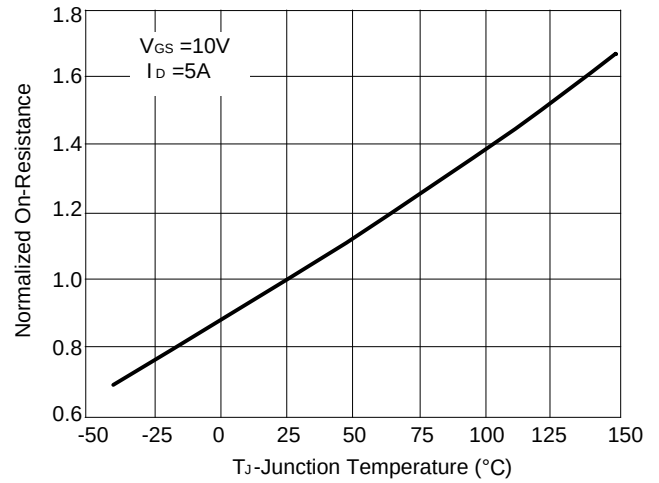


Figure 4: Drain-Source On-Resistance

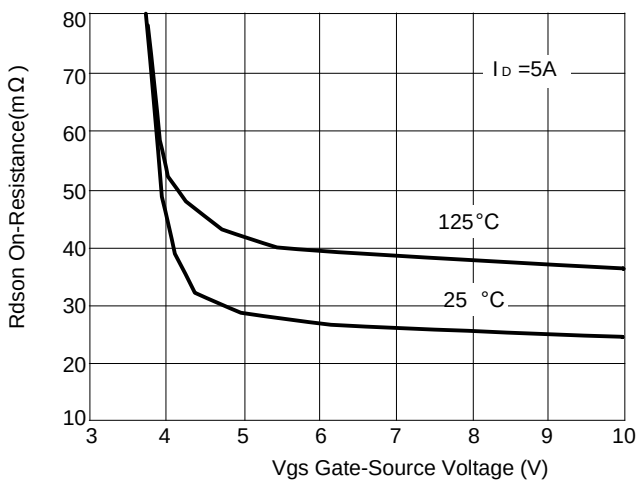


Figure 5: Rds(on) vs. Vgs

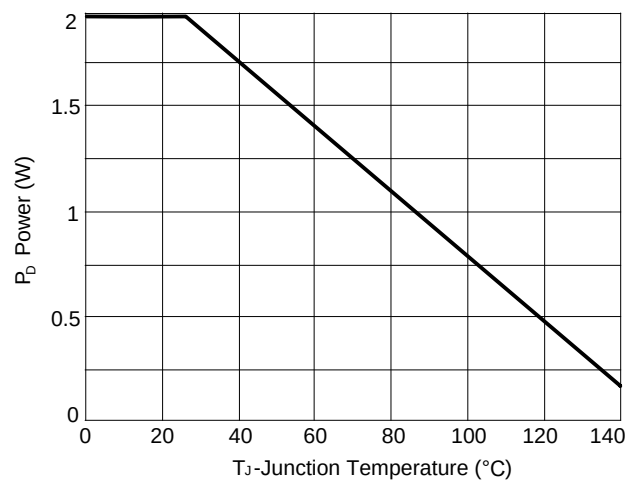


Figure 6: Power Dissipation

■ N-MOS TYPICAL CHARACTERISTICS(Cont.)

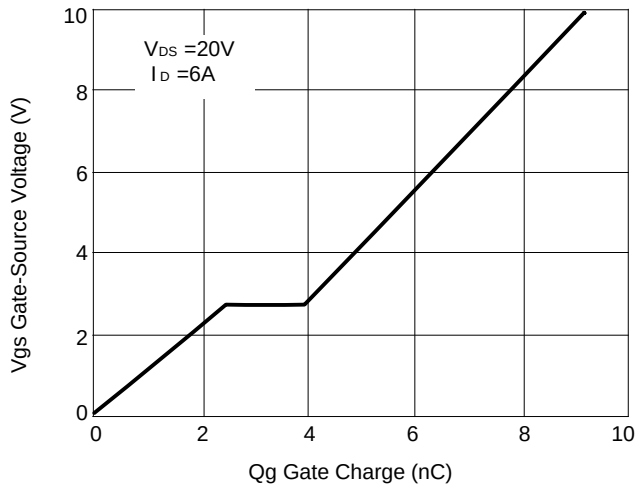


Figure 7: Gate Charge

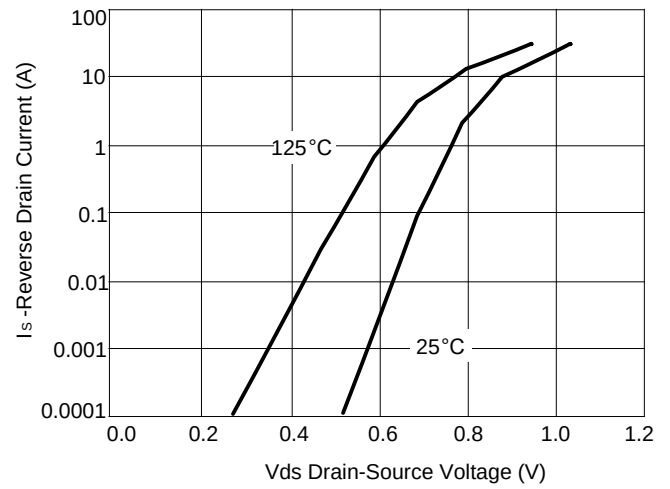


Figure 8: Source-Drain Diode Forward

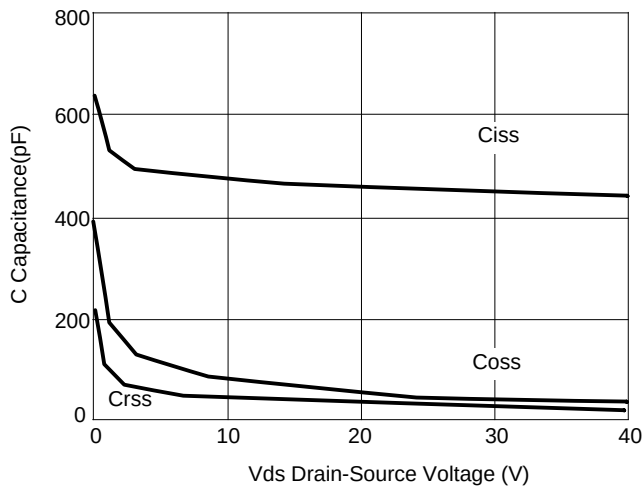


Figure 9: Capacitance vs Vds

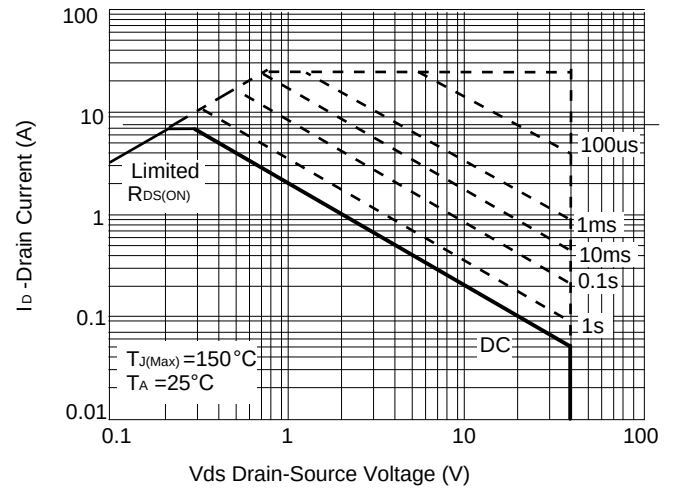


Figure 10: Safe Operation Area

■ P-MOS TYPICAL CHARACTERISTICS

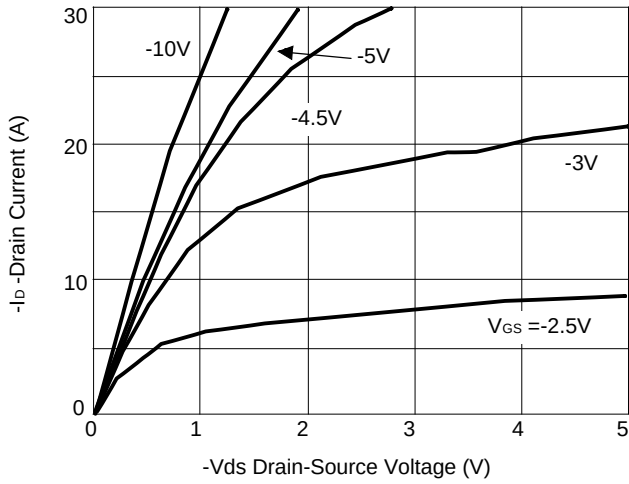


Figure 1: Output Characteristics

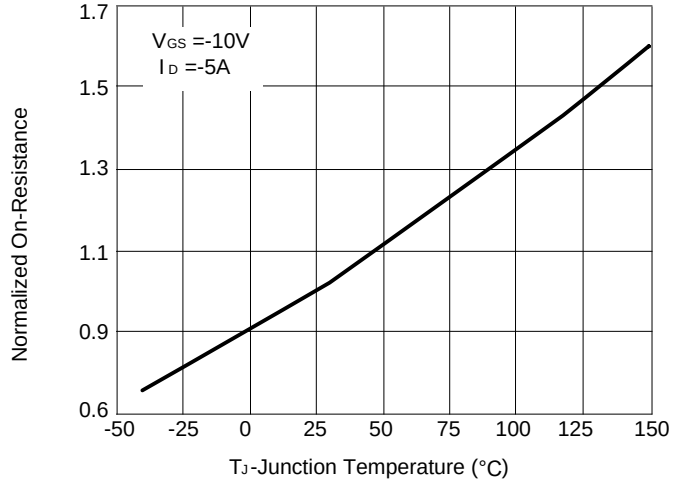


Figure 2: Drain-Source On-Resistance

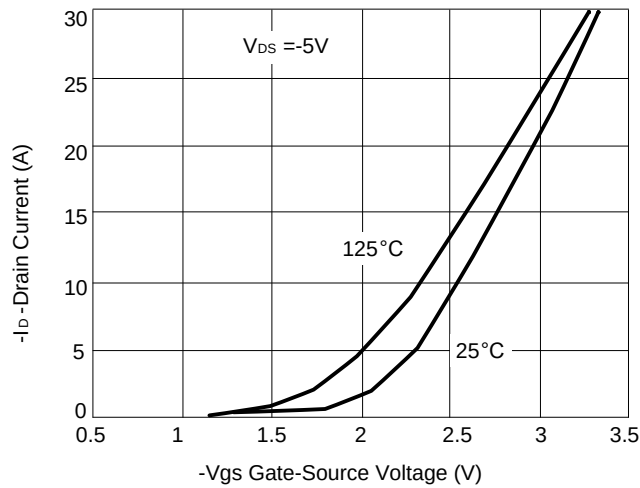


Figure 3: Transfer Characteristics

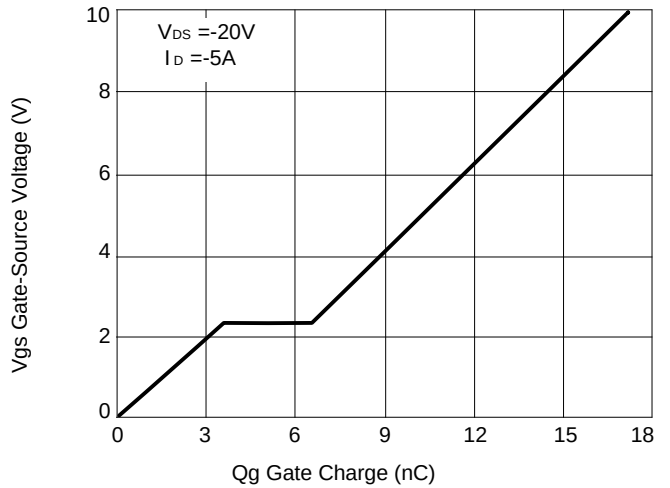


Figure 4: Gate Charge

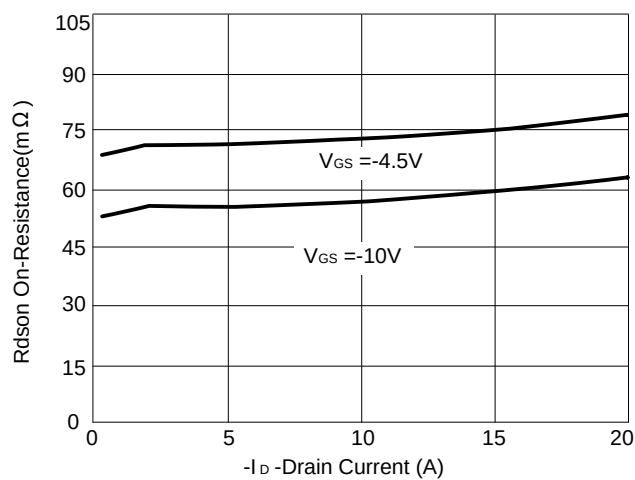


Figure 5: Drain-Source On-Resistance

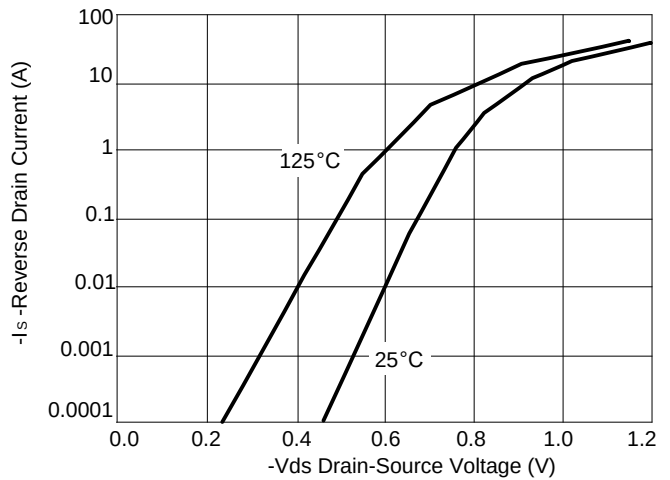


Figure 6: Source-Drain Diode Forward

■ P-MOS TYPICAL CHARACTERISTICS(Cont.)

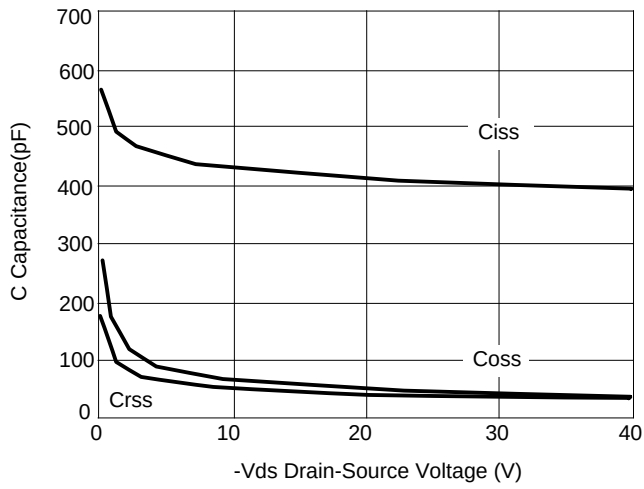


Figure 7: Capacitance vs Vds

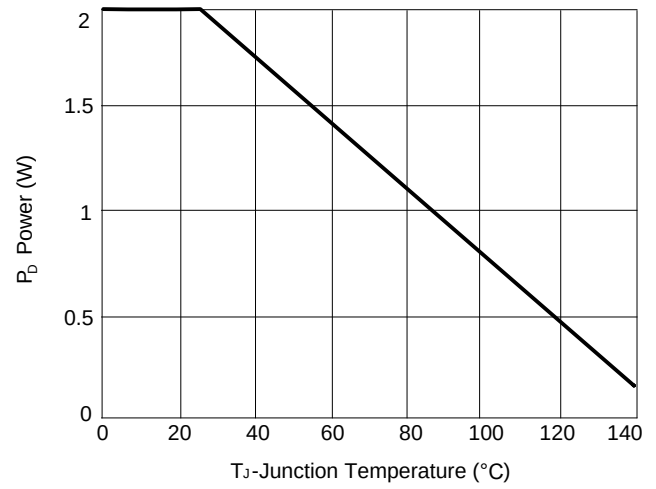


Figure 8: Power Dissipation

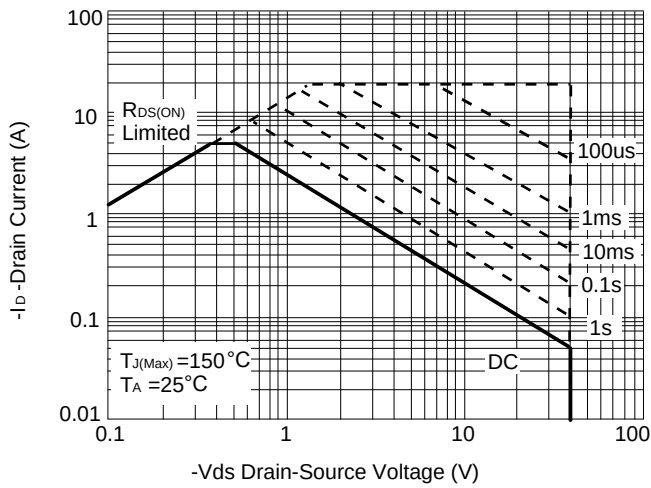


Figure 9: Safe Operation Area

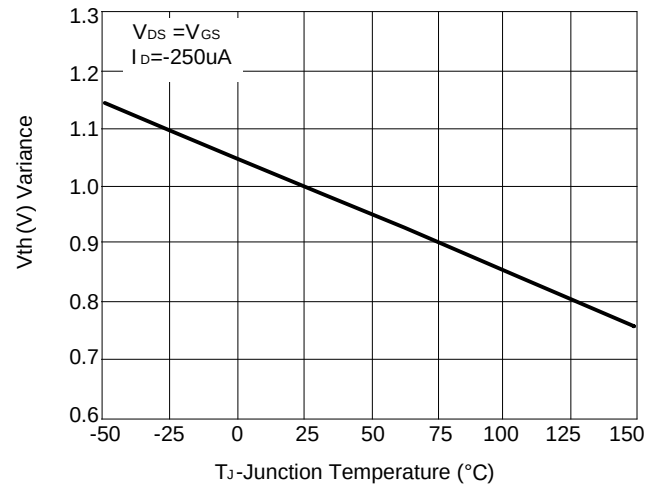


Figure 10: VGS(th) vs. Junction Temperature

■ SOP8 PACKAGE OUTLINE DIMENSIONS

