

VDS= 30V

RDS(ON), Vgs@10V, Ids@5.8A < 30mΩ

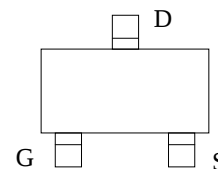
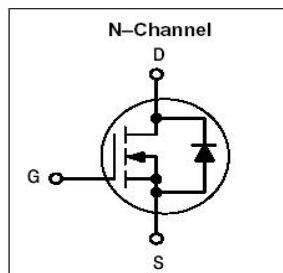
RDS(ON), Vgs@4.5V, Ids@5.0A < 33mΩ

RDS(ON), Vgs@2.5V, Ids@4.0A < 52mΩ

Features

Advanced trench process technology

High Density Cell Design For Ultra Low On-Resistance



SOT-23

Order Information

Product	Marking	Package	Packing
WTM3400	3400	SOT-23	3000PCS/Reel

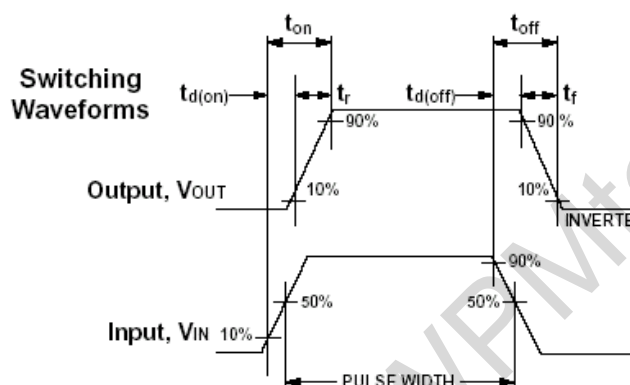
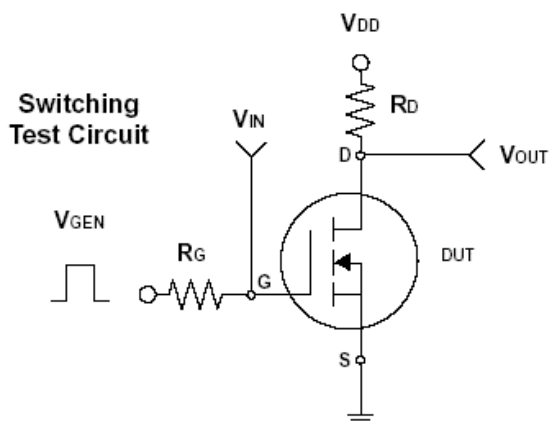
Maximum Ratings and Thermal Characteristics (TA = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±12	
Continuous Drain Current	I _D	5.8	A
Pulsed Drain Current	I _{DM}	30	
Maximum Power Dissipation	P _D	1.4	W
		1	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C
Junction-to-Ambient Thermal Resistance (PCB mounted)	R _{θJA}	145	°C/W

ELECTRICAL CHARACTERISTICS (TA = 25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250uA	30			V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 5.8A		22.0	30.0	mΩ
Drain-Source On-State Resistance	R _{Ds(on)}	V _{GS} = 4.5V, I _D =5A		27.0	33.0	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 2.5V, I _D =4A		43.0	52.0	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D = 250uA	0.7		1.4	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			1	uA
Gate Body Leakage	I _{GSS}	V _{GS} = ± 12V, V _{DS} = 0V			±100	nA
Forward Transconductance	g _{fs}	V _{DS} = 5V, I _D = 5A	10	15	—	S
Gate Resistance	R _g	F=1.0MHz	6	7	7.5	Ω
Dynamic						
Total Gate Charge	Q _g	V _{DS} = 15V, I _D = 5.8A V _{GS} = 4.5V		11		nC
Gate-Source Charge	Q _{gs}			1.6		
Gate-Drain Charge	Q _{gd}			2.8		
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15V, R _L =2.7Ω I _b = 1A, V _{GEN} = 10V R _G = 3Ω		7		ns
Turn-On Rise Time	t _r			15		
Turn-Off Delay Time	t _{d(off)}			38		
Turn-Off Fall Time	t _f			3		
Input Capacitance	C _{iss}	V _{DS} = 10V, V _{GS} = 0V f = 1.0 MHz		340		pF
Output Capacitance	C _{oss}			115		
Reverse Transfer Capacitance	C _{rss}			33		
Source-Drain Diode						
Max. Diode Forward Current	I _S				1.6	A
Diode Forward Voltage	V _{SD}	I _S = 1.6A, V _{GS} = 0V			1.2	V

Note: Pulse test: pulse width ≤ 300μs, duty cycle ≤ 2%



Characteristics Curve

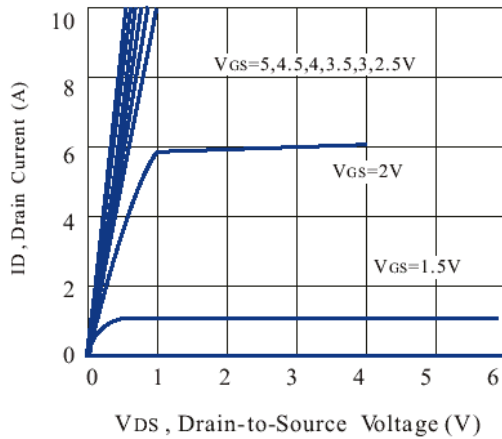


Figure 1. Output Characteristics

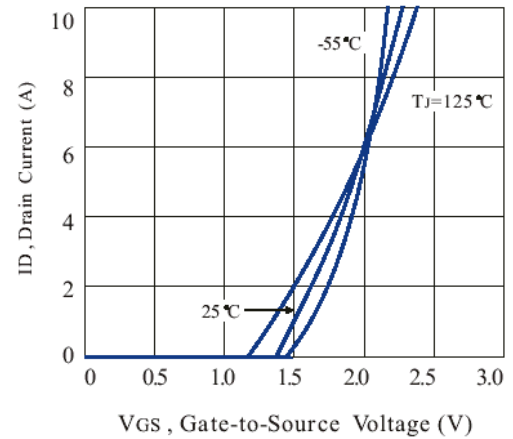


Figure 2. Transfer Characteristics

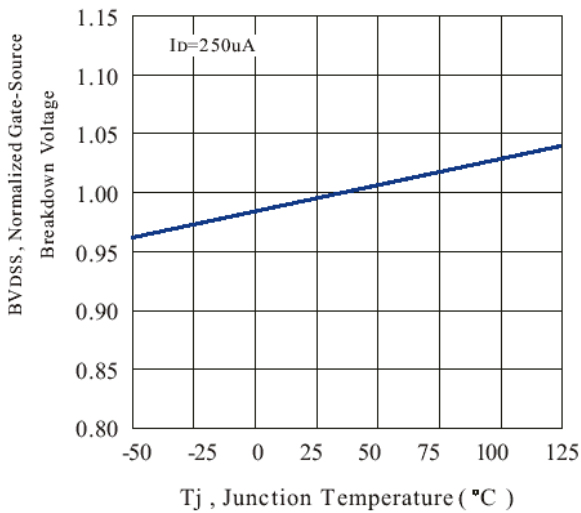


Figure 3. Breakdown Voltage Variation with Temperature

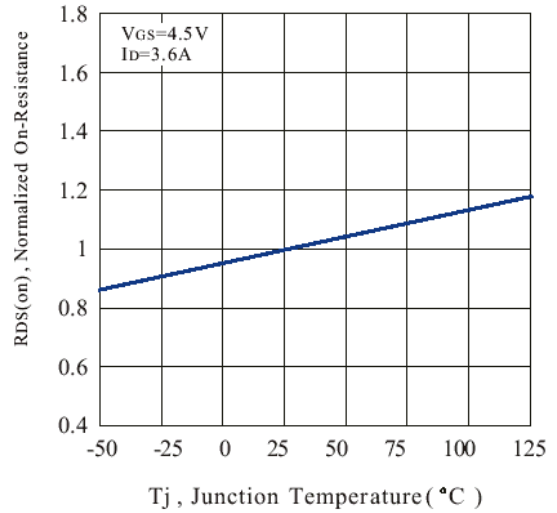


Figure 4. On-Resistance Variation with Temperature

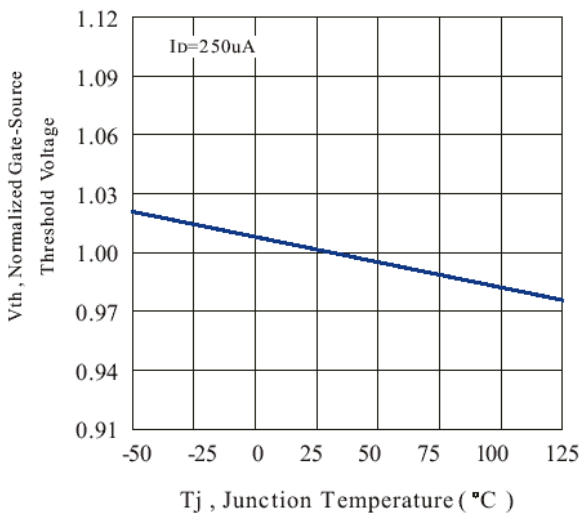


Figure 5. Gate Threshold Variation with Temperature

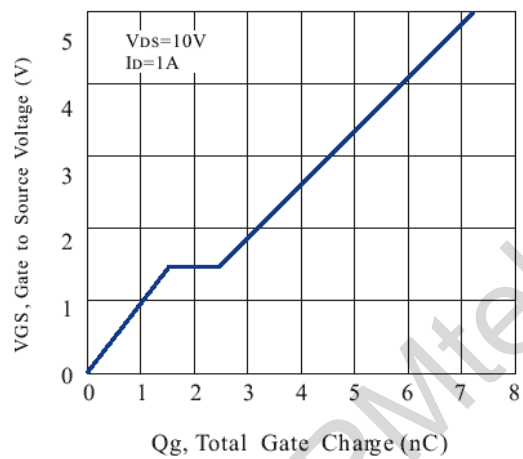
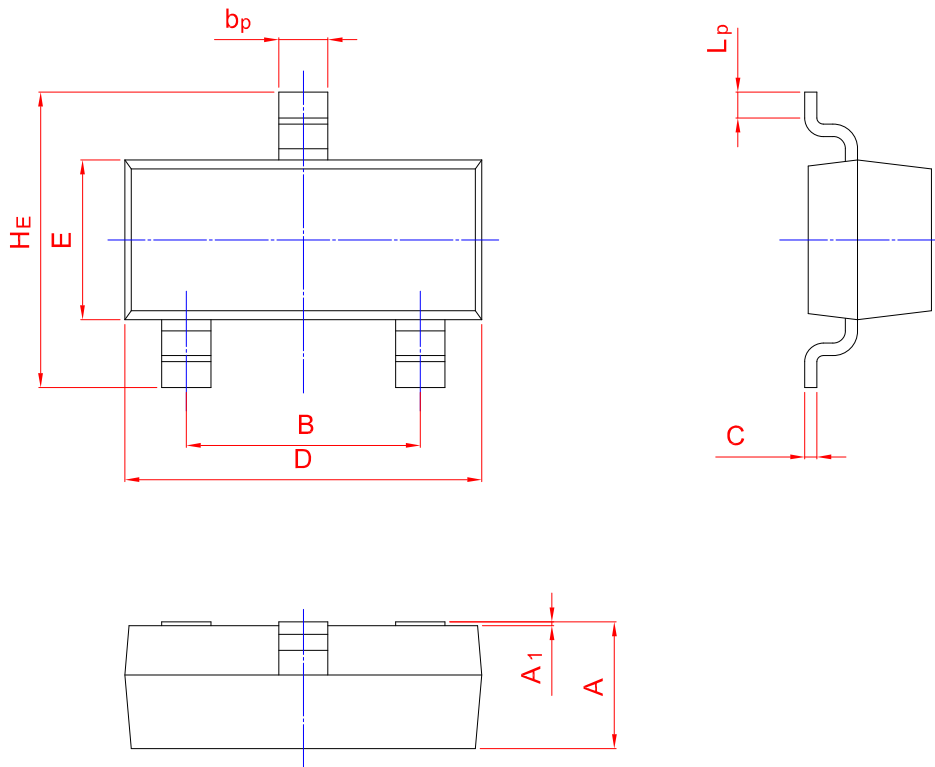
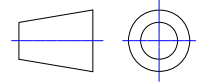


Figure 6. Gate Charge

SOT-23 Package Outline Dimensions (Units: mm)

SOT-23



UNIT	A	B	b _p	C	D	E	H _E	A ₁	L _p
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20

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