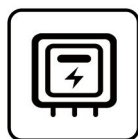


TDSiEMIC

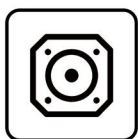
拓 電 半 導 體

自主封測 品質把控 售後保障

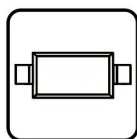
WEB | WWW.TDSEMIC.COM



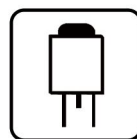
電源管理



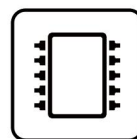
顯示驅動



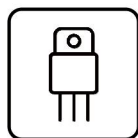
二三極管



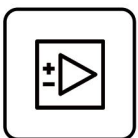
LDO穩壓器



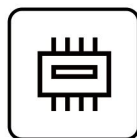
觸摸芯片



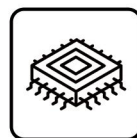
MOS管



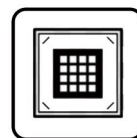
運算放大器



存儲芯片



MCU



串口通信

2309-TD

產品規格說明書

General Description

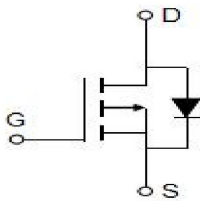
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge

Features

- -60V, -3A
 $R_{DS(ON)}$ Typ = 144 m Ω @ V_{GS} = -10V
 $R_{DS(ON)}$ Typ = 157 m Ω @ V_{GS} = -4.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free

Applications

- Load Switch
- PWM Application
- Power Management



Schematic Diagram

Absolute Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		-60	V
V_{GS}	Gate-to-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_A = 25^{\circ}\text{C}$	-3	A
		$T_A = 100^{\circ}\text{C}$	-1.8	
I_{DM}	Pulsed Drain Current ⁽¹⁾		-12	A
P_D	Power Dissipation	$T_A = 25^{\circ}\text{C}$	1.56	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽²⁾		80	$^{\circ}\text{C/W}$
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	$^{\circ}\text{C}$

-55 to 150

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = -250μA, V _{GS} = 0V	-60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -60V, V _{GS} = 0V	-	-	-1.0	μA
I _{GSS}	Gate-Body Leakage Current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-1.1	-1.5	-2.4	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽⁴⁾	V _{GS} = -10V, I _D = -3 A	-	144	150	mΩ
		V _{GS} = -4.5V, I _D = -2A	-	157	165	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = -25V, f = 1MHz	-	716	-	pF
C _{oss}	Output Capacitance		-	30	-	pF
C _{rss}	Reverse Transfer Capacitance		-	21	-	pF
Q _g	Total Gate Charge	V _{GS} = 0 to -10V V _{DS} = -30V, I _D = -2A	-	4.92	-	nC
Q _{gs}	Gate Source Charge		-	0.97	-	nC
Q _{gd}	Gate Drain("Miller") Charge		-	0.72	-	nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GS} = -10V, V _{DD} = -30V I _D = -2A , R _{GEN} = 5Ω	-	6.8	-	ns
t _r	Turn-On Rise Time		-	8	-	ns
t _{d(off)}	Turn-Off DelayTime		-	16	-	ns
t _f	Turn-Off Fall Time		-	4	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-3	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-12	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = -3A	-	-	-1.2	V

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. R_{θJA} is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB
 3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%.

Test Circuit

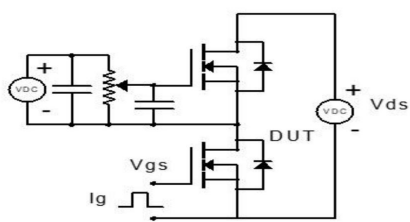


Figure 1: Gate Charge Test Circuit & Waveform

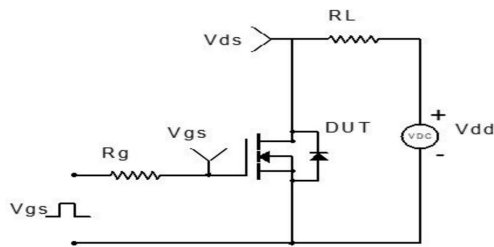


Figure 2: Resistive Switching Test Circuit & Waveform

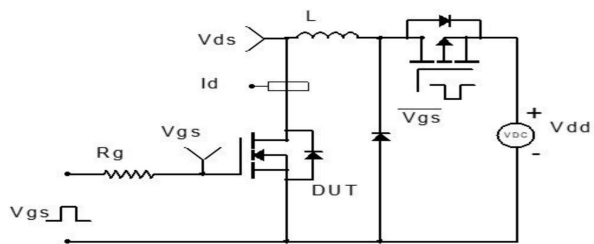


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

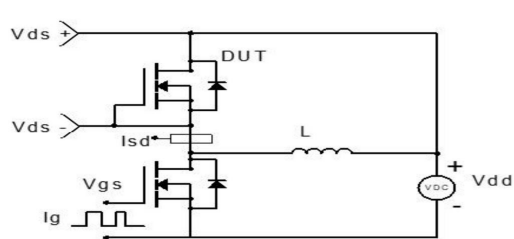


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOT-23)

