



SS8050

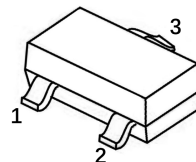
NPN Plastic-Encapsulate Transistors

Product data sheet

1. General description

NPN Transistor encapsulated in a small SOT-23 Surface- Mounted Device (SMD) plastic package.

SOT-23



2.Features

- Ideal for Medium Power Amplification and Switching
- Complementary to SS8550

3. Applications

- Surface Mount Package Ideally Suited for Automatic Insertion
- For General Purpose Switching Applications

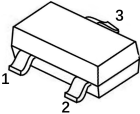
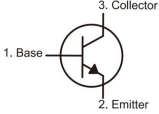
4. Quick reference data

Table 1. Quick reference data
 $T_a=25^{\circ}\text{C}$

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-Base Voltage	40	V
V_{CEO}	Collector-Emitter Voltage	25	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current	1.5	A
P_C	Collector Power Dissipation	300	mW

5.Pinning information

Table 2. Pinning information

Pin	Name	SOT-23 Simplified outline	Graphic symbol
1	Base		
2	Emitter		
3	Collector		

6.Ordering information

Table 3. Ordering information

Part number	Package	Material	Packing	Quantity per reel	Flammability rating	Reel size
SS8050	SOT-23	halogen free	tape & reel	3,000pcs	UL 94V-0	7 inches

7.Marking

Table 4. Marking codes

Part number	Marking code
SS8050	Y1

8. MAXIMUM RATINGS (Ta=25℃ unless otherwise noted)

Table 5. Maximum Ratings

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-Base Voltage	40	V
V_{CEO}	Collector-Emitter Voltage	25	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current	1.5	A
P_C	Collector Power Dissipation	300	mW
$R_{\theta JA}$	Thermal Resistance From Junction To Ambient	417	℃/W
T_J, T_{stg}	Operation Junction and Storage Temperature Range	-55~+150	℃

9. ELECTRICAL CHARACTERISTICS (Ta=25℃ unless otherwise specified)

Table 6. Electrical Characteristics

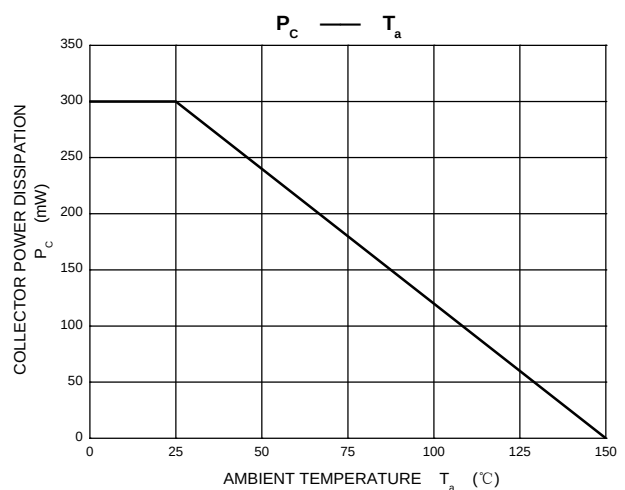
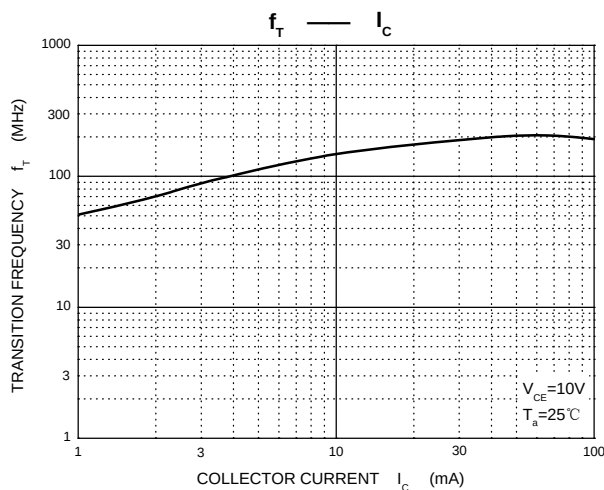
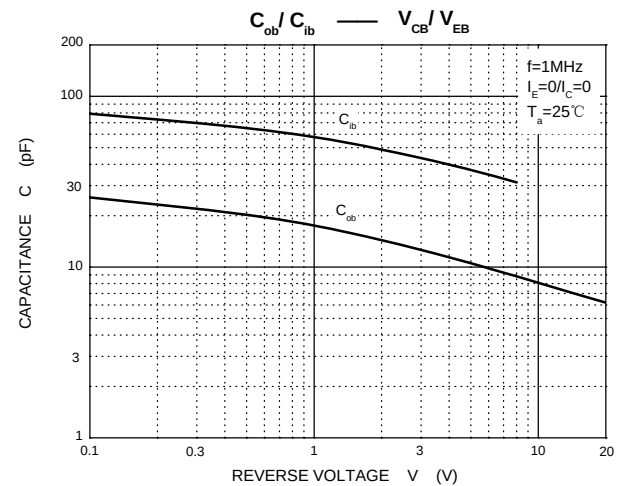
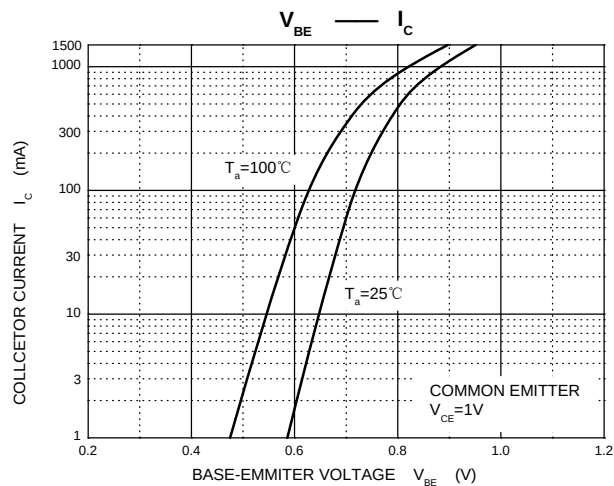
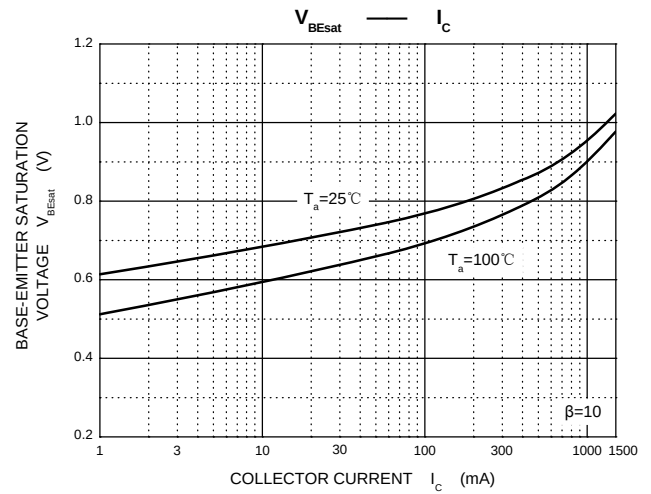
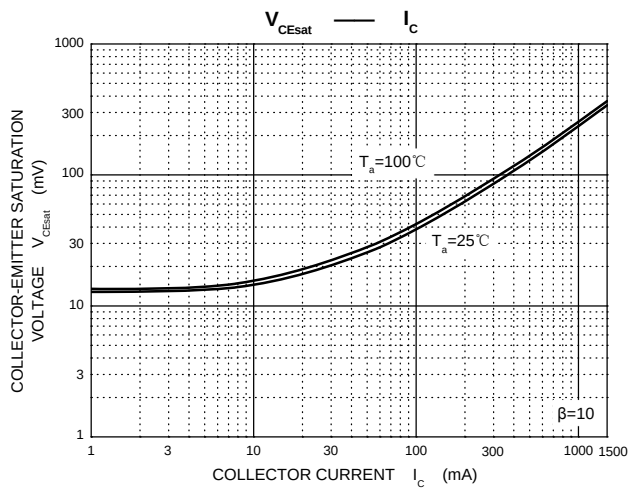
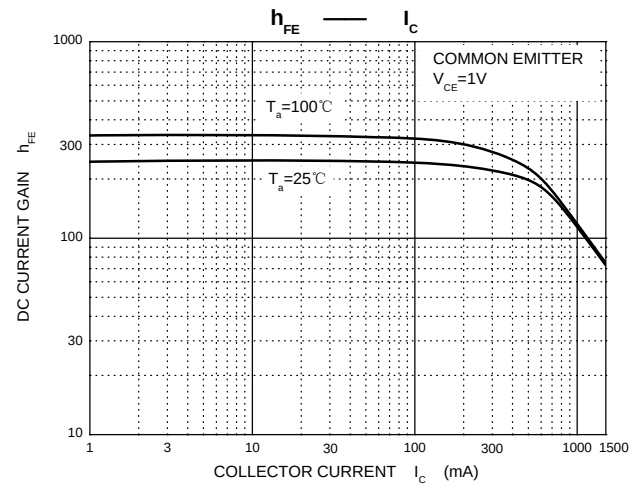
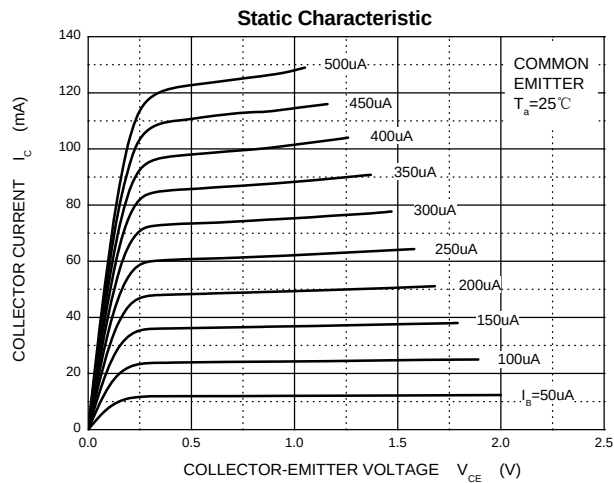
Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = 100\mu A, I_E = 0$	40			V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = 0.1mA, I_B = 0$	25			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E = 100\mu A, I_C = 0$	5			V
Collector cut-off current	I_{CBO}	$V_{CB} = 40V, I_E = 0$			0.1	μA
Collector cut-off current	I_{CEO}	$V_{CE} = 20V, I_E = 0$			0.1	μA
Emitter cut-off current	I_{EBO}	$V_{EB} = 5V, I_C = 0$			0.1	μA
DC current gain	$h_{FE(1)}$	$V_{CE} = 1V, I_C = 100mA$	120		400	
	$h_{FE(2)}$	$V_{CE} = 1V, I_C = 800mA$	40			
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 800mA, I_B = 80mA$			0.5	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C = 800mA, I_B = 80mA$			1.2	V
Transition frequency	f_T	$V_{CE} = 10V, I_C = 50mA$ $f = 30MHz$	100			MHz

*Pulse test: pulse width $\leq 300\mu s$, duty cycle $\leq 2.0\%$.

Table 7. CLASSIFICATION OF h_{FE}

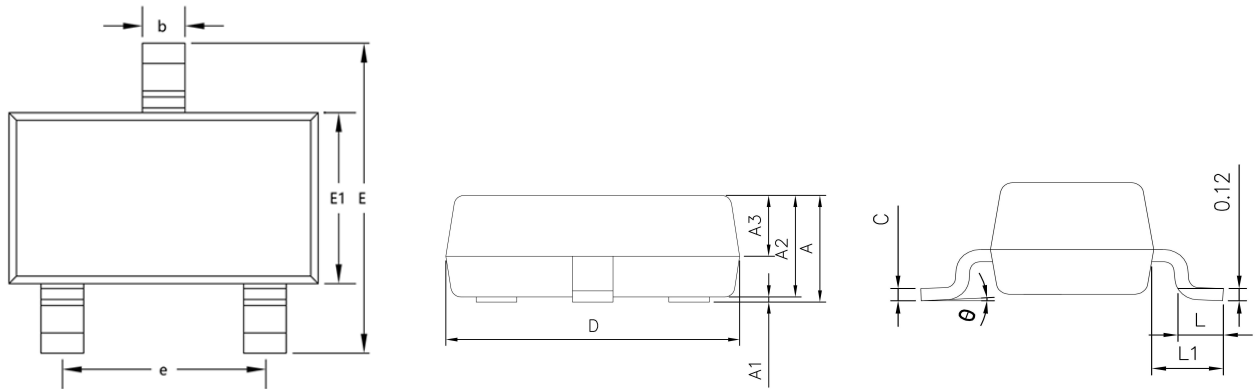
Rank	L	H	J
Range	120-200	200-350	300-400

10. Typical characteristics



11.Package outline

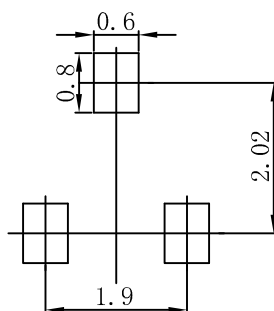
Table 8. SOT-23 Package outline



Symbol	Dimension in millimeter		
	Min	Nom	Max
A	0.9	1.05	1.2
A1	0.0	0.05	0.1
A2	0.9	1.00	1.10
A3	0.55	0.60	0.65
b	0.30	0.40	0.50
c	0.10	0.11	0.12
D	2.80	2.90	3.0
E	2.25	2.40	2.55
E1	1.20	1.30	1.40
e	1.8	1.9	2.0
L	0.30	0.40	0.50
L1	0.475	0.55	0.625
θ	0°	3°	6°

12.Suggested pad layout

Table 9. Suggested pad layout



Note:

1. Controlling dimension: in millimeters
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference purposes only

13. Disclaimer

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