

Product Summary

- V_{DS} 30 V
- I_{DS} (at $V_{GS}=10V$) 100A
- $R_{DS(ON)}$ (at $V_{GS}=10V$) $\leq 2.5m\Omega$ (TYP)

Application

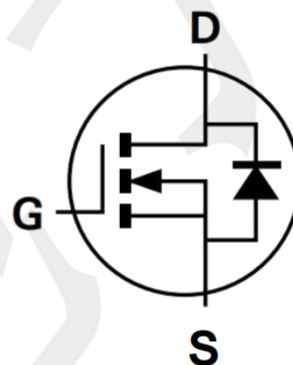
- Load switch
- High Frequency Switching and Synchronous Rectification
- Active Clamp in Intermediate
- DC/DC Power Supplies

Package and Pin Configuration



PDFN3X3-8

Circuit diagram



Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	100	A
		79	
Pulsed Drain Current (Note1)	I_{DM}	400	A
Single Pulse Avalanche Energy	EAS	230	mJ
Total Power Dissipation	P_{DTOT}	65	W
Operating Junction Temperature Range	T_J	-55 to +150	°C
Storage Temperature Range	T_{stg}	-55 to +150	°C

Thermal Characteristic

PARAMETER	Symbol	Value	Unit
Junction-to-Ambient Thermal Resistance	$R_{\theta JA}$	60	°C/W
Thermal Resistance Junction-Case	$R_{\theta JC}$	1.88	°C/W

notes 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. When mounted on 2" square PCB (FR4 material).

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static						
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	BV_{DSS}	30	--	--	V
Gate-Source Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	$V_{GS(th)}$	1.0	1.4	2.2	V
Gate-Source Leakage	$V_{DS}=0V, V_{GS}=\pm 20V$	I_{GSS}	--	--	± 100	nA
Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$	I_{DSS}	--	0.1	1.0	μA
	$V_{DS}=30V, T_J=55^{\circ}C$		--	1.0	5.0	μA
Drain-Source On-State Resistance (Note 1)	$V_{GS}=10V, I_D=12A$	$R_{DS(on)}$	--	2.5	3.0	m Ω
	$V_{GS}=4.5V, I_D=8A$		--	3.5	4.2	
Forward Transconductance (Note 2)	$V_{DS}=10V, I_D=3A$	g_{fs}	--	11	--	S
Dynamic (Note 2)						
Total Gate Charge (Note 3)	$V_{DS}=15V, I_D=10A, V_{GS}=4.5V$	Q_g	--	55	--	nC
Gate-Source Charge (Note 3)		Q_{gs}	--	11	--	
Gate-Drain Charge (Note 3)		Q_{gd}	--	12	--	
Input Capacitance	$V_{DS}=15V, V_{GS}=0V, F=1.0MHz$	C_{iss}	--	3600	--	pF
Output Capacitance		C_{oss}	--	490	--	
Reverse Transfer Capacitance		C_{rss}	--	420	--	
Switching						
Turn-On Delay Time (Note 3)	$V_{DD}=15V, I_D=1A, V_{GS}=10V, R_G=3.3\Omega$	$t_{d(on)}$	--	8.2	--	nS
Rise Time (Note 3)		t_r	--	13	--	
Turn-Off Delay Time (Note 3)		$t_{d(off)}$	--	17	--	
Fall Time (Note 3)		t_f	--	22	--	
Source-Drain Diode Ratings and Characteristics (Note 2)						
Forward Voltage	$V_{GS}=0V, I_F=10A$	V_{SD}	--	0.7	1.0	V
Continuous Source Current	Integral reverse diode in the MOSFET	I_S	--	--	100	A
Pulsed Current (Note 1)		I_{SM}	--	--	400	A

Notes:

1. Pulse test; pulse width ≤ 300 μS, duty cycle ≤ 2%.
2. Guaranteed by design, not subject to production testing.
3. Independent of operating temperature

Typical Electrical and Thermal Characteristics

Fig.1 Continuous Drain Current vs. T_C
 T_C , Case Temperature ($^{\circ}\text{C}$)

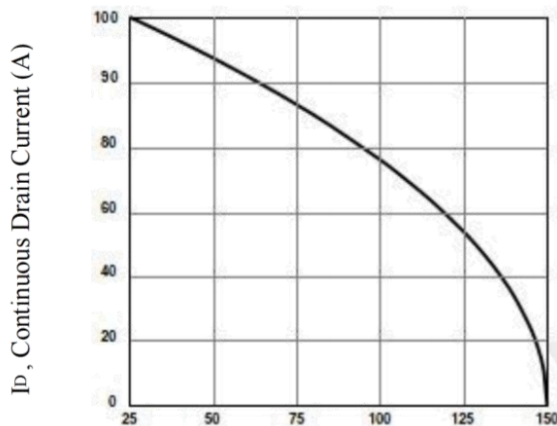


Fig.2 Normalized $R_{DS(on)}$ vs. T_J
 T_J , Junction Temperature ($^{\circ}\text{C}$)

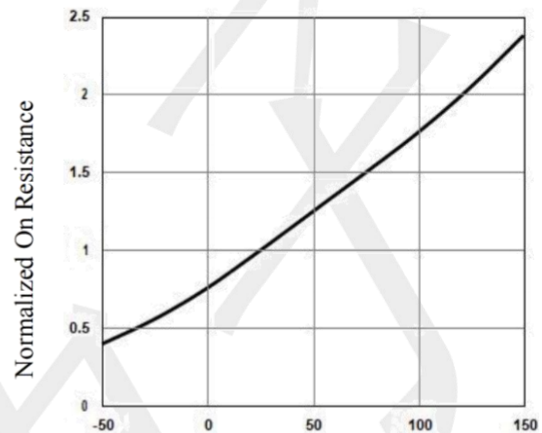


Fig.3 Normalized V_{th} vs. T_J

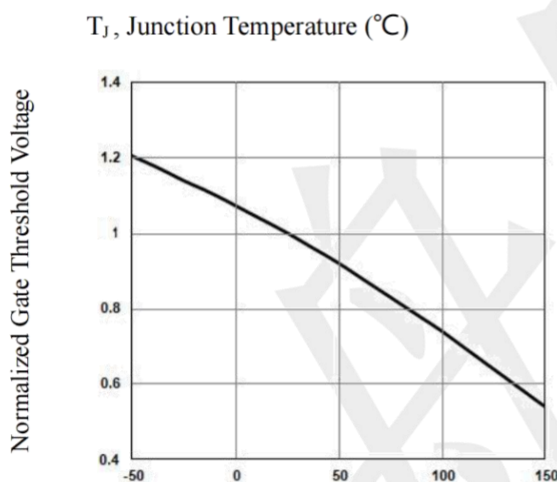


Fig.4 Gate Charge Waveform

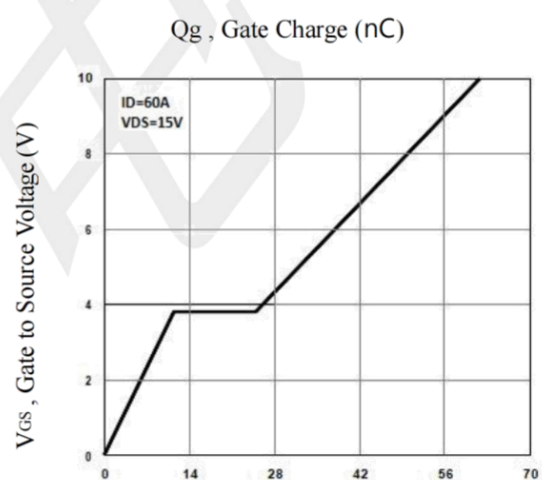


Fig.5 Normalized Transient Impedance
SquareWave Pulse Duration (s)

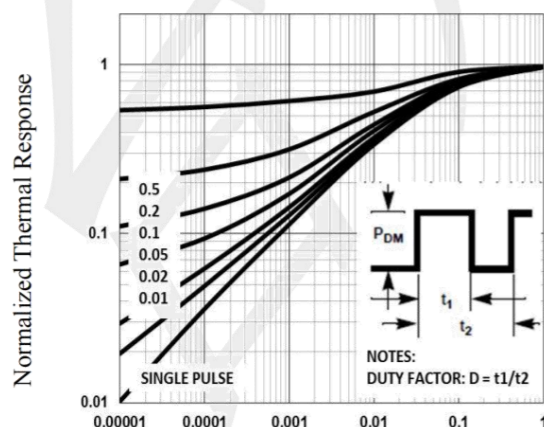
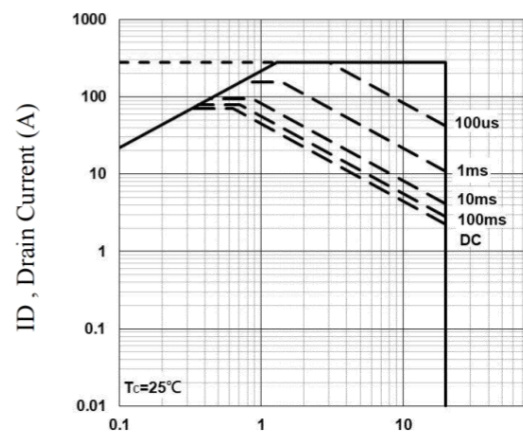
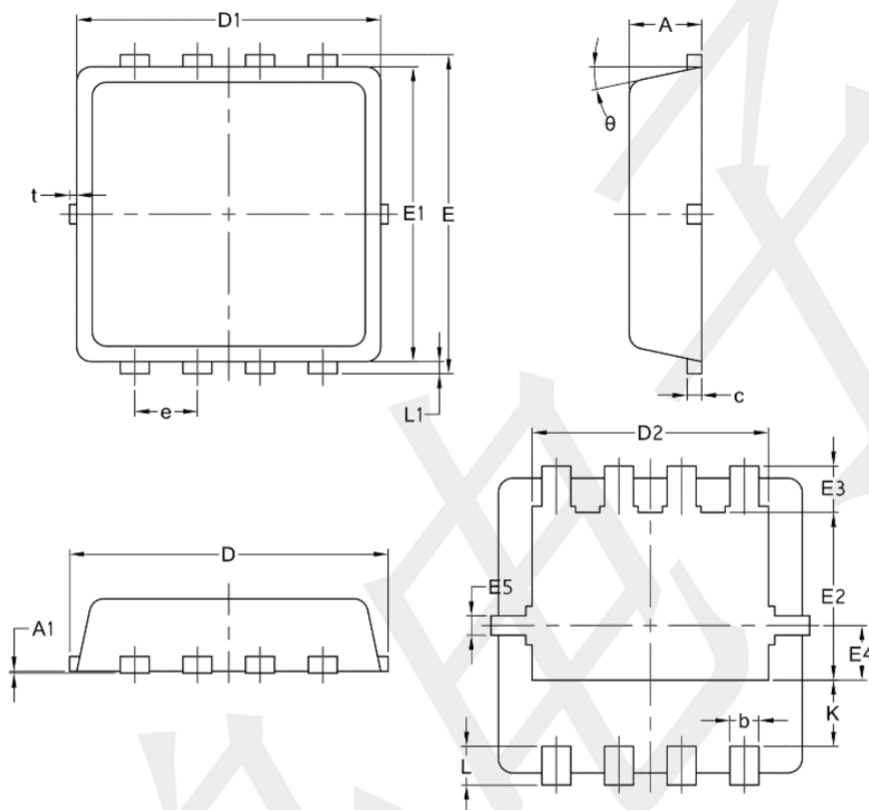


Fig.6 Maximum Safe Operation Area
 V_{DS} , Drain to Source Voltage (V)



Package Information

PDFN3X3-8



Symbol	Common		
	mm		
	Mim	Nom	Max
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.59	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
Φ	10	12	14