

General Description

The CMF5N65 uses advanced planar stripe DMOS technology and design to provide excellent $R_{DS(ON)}$. It will provide much high efficiency by using optimized charge coupling technology.

Features

- 100% avalanche tested
- RoHS Compliant
- Low Power Loss by High Speed Switching and Low On-Resistance

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	650	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current	5	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current	4	A
I_{DM}	Pulsed Drain Current	20	A
EAS	Single Pulse Avalanche Energy ¹	360	mJ
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation	35	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	62.5	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-case	3.5	$^\circ\text{C/W}$

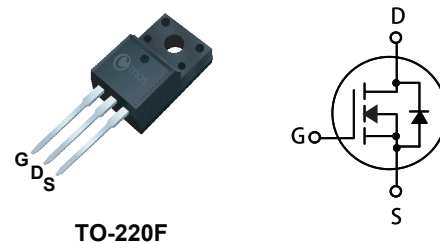
Product Summary

BVDSS	$R_{DS(on)}$ max.	ID
650V	3Ω	5A

Applications

- Charger
- Adaptor
- Power Supply
- Electrodeless lamp

TO-220F Pin Configuration



TO-220F

Type	Package	Marking
CMF5N65	TO-220F	CMF5N65

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	650	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V$, $I_D=2.5A$	---	---	3	Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=650V$, $V_{GS}=0V$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 30V$, $V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=15V$, $I_D=2.5A$	---	5	---	S
Q_g	Total Gate Charge	$I_D=4.5A$	---	15	---	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=520V$	---	2	---	
Q_{gd}	Gate-Drain Charge	$V_{GS}=10V$	---	7	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{GS}=10V$	---	11	---	ns
T_r	Rise Time	$V_{DD}=325V$	---	41	---	
$T_{d(off)}$	Turn-Off Delay Time	$I_D=4.5A$	---	40	---	
T_f	Fall Time	$R_G=25\Omega$	---	51	---	
C_{iss}	Input Capacitance	$V_{DS}=25V$, $V_{GS}=0V$, $f=1MHz$	---	730	---	pF
C_{oss}	Output Capacitance		---	55	---	
C_{rss}	Reverse Transfer Capacitance		---	7	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	5	A
I_{SM}	Pulsed Source Current		---	---	20	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=5A$, $T_J=25^{\circ}\text{C}$	---	0.88	1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is $V_{DD}=80V$, $V_{GS}=10V$, $L=20mH$, $I_{AS}=6A$.

This product has been designed and qualified for the consumer market.

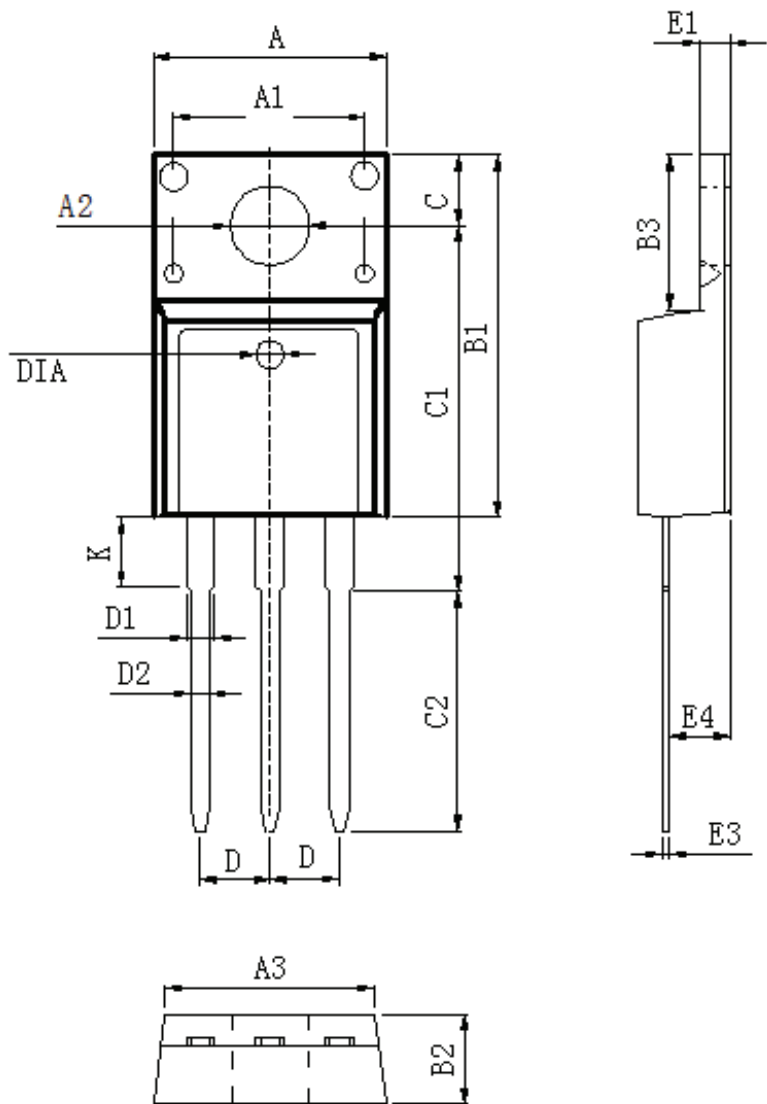
Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.Please refer to the latest version of specification.

Package Dimension

TO-220F

Unit :mm



DIM	MILLIMETERS
A	10.16±0.3
A1	7.00±0.1
A2	3.3±0.2
A3	9.5±0.2
B1	15.87±0.3
B2	4.7±0.2
B3	6.68±0.4
C	3.3±0.2
C1	12.57±0.3
C2	10.02±0.5
D	2.54±0.05
D1	1.28±0.2
D2	0.8±0.1
K	3.1±0.3
E1	2.54±0.1
E3	0.5±0.1
E4	2.76±0.2
DIA	⌀1.5 (deep 0.2)