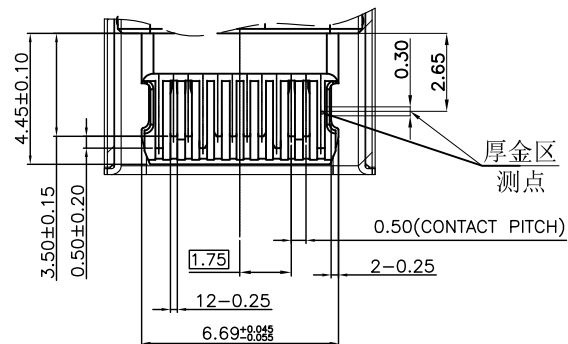
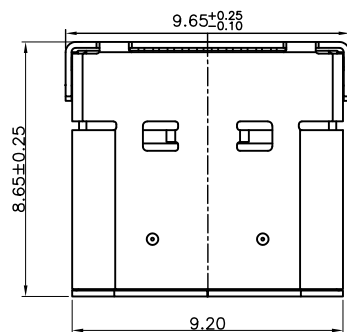
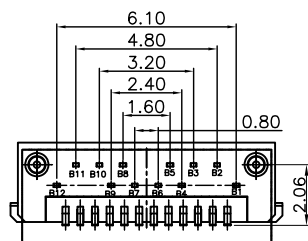
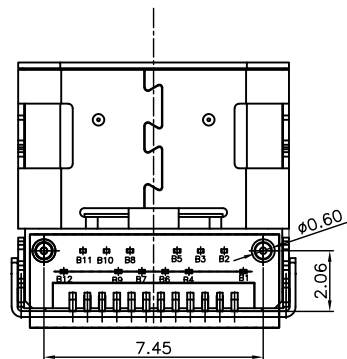
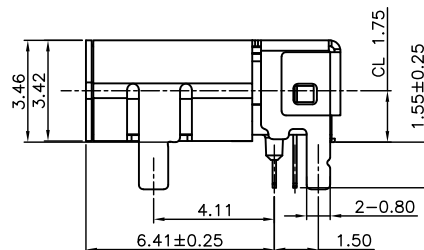
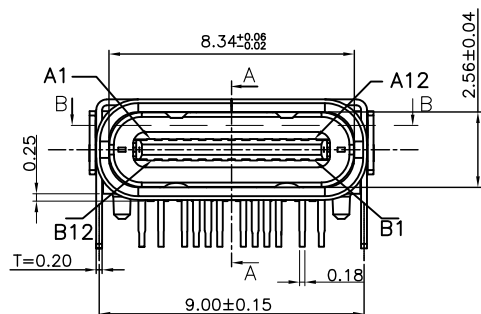
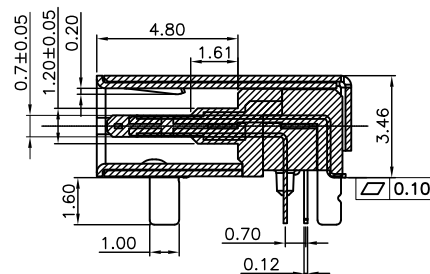




SECTION B-B

Detail A
Scale 1:1Section A-A
Scale 1:1

NOTES:

1.MATERIAL:

MOLDING: LCP UL94 V-0

CONTACT: COPPER ALLOY.

PLATED GOLD ON CONTACT AREA,
G/F GOLD ON SOLDER AREA,
50u" NICKEL PLATING OVER ALL.OUTER SHELL: SUS304-H, T=0.20±0.03mm
50u" NICKEL SOLDER AREA TAIL

INNER SHELL: SUS304, T=0.20±0.03mm

LATCH: SUS301, T=0.15±0.03mm

2.MECHANICAL:

INSERTION: 5~20N.

EXTRACTION: 8~20N.

DURABILITY: 10000 CYCLES

3.ELECTRICAL:

CURRENT: 5A MAX FOR VBUS;

1.25A FOR GND, 0.25A FOR OTHER OTHER.

VOLTAGE: 20VDC MAX

WITHSTANDING VOLTAGE: 100V AC.

CONTACT RESISTANCE: 40mΩ MAX.

INSULATION RESISTANCE: 100MΩ MIN.

4.ENVIRONMENTAL

TEMPERATURE RANGE -40°C ~ +85°C



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TOLERANCE:

X.X ±0.30

X.XX ±0.25

X.XXX ±0.15

X' ±2" X.X' ±0.5"

UNIT: mm [inch]

SCALE: 1:1 SIZE: A4

DRAWN BY :

陈一鸣

DATE :

2014-02-23

PART NAME:

USB TYPE C 母座24P U4款 板上DIP+SMT 双壳L=8.65

CHECKED BY:

马跃

DATE :

2014-02-23

PART NO.

HYCW662-USBC24-865B

MOLD NO.

APPROVED BY:

邱敏

DATE :

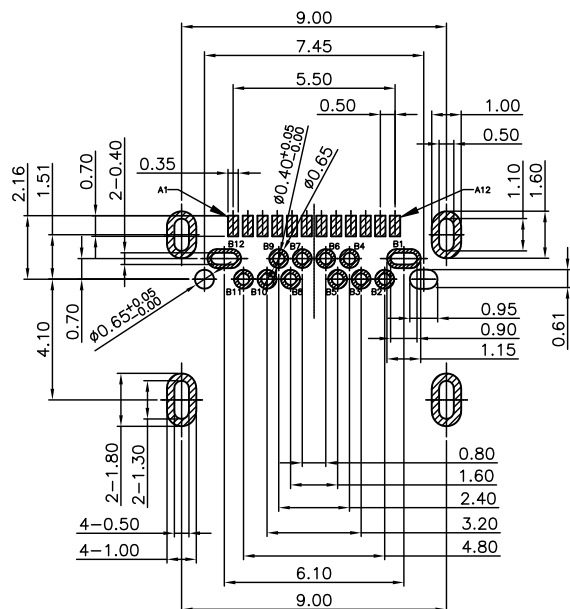
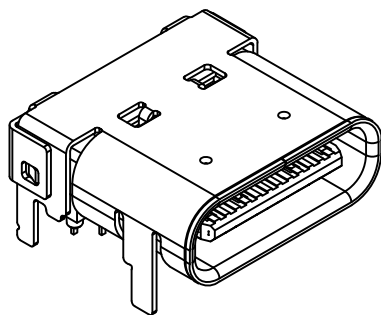
2014-02-23

DRAW NO:

HYC-2504151057

SHEET NO.

1 OF 1



RECOMMEND P.C.B LAYOUT (COMPONENT SIDE)

TOLERANCE FOR PCB LAYOUT IS ± 0.05

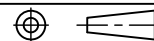
USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	VBUS	Bus Power	B9	VBUS	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use (SBU)	B5	CC2	Configuraation Channel
A9	VBUS	Bus Power	B4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return



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TOLERANCE:

X.X ± 0.30 X.XX ± 0.25 X.XXX ± 0.15 X' $\pm 2'$ X.X' $\pm 0.5'$ 

UNIT: mm [inch]

SCALE: 1:1 SIZE: A4

DRAWN BY :

陈一鸣

DATE :

2014-02-23

PART NAME:

USB TYPE C 母座24P GEN3 板上DIP+SMT 双壳L=8.65

CHECKED BY:

马跃

DATE :

2014-02-23

APPROVED BY:

邱敏

DATE :

2014-02-23

PART NO.

HYCW662-USBC24-865B

MOLD NO.

DRAW NO:

HYC-2504151057

SHEET NO.

1 OF 1