

1. Description

The PRTR5V0U2F,115(ES) is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

2. Features

- IEC 61000-4-2 Level 4 ESD Protection
 - $\pm 12\text{kV}$ Contact Discharge
 - $\pm 17\text{kV}$ Air Discharge
- 70W Peak pulse Power (8/20us)
- Low clamping voltage
- Working voltage: 5V
- Low leakage current
- RoHS compliant
- Protecting two unidirectional lines
- Ultra-low capacitance: 0.60pF Typ.

3. Applications

- USB 2.0
- Monitors and flat panel displays
- 10/100/1000 ethernet
- Notebook computers
- SIM ports
- ATM interface

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
PRTR5V0U2F,115(ES)	DFN1610-6L	.0522P	Halogen free	Tape & Reel	3,000 PCS	UL 94V-0	7 inches

Table-1 Ordering information

5. Pin Configuration and Functions

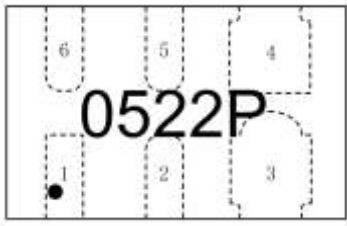
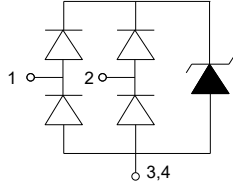
Pin	Name	Description	Outline	Circuit Diagram
1	IO1	Connect to IO	Note1 	
2	IO2	Connect to IO		
3,4	GND	Connect to GND		
5,6	NC	No connection		

Table-2 Pin configuration

Note1:

This diagram is only an electrical schematic, and the actual pin size is based on POD.

6. Specification

6.1. Absolute Maximum rating

Over operating free-air temperature range (unless otherwise noted)

Parameters	Symbol	Min.	Max.	Unit
Peak pulse power (tp=8/20us)@25°C	P _{pk}	-	70	W
Peak pulse current (tp=8/20us)@25°C	I _{PP}		4.5	A
ESD (IEC61000-4-2 air discharge) @25°C	V _{ESD}	-	±17	kV
ESD (IEC61000-4-2 contact discharge) @25°C	V _{ESD}	-	±12	kV
Junction temperature	T _J	-	150	°C
Operating temperature	T _{OP}	-40	125	°C
Storage temperature	T _{STG}	-55	150	°C
Lead temperature	T _L	-	260	°C

Table-3 Absolute Maximum rating

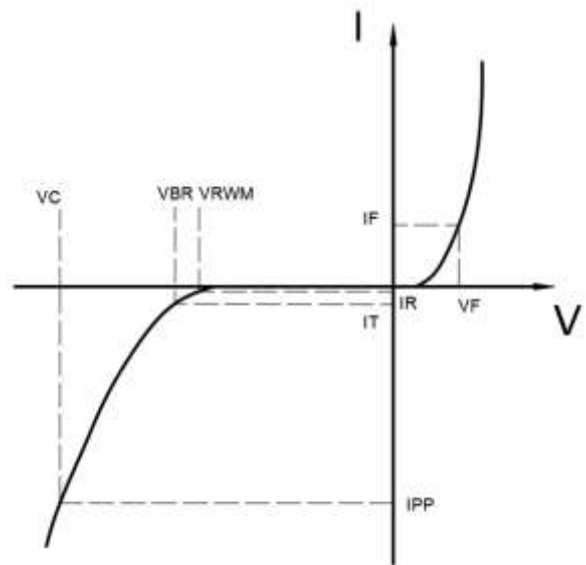
6.2. Electrical Characteristics

At TA = 25°C unless otherwise noted

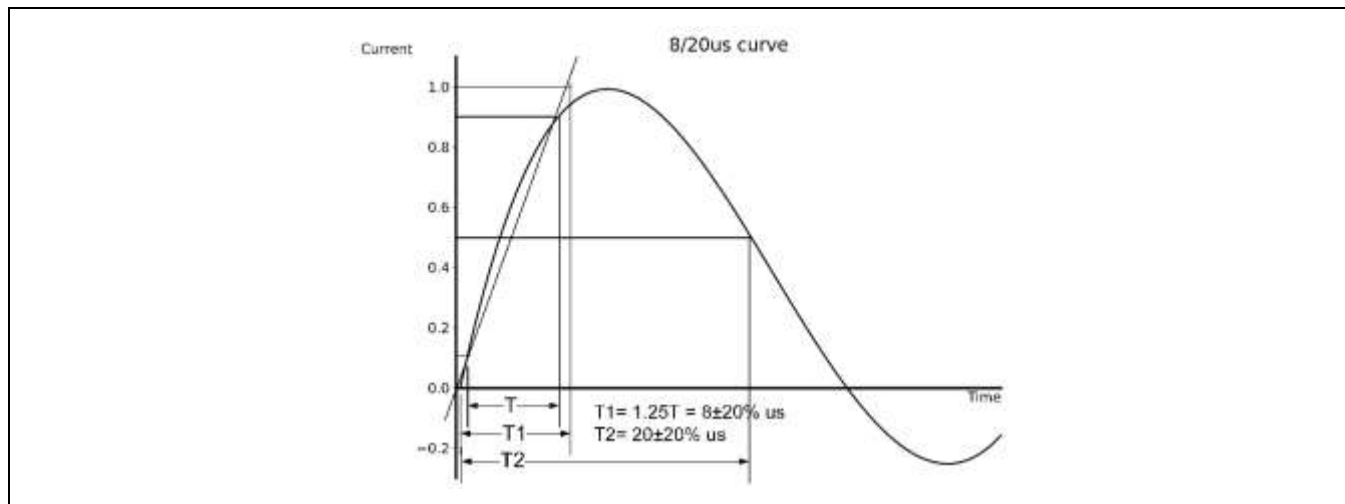
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-off Voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1mA$	6		9.5	V
Reverse Leakage Current	I_R	$V_{RWM}=5V$			1.0	uA
Clamping Voltage	V_C	$I_{PP}=1A$; $t_p=8/20us$		8.5	10.0	V
Clamping Voltage	V_C	$I_{PP}=4.5A$; $t_p=8/20us$		12.0	15.0	V
Junction Capacitance	C_J	I/O to GND; $V_R=0V$; $f=1MHz$		0.6	0.8	pF
		Between I/O; $V_R=0V$; $f=1MHz$		0.3	0.4	pF

Table-4 Electrical Characteristics

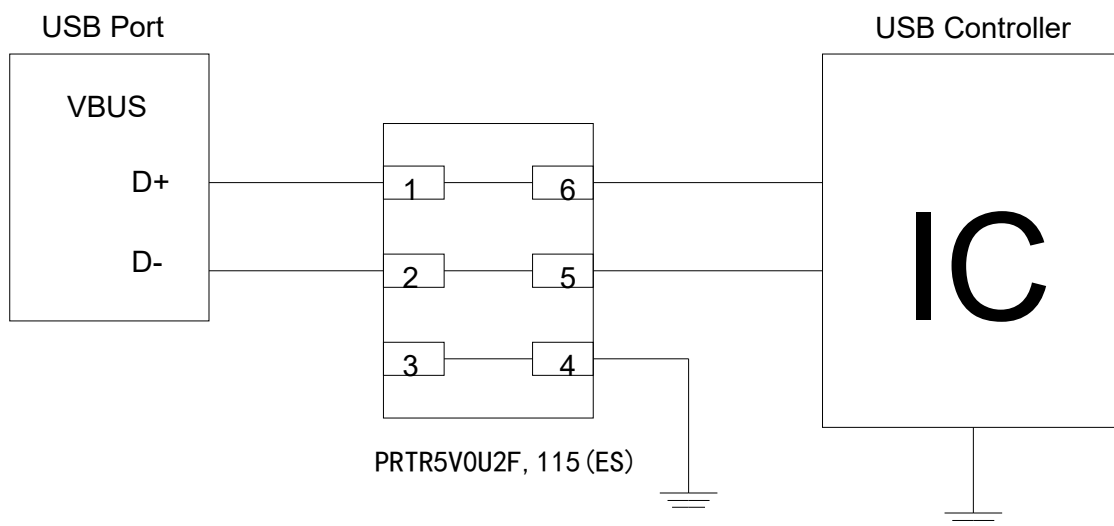
Symbol	Parameters
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
I_F	Forward Current
V_F	Forward Voltage @ I_F



7. Typical Characteristic



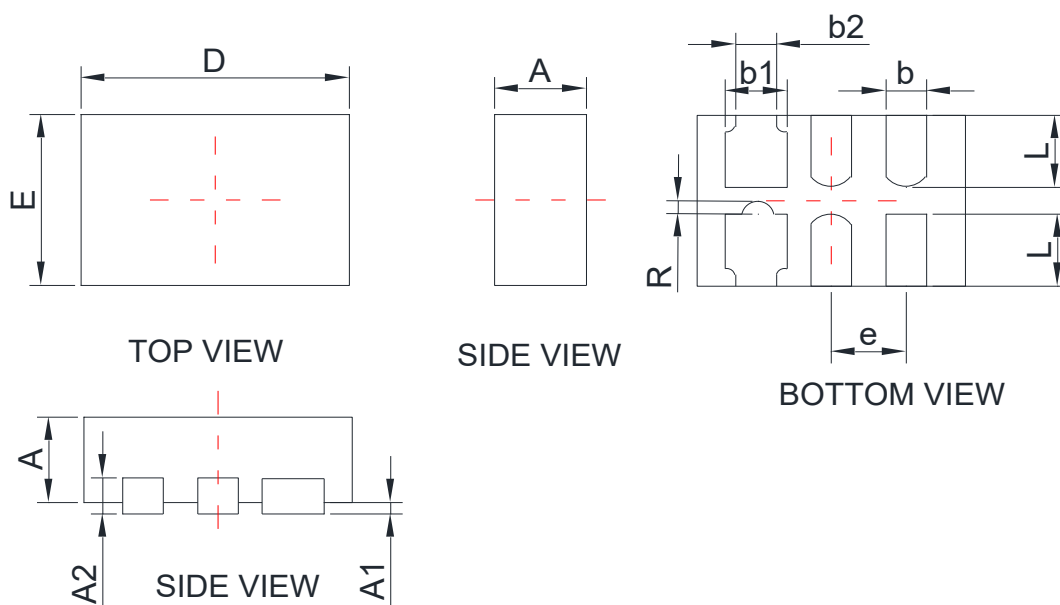
8. Typical Application



Typical Interface Application

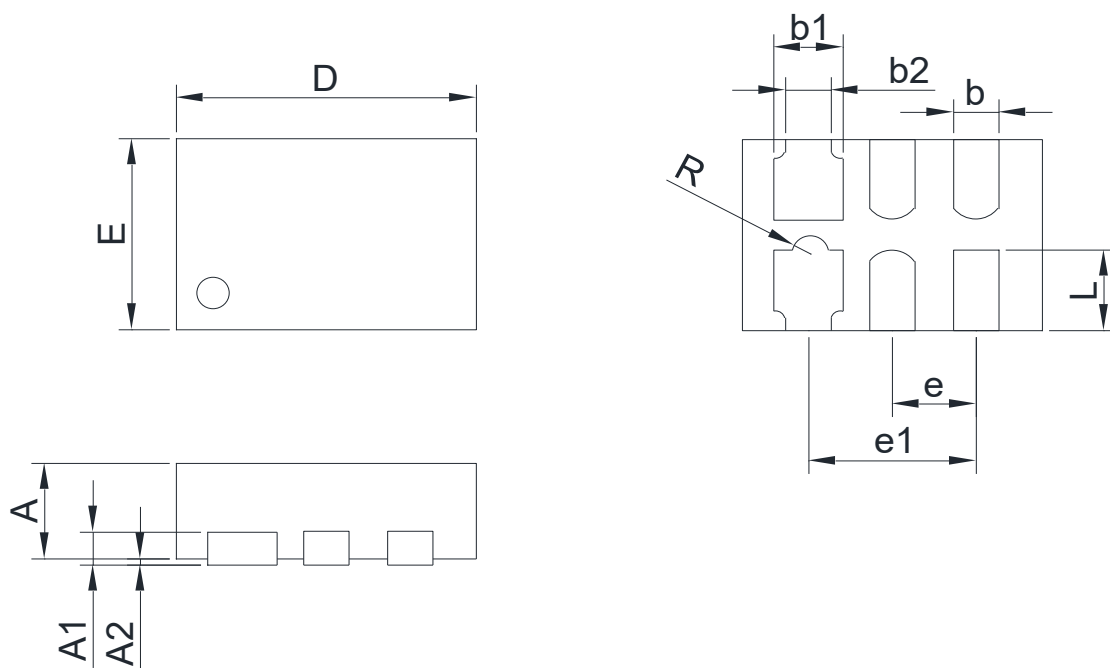
9. Dimension (DFN1610-6L)

POD A(A)



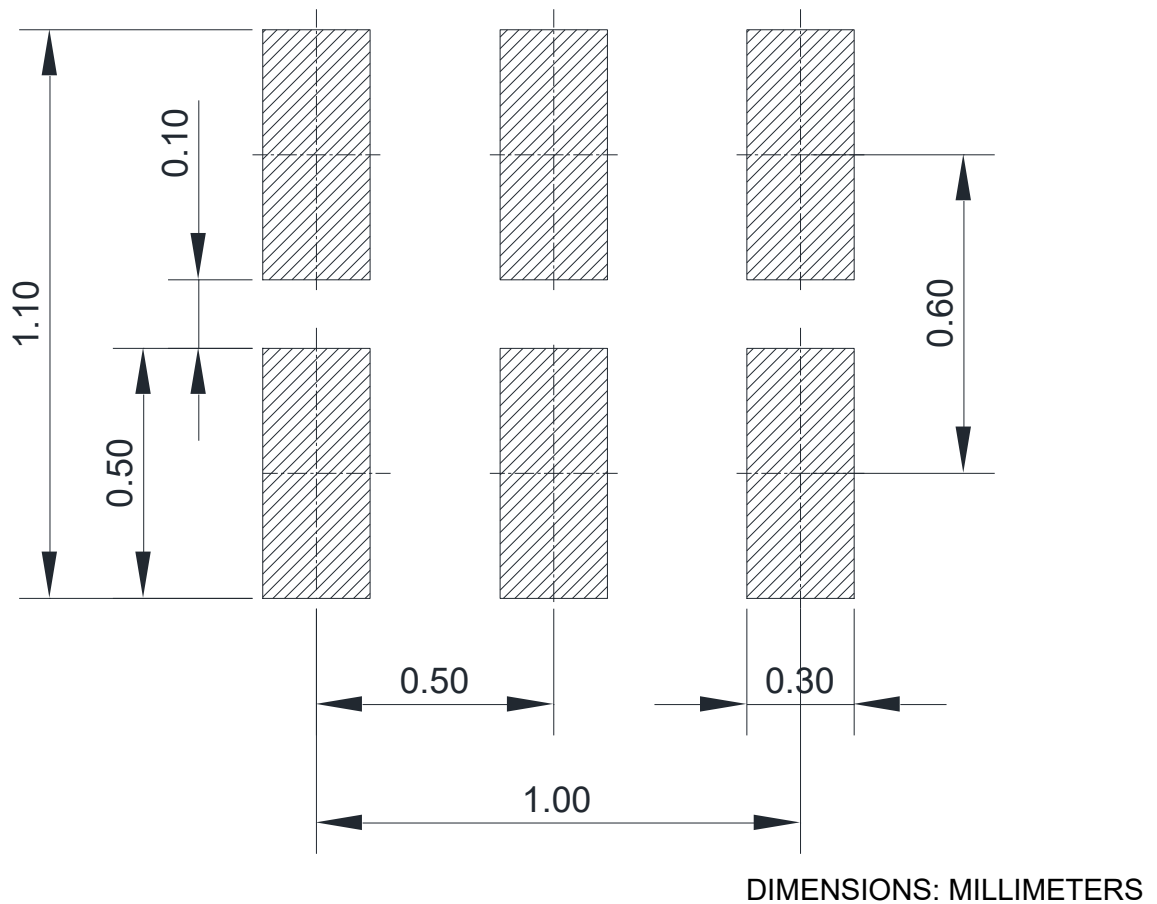
Common Dimensions (mm)			
Symbols	Min	Nom	Max
A	0.50	0.55	0.60
A1	0.00	0.025	0.05
A2	0.15REF		
D	1.55	1.60	1.65
E	0.95	1.00	1.05
b	0.15	0.20	0.25
b1	0.35	0.40	0.45
b2	0.3REF		
L	0.33	0.38	0.43
e	0.50BSC		
R	0.12REF		

POD B(T)



Common Dimensions (mm)			
Symbols	Min	Nom	Max
D	1.55	1.60	1.65
E	0.95	1.00	1.05
L	0.33	0.38	0.43
b	0.15	0.20	0.25
b1	0.35	0.40	0.45
b2	0.25	0.30	0.35
e	0.50BSC		
e1	1.00BSC		
R	0.125BSC		
A	0.45	0.50	0.55
A1	0.152REF		
A2		0.00	0.05

10. Recommended Soldering Footprint



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