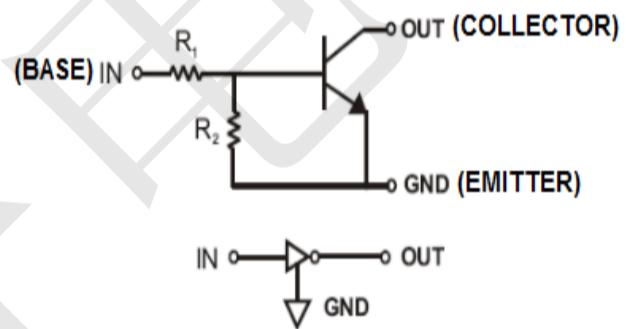
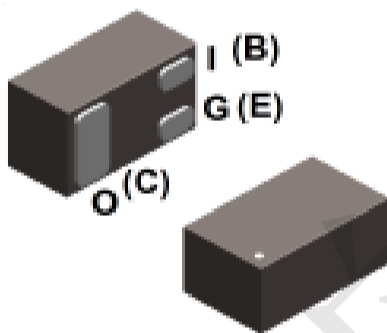


Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Mechanical Data

- Case: DFN1006-3
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208



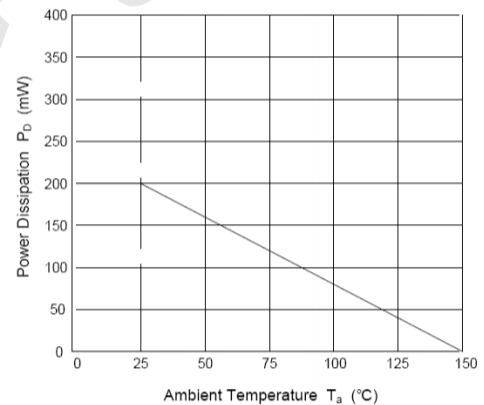
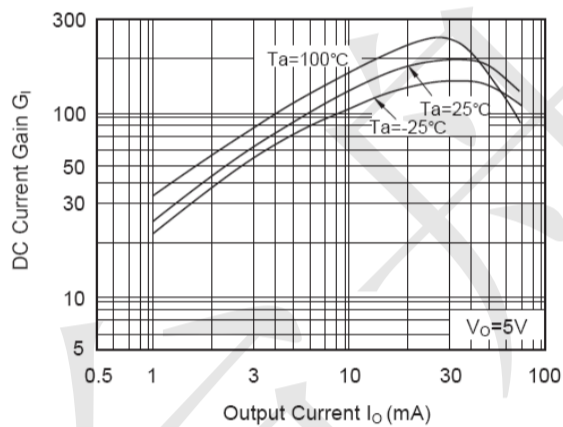
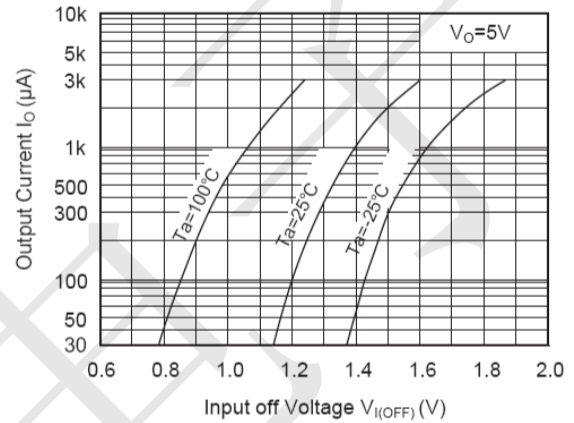
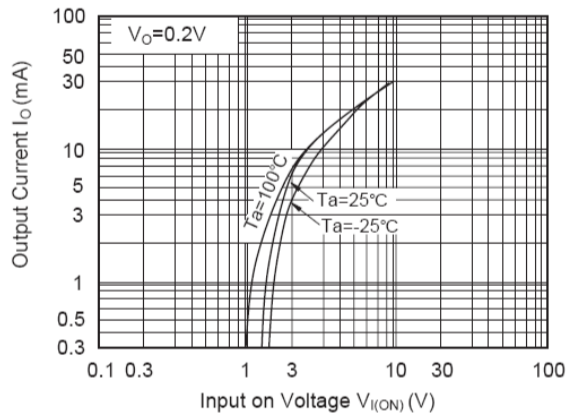
Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Output Voltage	V_O	50	V
Input Voltage	V_I	40,-10	V
Output Current	I_O	100	mA
Maximum Power Dissipation	P_D	200	mW
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (T_A=25°C)

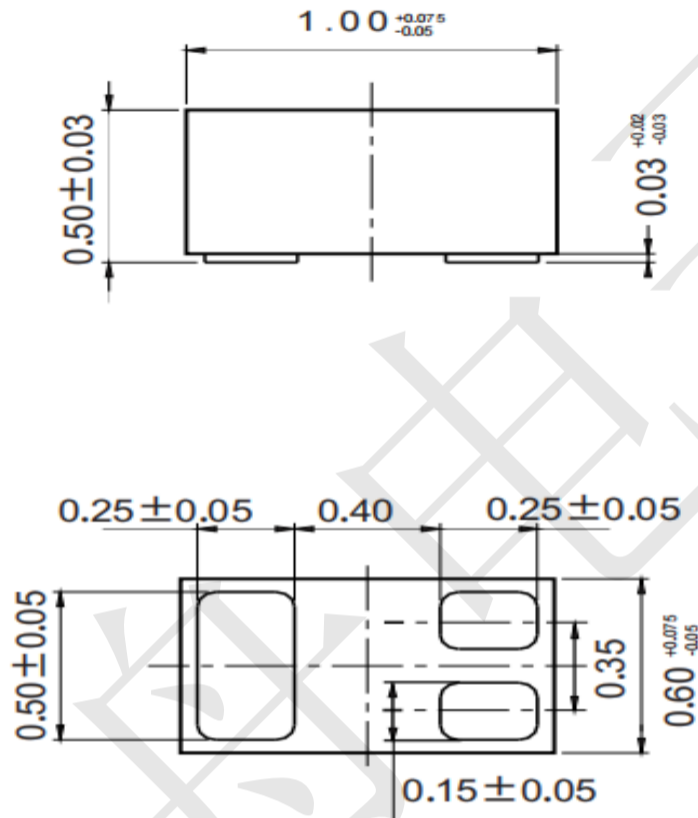
Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at V _O = 5 V, I _O = 10 mA	G _I	70	--	--	--
Output Cutoff Current at V _O = 50 V	I _{O(OFF)}	--	--	500	nA
Input Cutoff Current at V _I = 5 V	I _I	--	--	0.36	mA
Output Voltage (ON) at I _O = -10 mA, I _I = 0.5 mA	V _{O(ON)}	--	--	0.3	V
Input Voltage (ON) at V _O = 0.2 V, I _O = 5 mA	V _{I(ON)}	--	--	3	V
Input Voltage (OFF) at V _{CC} = 5 V, I _O = 0.1 mA	V _{I(OFF)}	1	--	--	V
Transition Frequency at V _O = 10 V, I _O = 5 mA	f _T	--	200	--	MHz

Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)



Package Outline Dimensions (unit:mm)

DFN1006-3



Mounting Pad Layout (unit: mm)

