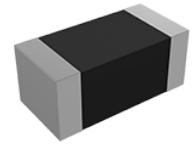


BLM4532PG-121T90

Ultra-High Current Multilayer Chip Ferrite Beads

FEATURES

- Monolithic inorganic material construction.
- Closed magnetic circuit avoids crosstalk.
- Low DC resistance structure of electrode to prevent wasteful electric power consumption.
- Operate temperature range $-55^{\circ}\text{C} \sim +125^{\circ}\text{C}$ (Including self temp. rise)
- RoHS compliant



APPLICATIONS

- To remove noise from mobile devices and various components such as smartphones and tablet terminals, as well as from home appliances such as PCs, recorders, STBs, smart grids and industrial machinery.

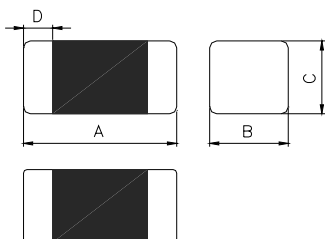
Explanation of Part Number

BLM 4532 PG -121 T 90

1 2 3 4 5 6

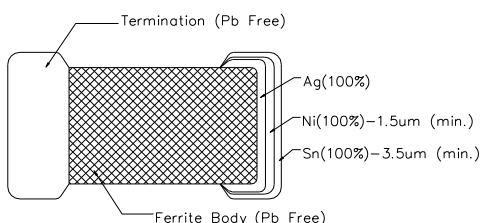
- ◆ 1:Product Series:Ultra-High Current Multilayer Chip Ferrite Beads
- ◆ 2:Dimensions:
- ◆ 3: Characteristics:
- ◆ 4:Nominal Impedance:121=120Ω
- ◆ 5:Packing:Tape Carrier Package
- ◆ 6:Rate Current: 90=9000mA

SHAPE AND DIMENSIONS(Units:mm)



Chip Size	
A	4.50±0.20
B	3.20±0.20
C	1.50±0.20
D	0.50±0.30

Product Structure

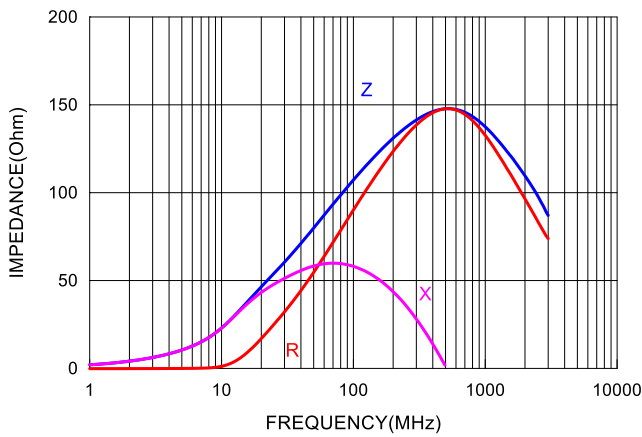


Electrical Properties:

Part Number	Impedance (Ω)	Test Frequency (Hz)	DC Resistance (Ω) max.	Rated Current (mA) max.
BLM4532PG-121T90	120 \pm 25%	60mV/100M	0.006	9000

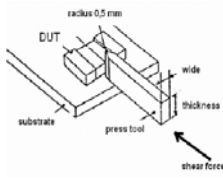
- Rated current: based on temperature rise test
- In compliance with EIA 595

Impedance Frequency Characteristics(Typical)



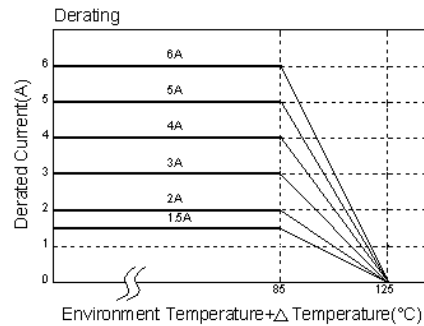
Reliability and Test Condition

Item	Performance	Test Condition				
Series No.	BLM4532PG	--				
Operating Temperature	-55~+125℃ (Including self-temperature rise)	--				
Transportation Storage Temperature	-55~+125℃ (on board)	For long storage conditions, please see the Application Notice				
Impedance (Z)	Refer to standard electrical characteristics list	Agilent4291 Agilent E4991 Agilent4287 Agilent16192				
DC Resistance		Agilent 4338				
Rated Current		DC Power Supply Over Rated Current requirements, there will be some risk				
Temperature Rise Test	Rated Current < 1A ΔT 20℃Max Rated Current ≧ 1A ΔT 40℃Max	1. Applied the allowed DC current. 2. Temperature measured by digital surface thermometer.				
Life test	Appearance: no damage. Impedance: within±15%of initial value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Temperature: 125±2℃ Applied current: rated current. Duration: 1000±12hrs. Measured at room temperature after placing for 24±2 hrs.				
Load Humidity		Preconditioning: Run through reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2℃. Duration:1000hrsMin. Bead:with100%ratedcurrent Inductance: with 10% rated current Measured at room temperature after placing for 24±2 hrs.				
Thermal shock	Appearance: no damage. Impedance: within±15%of initial value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Condition for 1 cycle Step1: -55±2℃ 30±5 min. Step2: 125±2℃ ≅ 0.5min Step3: 125±2℃ 30±5min. Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs.				
Vibration	Appearance : No damage. Impedance : within±15% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency:10Hz~2KHz~10Hz for 20 minutes Equipment : Vibration checker Total Amplitude:10g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations) °				
Bending	Appearance : No damage. Impedance : within±10% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805inch(2012mm):40x100x1.2mm <0805inch(2012mm):40x100x0.8mm Bending depth: >=0805inch(2012mm):1.2mm <0805inch(2012mm):0.8mm Duration of 10 sec for a min.				
Shock	Appearance : No damage. Impedance : within±10% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	Test condition:				
		Type	Peak Value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vj)ft/sec
		SMD	50	11	Half-sine	11.3
		Lead	50	11	Half-sine	11.3

Item	Performance	Test Condition						
Solderability	More than 95% of the terminal electrode should be covered with solder.	a. Method B, 4 hrs @155°C dry heat @235°C±5°C Test time: 5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8 hours ± 15 min) @ 260°C±5°C Test time: 30 +0/-0.5 seconds.						
Resistance to Soldering Heat	Appearance : No damage. Impedance : within ±15% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	Number of heat cycles: 1 <table border="1"> <thead> <tr> <th>Temperature (°C)</th><th>Time (s)</th><th>Temperature ramp/immersion and emersion rate</th></tr> </thead> <tbody> <tr> <td>260 ±5 (solder temp)</td><td>10 ±1</td><td>25mm/s ±6 mm/s</td></tr> </tbody> </table> Depth: completely cover the termination	Temperature (°C)	Time (s)	Temperature ramp/immersion and emersion rate	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s
Temperature (°C)	Time (s)	Temperature ramp/immersion and emersion rate						
260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s						
Terminal strength	Appearance : No damage. Impedance : within ±15% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	 Preconditioning: Run through reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Component mounted on a PCB apply a force >0.805inch(2012mm):1kg <=0.805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.						

**Derating Curve

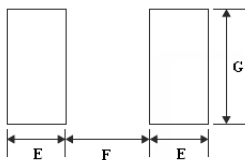
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85°C, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



Soldering and Mounting

1. Recommended PC Board Pattern

Chip Size						Land Patterns For Reflow Soldering		
Series	Type	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)
BLM4532PG	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

2. Soldering

Mildly activated rosin fluxes are preferred. Metal-lions terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

2.1 Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 350°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

Fig.1 Soldering Reflow

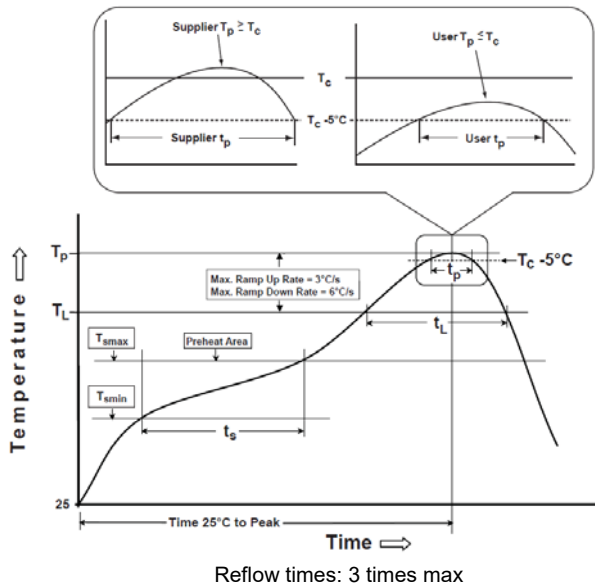


Fig.2 Iron soldering temperature profiles

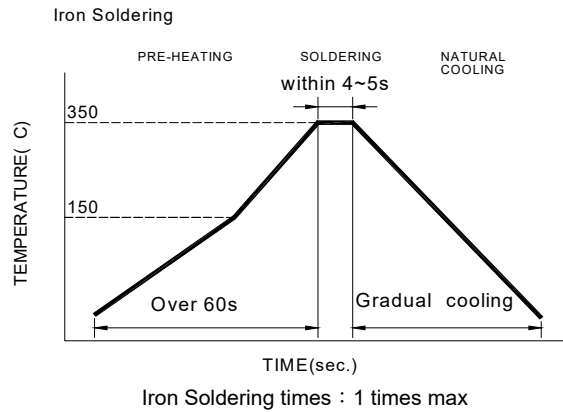


Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min(T_{smin})	150°C
-Temperature Max(T_{smax})	200°C
-Time(t_s)from(T_{smin} to T_{smax})	60-120seconds
Ramp-up rate(T_L to T_p)	3°C/second max.
Liquidus temperature(T_L)	217°C
Time(t_L)maintained above T_L	60-150 seconds
Classification temperature(T_c)	See Table (1.2)
Time(t_p) at $T_c - 5^\circ\text{C}$ (T_p should be equal to or less than T_c .)	< 30 seconds
Ramp-down rate(T_p to T_L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

T_p : maximum peak package body temperature, T_c : the classification temperature.

For user (customer) T_p should be equal to or less than T_c .

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

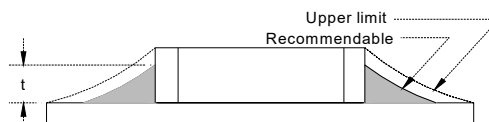
	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E .

2.3 Solder Volume:

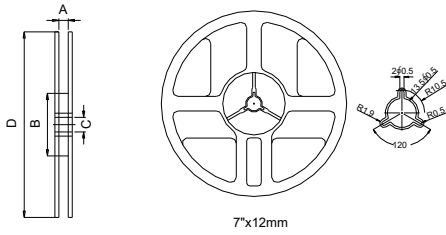
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



Packaging Information

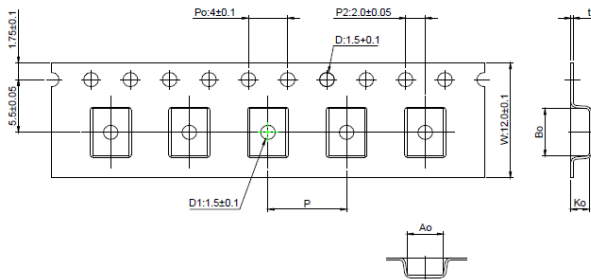
1. Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2

2. Tape Dimension / 12mm

■ Material of taping is plastic

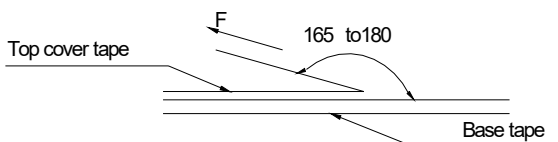


Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

3. Packaging Quantity

Chip Size	BLM4532PG
Chip / Reel	1000
Inner box	4000
Middle box	20000
Carton	400000

4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

- Storage Conditions(component level)
To maintain the solder ability of terminal electrodes:
 1. Metal-lions products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
 3. Recommended products should be used within 12 months from the time of delivery.
 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.