MSKSEMI 美森科













TC

MOV

GDT

PIFD

1SS181-MS

Product specification





FEATURE

- Small package
- Low forward voltage
- Fast reverse recovery time
- Small total capacitance

Applications

Ultra high speed switching application

Reference News

SOT-23	Pin Configuration	MARKING
1 2	3 1 2	A3

Absolute Maximum Ratings (Ta = 25 °C)

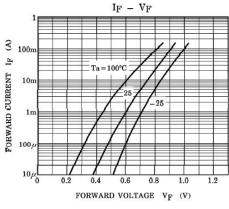
Parameter	Symbol	Value	Unit
Maximum Peak Reverse Voltage	V _{RM}	85	V
Reverse Voltage	V _R	80	V
Average Rectified Forward Current	I _{F(AV)}	100	mA
Maximum Peak Forward Current	I _{FM}	300	mA
Non-repetitive Peak Forward Surge Current (10 ms)	I _{FSM}	2	А
Power Dissipation	P _{tot}	350	mW
Junction Temperature	T _j	150	°C
Storage Temperature Range	T _{stg}	- 55 to + 150	°C

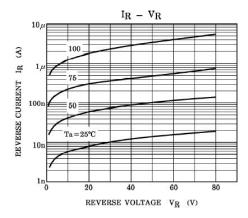
Characteristics at T_a = 25℃

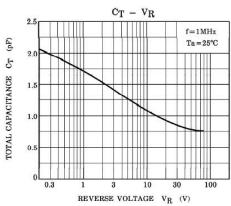
Parameter	Symbol	Max.	Unit
Forward Voltage at I _F = 100 mA	V _F	1.2	V
Reverse Current at $V_R = 30 \text{ V}$ at $V_R = 80 \text{ V}$	l _R	0.1 0.5	μΑ
Total Capacitance at V _R = 0 , f = 1 MHz	Ст	4	pF
Reverse Recovery Time at I _F = 10 mA	t _{rr}	4	ns



Typical Characteristics







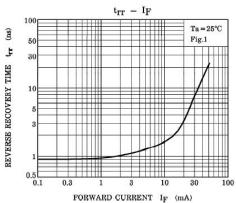
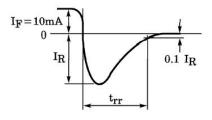


Fig.1 Reverse recovery time (t_{rr}) test circuit

INPUT WAVEFORM

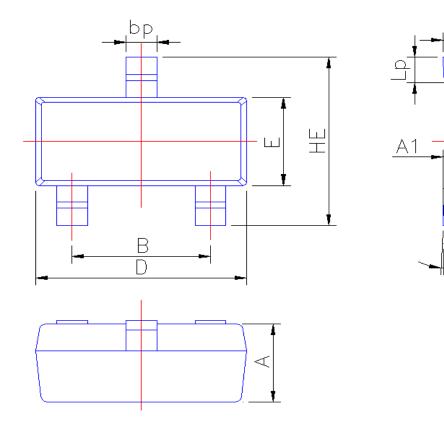
$\begin{array}{c|c} 0.01\mu F & DUT \\ \hline -6V & & & & & \\ -6V & & & & & \\ \hline -6V & & & & & \\ -6V & & & & & \\ \hline -6V & & & & & \\ -6V & & & & & \\ \hline -6V & & & & & \\ -6V & & & & & \\ \hline -6V & & & & & \\ -6V & & &$

OUTPUT WAVEFORM





PACKAGE MECHANICAL DATA



SOT-23

Symbol	Dimension in Millimeters		
Symbol	Min	Max	
A	0.90	1.10	
A1	0.013	0.100	
В	1.80	2.00	
bp	0.35	0.50	
С	0.09	0.150	
D	2.80	3.00	
Е	1.20	1.40	
HE	2.20	2.80	
Lp	0.20	0.50	
θ	0°	5°	

REELSPECIFICATION

P/N	PKG	QTY
1SS181-MS	SOT-23	3000



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SOT-23





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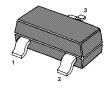
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Silicon Epitaxial Planar Switching Diode

Features

- Small package
- Low forward voltage
- Fast reverse recovery time
- Small total capacitance





Marking Code: A3 SOT-23 Plastic Package

Applications

• Ultra high speed switching application

Absolute Maximum Ratings (T_a = 25 °C)

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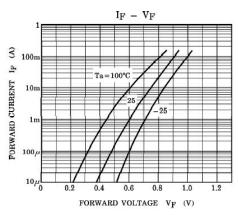


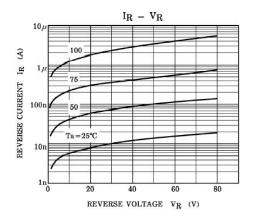
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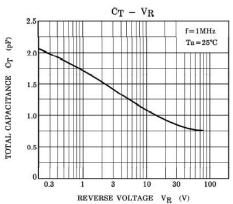




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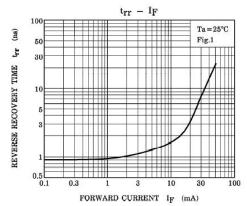
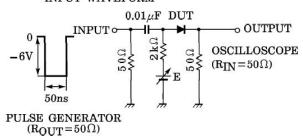


Fig.1 Reverse recovery time (t_{rr}) test circuit

INPUT WAVEFORM



OUTPUT WAVEFORM

