

N-Channel Enhancement Mode Power MOSFET

Description

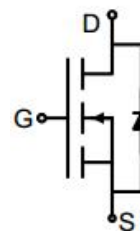
The GT115N15TL uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

General Features

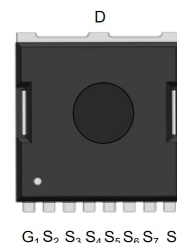
- V_{DS} 150V
- I_D (at $V_{GS} = 10V$) 105A
- $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 11.5m Ω
- 100% Avalanche Tested
- RoHS Compliant

Application

- Power switch
- DC/DC converters



Schematic diagram



TOLL-8L

Ordering Information

Device	Package	Marking	Packaging
GT115N15TL	TOLL-8L	GT115N15	2000pcs/Reel

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	150	V
Continuous Drain Current	I_D	105	A
		66	
Pulsed Drain Current (note1)	I_{DM}	420	A
Gate-Source Voltage	V_{GS}	± 20	V
Power Dissipation	P_D	190	W
Single pulse avalanche energy (note2)	E_{AS}	156	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	52	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Case	R_{thJC}	0.66	$^\circ\text{C/W}$

Specifications $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	150	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =150 V, V _{GS} =0V	--	--	1	μA
Gate-Source Leakage	I _{GSS}	V _{GS} = ±20V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.0	3.1	4.0	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 40A	--	9.1	11.5	mΩ
Forward Transconductance	g _{FS}	V _{DS} = 10V, I _D = 40A	--	43	--	S
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 75V, f = 1.0MHz	--	3300	--	pF
Output Capacitance	C _{oss}		--	260	--	
Reverse Transfer Capacitance	C _{rss}		--	5	--	
Total Gate Charge	Q _g	V _{DD} = 75V, I _D = 40A, V _{GS} = 10V	--	45	--	nC
Gate-Source Charge	Q _{gs}		--	15	--	
Gate-Drain Charge	Q _{gd}		--	9	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 75V, I _D = 40A, R _G = 3Ω	--	16	--	ns
Turn-on Rise Time	t _r		--	12	--	
Turn-off Delay Time	t _{d(off)}		--	30	--	
Turn-off Fall Time	t _f		--	18	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I _S	T _C = 25°C	--	--	105	A
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 40A, V _{GS} = 0V	--	--	1.2	V
Reverse Recovery Charge	Q _{rr}	I _F = 40A, V _{GS} = 0V di/dt=100A/us	--	182	--	nC
Reverse Recovery Time	T _{rr}		--	76	--	ns

Notes

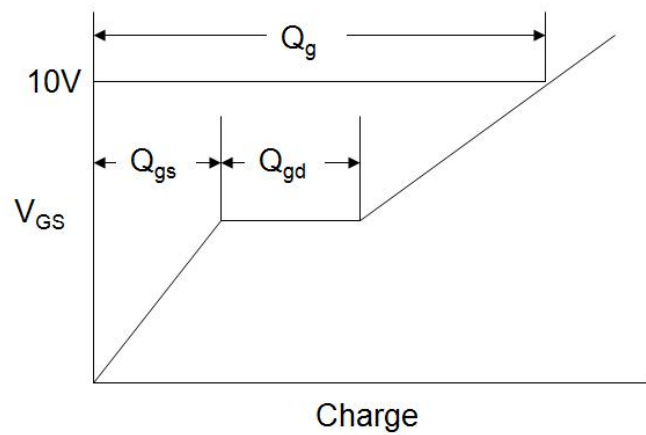
1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. EAS condition : $T_J = 25^{\circ}\text{C}, V_{DD} = 50V, V_{GS} = 10V, L = 0.5mH, R_G = 25\Omega$

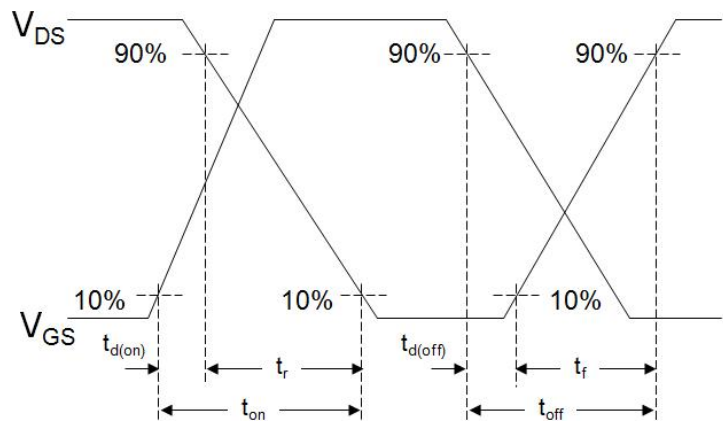
The table shows the minimum avalanche energy, which is 420mJ when the device is tested until failure

3. Identical low side and high side switch with identical R_G

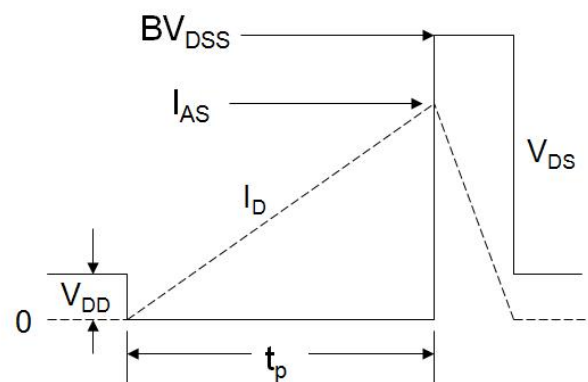
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

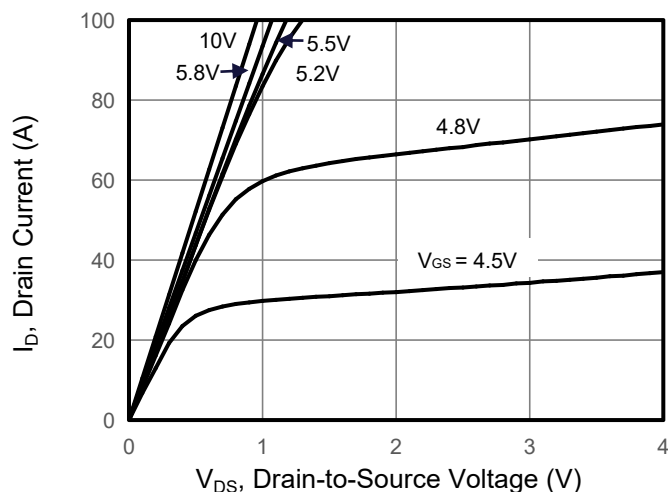


Figure 2. Transfer Characteristics

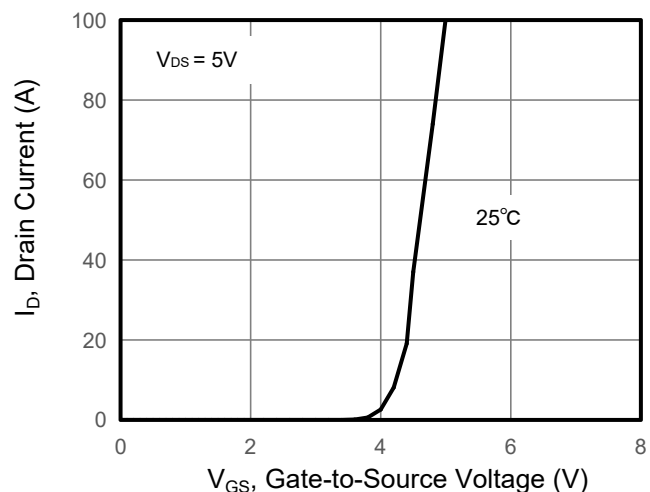


Figure 3. Drain Source On Resistance

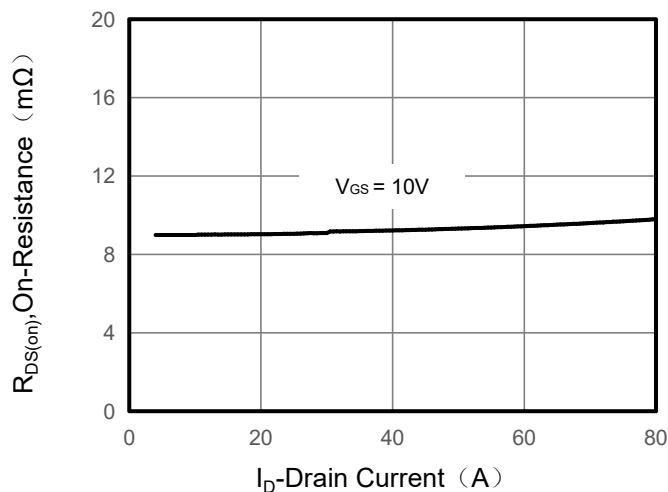


Figure 4. Gate Charge

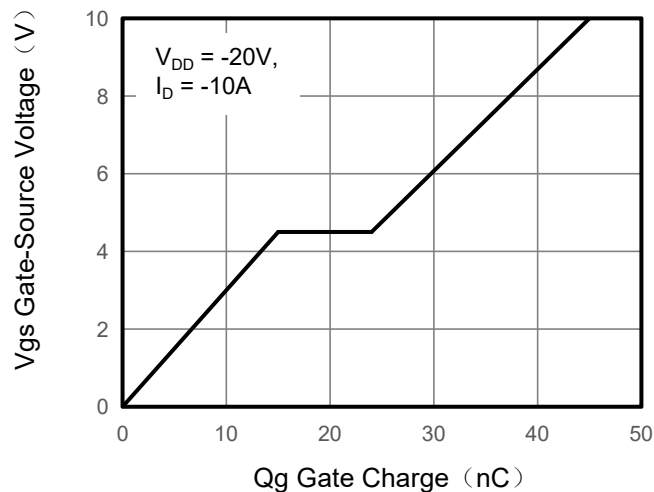


Figure 5. Capacitance

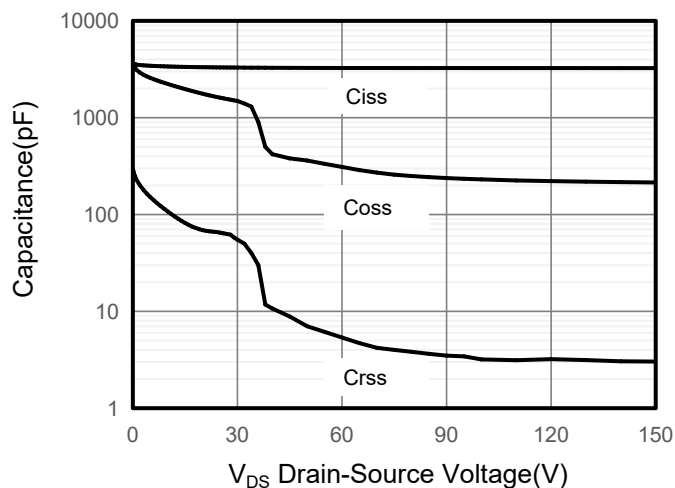
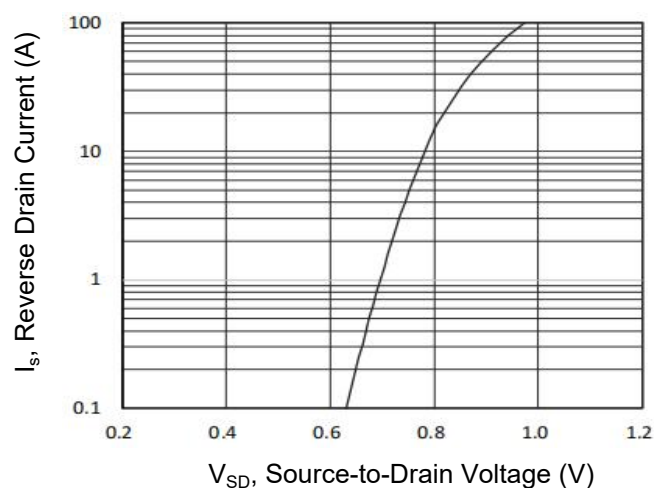


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

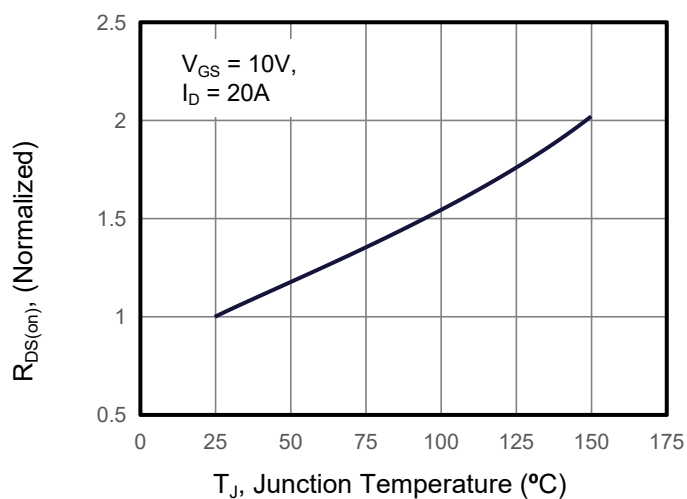


Figure 8. Safe Operation Area

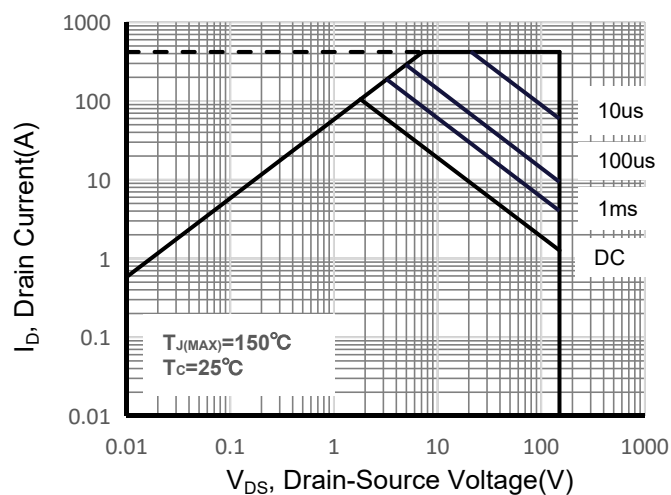


Figure 9. Maximum Continuous Drain Current vs Case Temperature

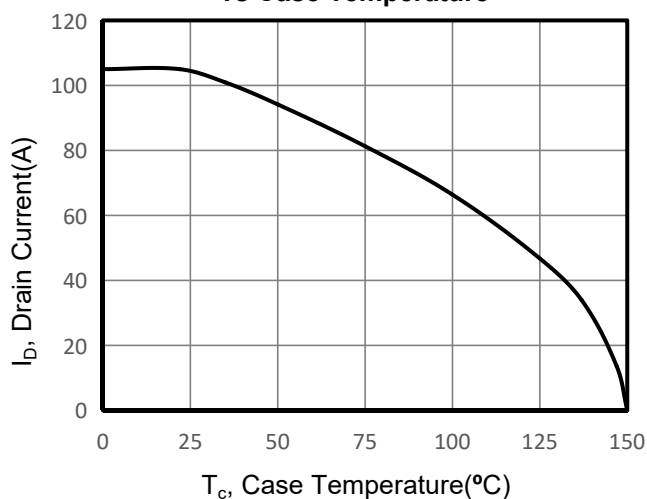
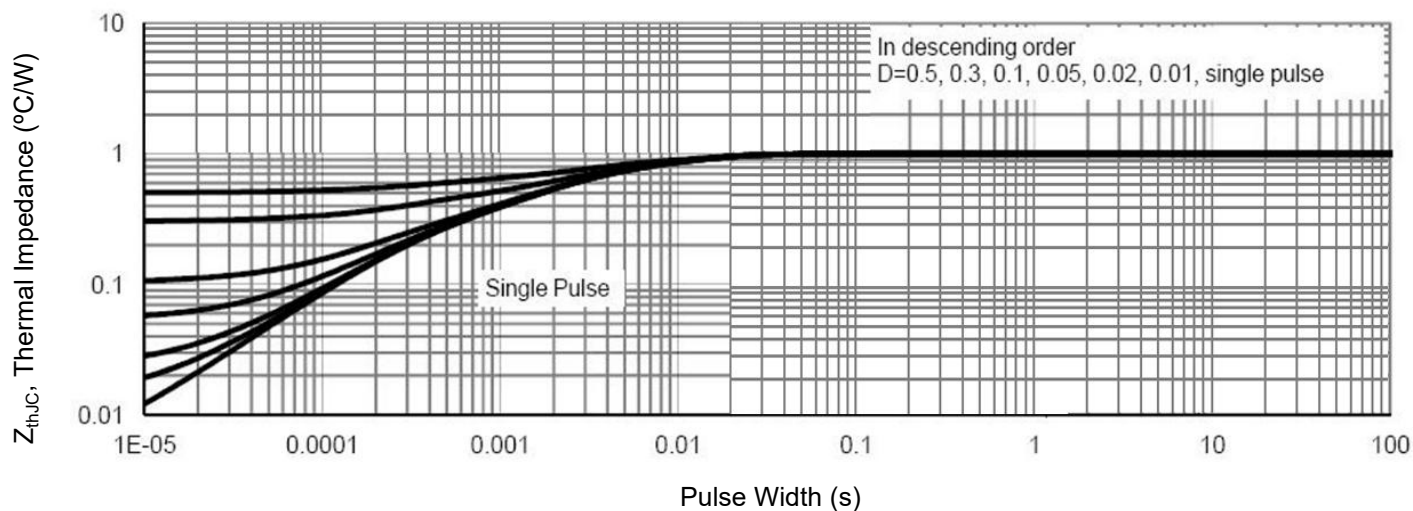
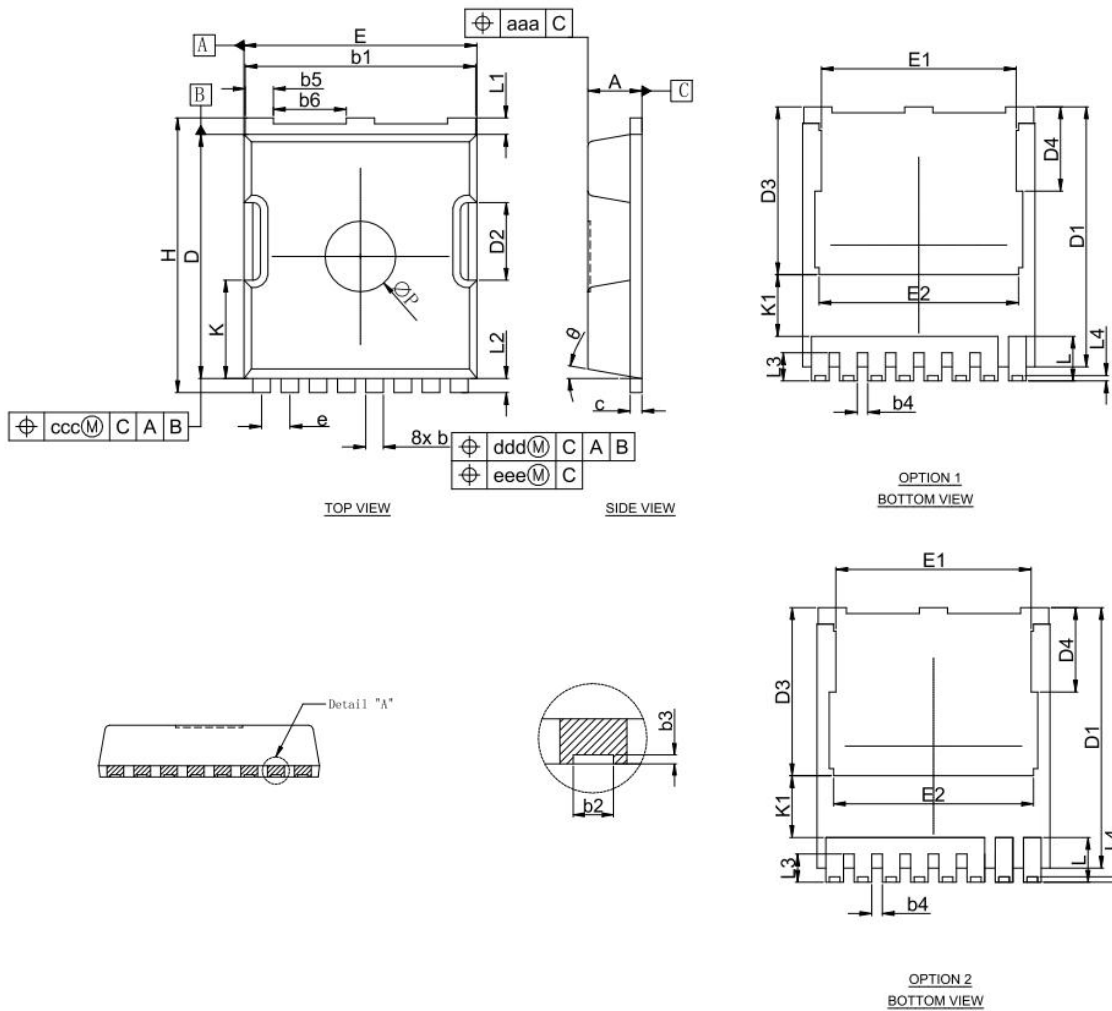


Figure 10. Normalized Maximum Transient Thermal Impedance



TOLL-8L Package Information



SYMBOL	COMMON			SYMBOL	COMMON		
	MILLIMETER				MILLIMETER		
	MIN.	NOMINAL	MAX.		MIN.	NOMINAL	MAX.
A	2.20	2.30	2.40	E1	8.20	8.30	8.40
b	0.70	0.80	0.90	E2	8.35	8.50	8.65
b1	9.70	9.80	9.90	H	11.50	11.68	11.85
b2	0.36	0.45	0.55	K	4.08	4.18	4.28
b3	0.05	0.10	0.35	K1	2.45	---	---
b4	0.30	0.40	0.50	L	1.60	1.90	2.10
b5	1.10	1.20	1.30	L1	0.50	0.70	0.90
b6	3.00	3.10	3.20	L2	0.50	0.60	0.70
c	0.40	0.50	0.60	L3	1.00	1.20	1.30
D	10.28	10.38	10.55	L4	0.13	0.23	0.33
D1	10.98	11.08	11.18	P	2.85	3.00	3.15
D2	3.20	3.30	3.40	θ	10° REF		
D3	7.00	7.15	7.30	aaa	0.20		
D4	3.44	3.59	3.74	ccc	0.20		
e	1.10	1.20	1.30	ddd	0.25		
E	9.80	9.90	10.00	eee	0.20		