

1. Description

The TLP351A is a gate driven optocoupler with an output current of 0.6A, with an AlGaAs LED, which is coupled to a photosensitive integrated circuit through infrared light. This optocoupler can drive most low-power IGBTs and MOSFETs. In the motor control inverter and high-performance power system applications, it is very suitable for fast switching drive power IGBTs and MOSFETs.

3. Features

- 30kV/ μ s minimum Common Mode Rejection
- 0.6A maximum peak output current
- Wide operating V_{CC} Range: 10V~30V

2. Applications

- Uninterrupted Power Supply
- IGBT isolation / power MOSFET gate drive
- Induction heating
- Industrial inverters

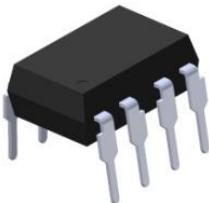
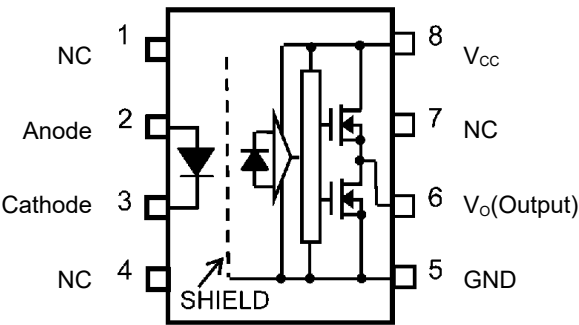
- 400ns maximum propagation delay
- 100ns of pulse width distortion
- Operating temperature range: -40°C~ +125°C

4. Truth Table

Input	LED	M1	M2	Output
H	ON	ON	OFF	H
L	OFF	OFF	ON	L



5.Pinning Information



DIP-8



SOP- 8

Note: 0.1uF bypass capacitor must be connected between pins 5 and 8.

6.Insulation And Safety Related Specifications

Parameter	Symbol	Note	Value	Unit
Creepage Distance	L	Measured from input terminals to output terminals, shortest distance path along body	≥7	mm
Clearance Distance	L	Measured from input terminals to output terminals, shortest distance through air	≥7	mm
Insulation Thickness	DTI	Insulation thickness between emitter and detector	≥0.4	mm
Peak Isolation Voltage	V _{IORM}	DIN/EN/IEC EN60747-5-5.	1500	V _{peak}
Transient Isolation Voltage	V _{IOTM}	DIN/EN/IEC EN60747-5-5.	7000	V _{peak}
Isolation Voltage	V _{ISO}	For 1 min	5000	V _{rms}



7. Absolute Maximum Ratings $T_A = 25^\circ\text{C}$

Parameter		Symbol	Value	Units
Input	Forward Input Current	I_{FM}	25	mA
	Reverse Voltage	V_R	5	V
	Input Power Dissipation	P_D	40	mW
Output	Peak Output Current ⁽¹⁾	$I_{O(PEAK)}$	0.6	A
	Supply Voltage	V_{CC}	0 to 35	V
	Output Voltage	V_O	0 to 35	V
	Output Power Dissipation	P_O	260	mW
Isolation Voltage		V_{ISO}	5000	V_{rms}
Total Power Consumption		P_{tot}	300	mW
Operating Temperature		T_{opr}	-40 to 125	$^\circ\text{C}$
Storage Temperature		T_{stg}	-55 to 125	$^\circ\text{C}$
Soldering Temperature ⁽²⁾		T_{sol}	260	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Exponential waveform. Pulse width $\leq 10\mu\text{s}$, $f \leq 15\text{ kHz}$

Note 2: $\geq 2\text{ mm}$ below seating plane.



8. Recommended Operating Conditions

Parameter	Symbol	Min	Max	Units
Power Supply Voltage ⁽¹⁾	V_{CC}	10	30	V
Input Current (ON) ⁽²⁾	$I_{F(ON)}$	7	10	mA
Input Voltage (OFF)	$V_{F(OFF)}$	0	0.8	V
Operating Temperature	T_A	-40	125	°C

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this datasheet should also be considered.

Note: A ceramic capacitor (0.1 μ F) should be connected between pin 8 and pin 5 to stabilize the operation of a highgain linear amplifier. Otherwise, this photocoupler may not switch properly. The bypass capacitor should be placed within 1 cm of each pin.

Note 1: Denotes the operating range, not the recommended operating condition

Note 2: The rise and fall times of the input on-current should be less than 0.5 μ s.



9. Electro-optical Characteristics ($T_A=25^\circ\text{C}$)

All minimum and maximum specifications are at recommended operating conditions, unless otherwise noted

All typical values are at $T_A=25^\circ\text{C}$, $V_{DD}=30\text{V}$, and $V_{SS}=\text{GND}$.

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Input forward voltage	V_F	$I_F=10\text{mA}$, $T_A=25^\circ\text{C}$	1.4	1.55	1.7	V
Input forward voltage temperature coefficient	$\Delta V_F/\Delta T_A$	$I_F=10\text{mA}$		-2		mV/ $^\circ\text{C}$
Input reverse current	I_R	$V_R=5\text{V}$, $T_A=25^\circ\text{C}$			10	μA
Input capacitance	C_i	$V=0\text{V}$, $f=1\text{MHz}$, $T_A=25^\circ\text{C}$		60		pF
Peak high-level output current ⁽¹⁾	I_{OPH}	$I_F=5\text{mA}$, $V_{CC}=15\text{V}$, $V_{8-6}=4\text{V}$		-0.38	-0.2	A
		$I_F=5\text{mA}$, $V_{CC}=15\text{V}$, $V_{8-6}=10\text{V}$		-0.6	-0.4	A
Peak low-level output current ⁽¹⁾	I_{OPL}	$I_F=0\text{mA}$, $V_{CC}=15\text{V}$, $V_{6-5}=2\text{V}$	0.2	0.36		A
		$I_F=0\text{mA}$, $V_{CC}=15\text{V}$, $V_{6-5}=10\text{V}$	0.4	0.62		A
High-level output voltage	V_{OH}	$I_F=5\text{mA}$, $V_{CC}=10\text{V}$, $I_O=-100\text{mA}$	6	8.5		V
Low-level output voltage	V_{OL}	$V_F=0.8\text{V}$, $V_{CC}=10\text{V}$, $I_O=100\text{mA}$		0.4	1	V
High-level supply current	I_{CCH}	$I_F=10\text{mA}$, $V_{CC}=10$ to 30V , $V_O=\text{Open}$		1.4	2	mA
Low-level supply current	I_{CCL}	$I_F=10\text{mA}$, $V_{CC}=10$ to 30V , $V_O=\text{Open}$		1.3	2	mA
Threshold input current (L/H)	I_{FLH}	$V_{CC}=15\text{V}$, $V_O>1\text{V}$		1.1	5	mA
Threshold input voltage (H/L)	V_{FHL}	$V_{CC}=15\text{V}$, $V_O<1\text{V}$	0.8			V
Supply voltage	V_{CC}		10		30	V
Isolation Resistance	R_{ISO}	$V_{I-O}=500\text{V}$, 40~60% R.H.		10^{11}		Ω
Isolation Resistance	C_{ISO}	$V_{I-O}=0\text{V}$, Freq=1MHz		1		pF
Propagation Delay Time to Low Output Level ⁽¹⁾	T_{PHL}	$I_F=7\text{mA}$ to 16mA $R_g=10\Omega$, $C_g=10\text{nF}$ $F=10\text{KHz}$, Duty Cycle=50%		100	300	ns
Propagation Delay Time to High Output Level ⁽¹⁾	T_{PLH}			100	300	ns
Pulse Width Distortion	PWD			3	100	ns
Propagation Delay Difference Between Any Two Parts	PDD		-250		250	ns



Parameter	Symbol	Conditions	Min	Typ	Max	Units
Output Rise Time(10% To 90%) ⁽¹⁾	T_R	$I_F=7\text{mA to }16\text{mA}, R_g=10\Omega, C_g=10\text{NF}$ $F=10\text{KHZ, Duty Cycle}=50\%$		80		ns
Output Drop Time(90%~10%) ⁽¹⁾	T_F			80		ns
Output High Level Common Mode Transient Immunity ⁽²⁾	$ CM_H $	$T_A=25^\circ\text{C}, V_{DD}=30\text{V}$ $V_{CM}=2000\text{V}, I_F=7\sim16\text{mA}, V_F=0\text{V}$	35	50		KV/ μs
Output Low Level Common Mode Transient Immunity ⁽³⁾	$ CM_L $	$T_A=25^\circ\text{C}, V_{DD}=30\text{V}$ $V_{CM}=2000\text{V}, I_F=7\sim16\text{mA}, V_F=0\text{V}$	35	50		KV/ μs

Note: All typical values are at $T_a = 25^\circ\text{C}$.

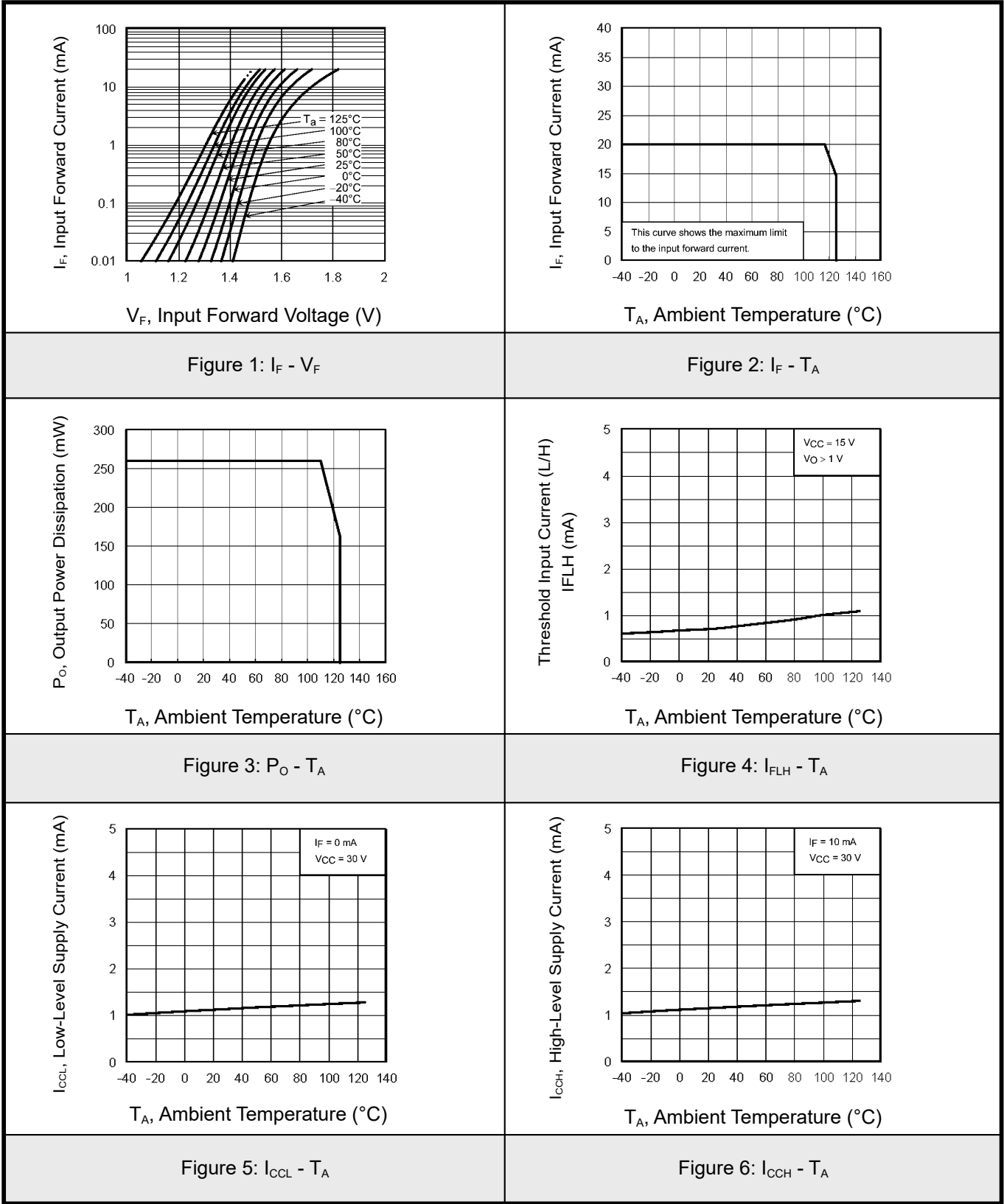
Note 1: Input signal ($f = 25\text{ kHz}$, duty = 50 %, $t_r = t_f = 5\text{ ns}$ or less). C_L is approximately 15 pF which includes probe and stray wiring capacitance.

Note 2: CM_H is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic high state ($V_O > 26\text{ V}$).

Note 3: CM_L is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic low state ($V_O < 1\text{ V}$).

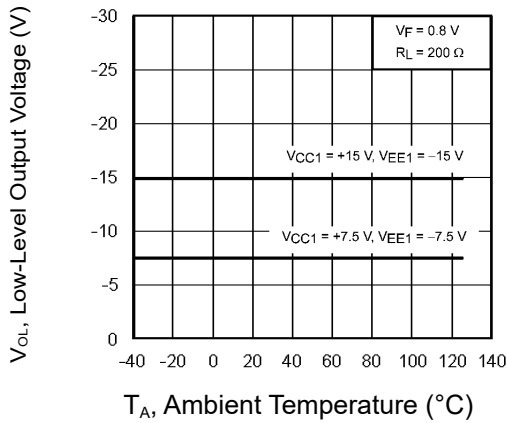
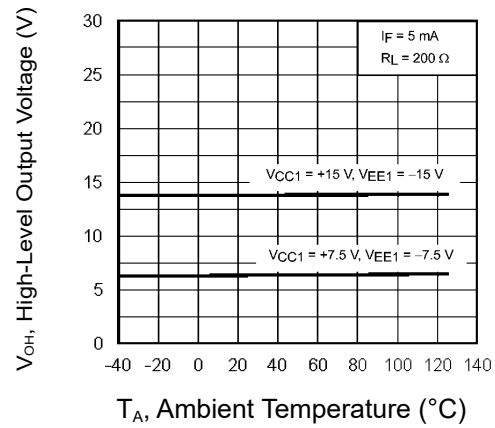
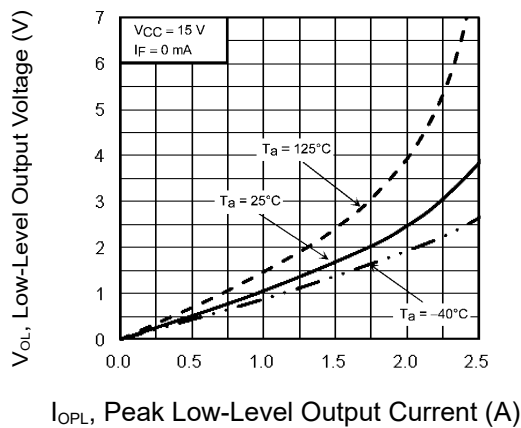
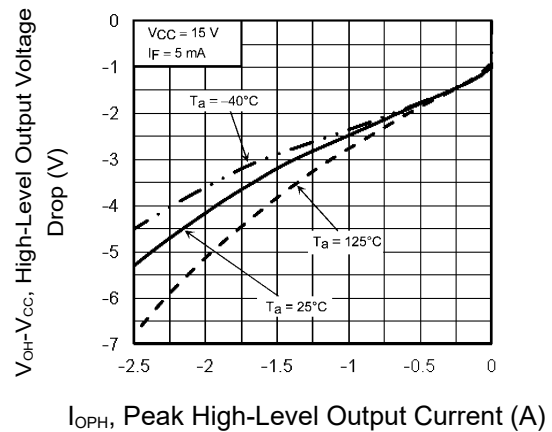
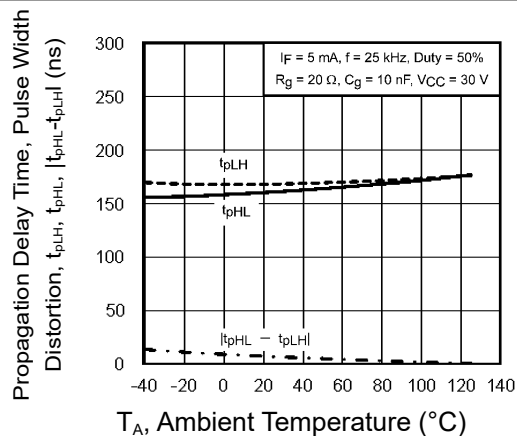
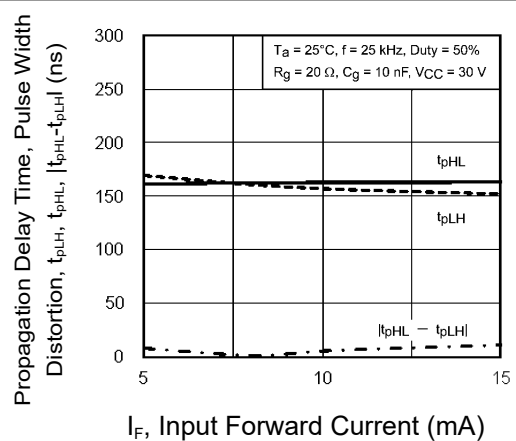


10.1 Typical Characteristic



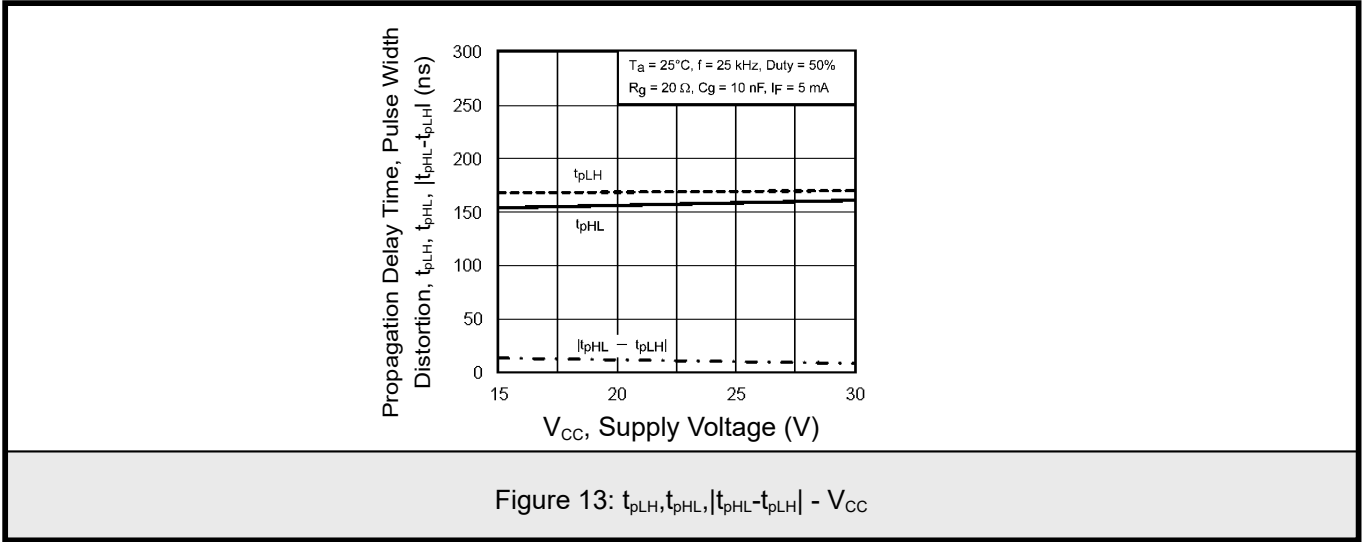


10.2 Typical Characteristic

Figure 7: $V_{OL} - T_A$ Figure 8: $V_{OH} - T_A$ Figure 9: $V_{OL} - I_{OPL}$ Figure 10: $(V_{OH} - V_{CC}) - I_{OPH}$ Figure 11: $t_{pLH}, t_{pHL}, |t_{pHL} - t_{pLH}| - T_A$ Figure 12: $t_{pLH}, t_{pHL}, |t_{pHL} - t_{pLH}| - I_F$

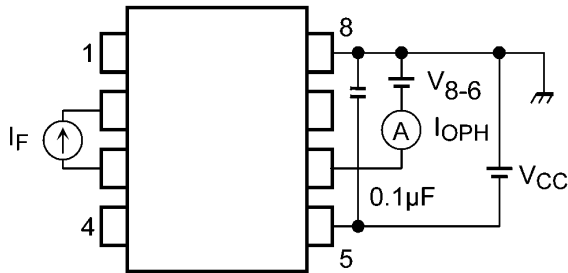
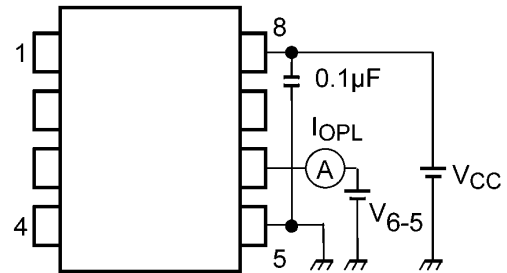
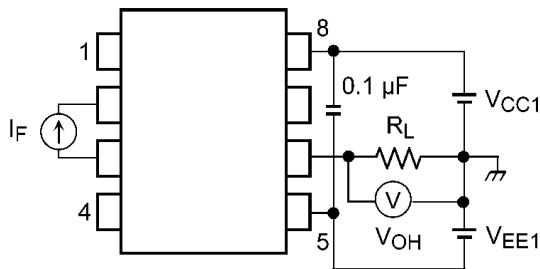
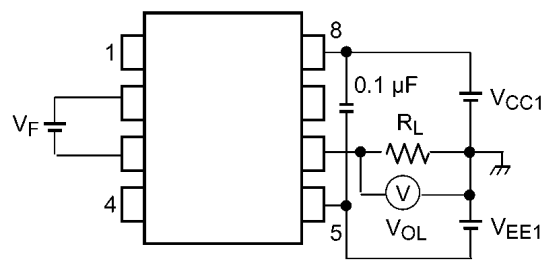
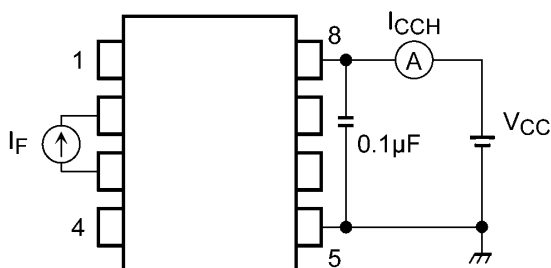
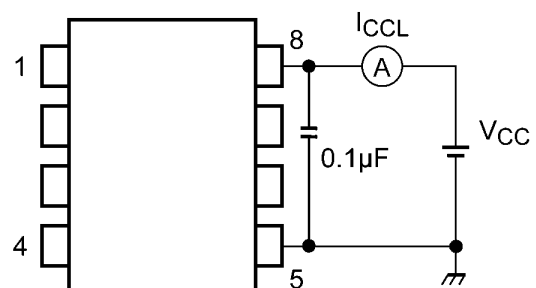


10.3 Typical Characteristic





11. Test Circuits

Figure 14: I_{OPH} Test CircuitFigure 15: I_{OPL} Test CircuitFigure 16: V_{OH} Test CircuitFigure 17: V_{OL} Test CircuitFigure 18: I_{CCH} Test CircuitFigure 19: I_{CCL} Test Circuit

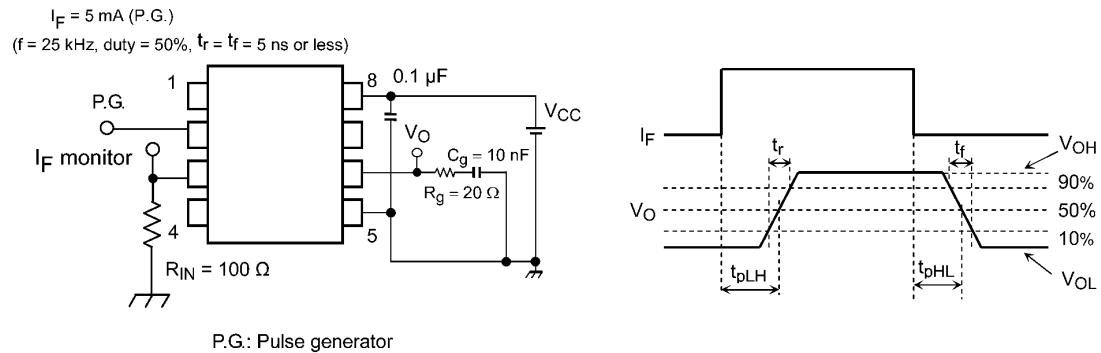
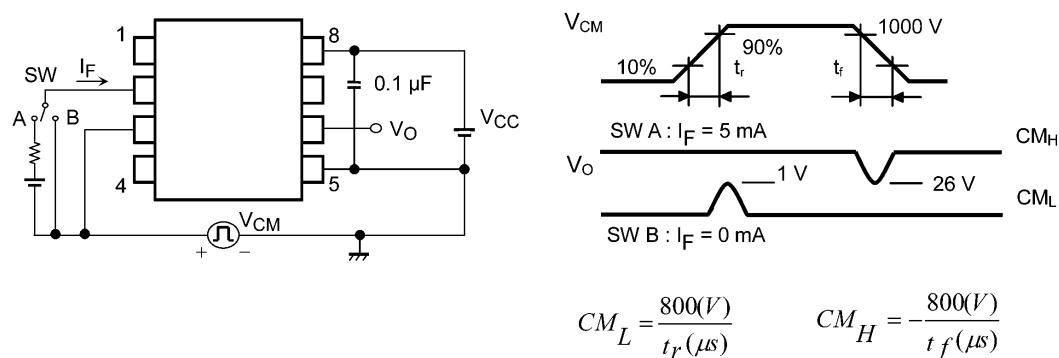


Figure 20: Switching Time Test Circuit and Waveform

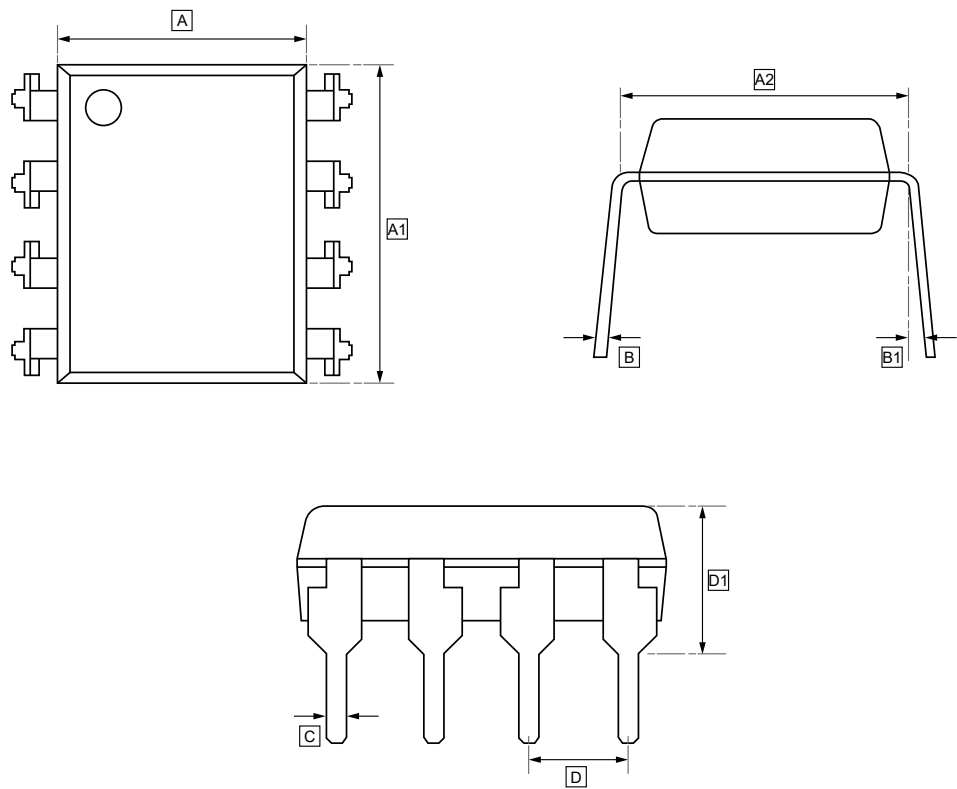


CM_L (CM_H) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.

Figure 21: Common-Mode Transient Immunity Test Circuit and Waveform



12.1 DIP-8 Package Outline Dimensions

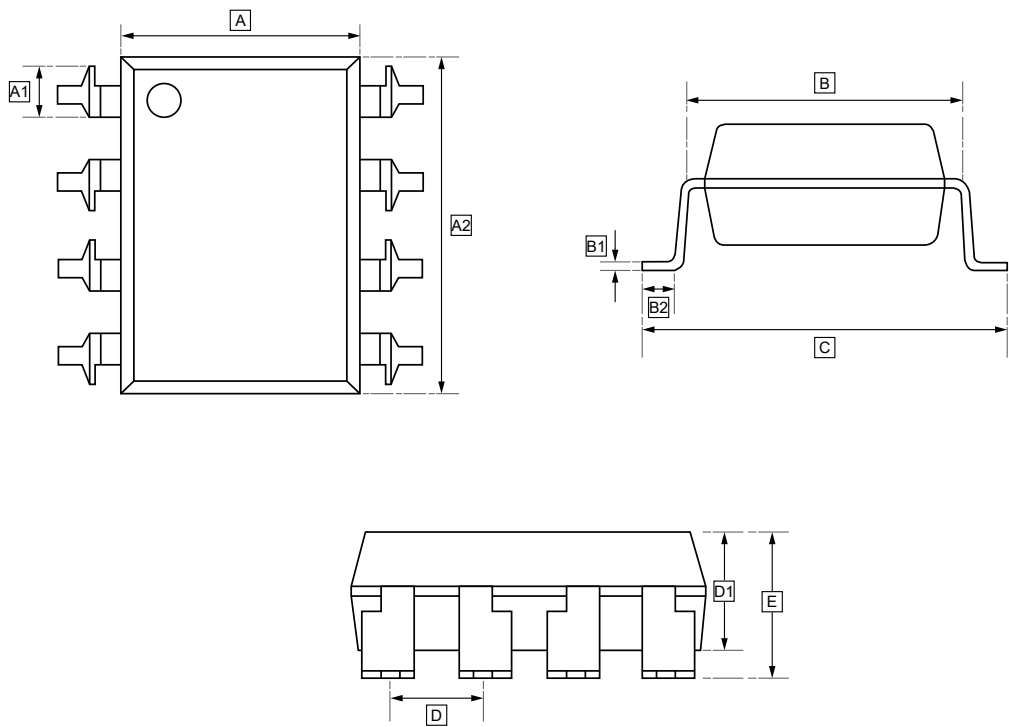


DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	B	B1	C	D	D1
Min	6.30	9.46	7.62	0.25	5°	0.40	2.54	4.20
Max	6.90	10.06	TYP.		15°	0.60	TYP.	4.80



12.2 SOP-8 Package Outline Dimensions

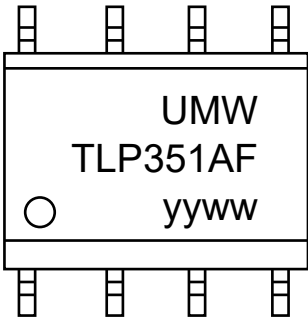


DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	B	B1	B2	C	D	D1	E
Min	6.30	1.45	9.46	7.62	0.25	0.6	-	2.54	3.20	4.00
Max	6.90		10.06	TYP		-	10.3	TYP	3.80	4.60



13.Ordering Information



yy: Year Code
ww: Week Code

Order Code	Package	Base QTY	Delivery Mode
UMW TLP351AF	DIP-8	2250	Tube and box



14.Disclaimer

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