

STGWA50IH65DF-VB Datasheet

650V Trench and Fieldstop IGBT

PRODUCT SUMMARY

V_{CE} (V)	650	
I_C (A)	120 (TC=25°C)	60 (TC=100°C)
$V_{CE(sat)}$ (V)	1.7	
I_{CM} (A)	180	

FEATURES

- Very Low V_{CEsat}
- Low turn-off losses
- High speed switching
- Maximum junction temperature 175°C
- Ultra low gate charge (Q_g)
- Avalanche energy rated (UIS)



RoHS
COMPLIANT
HALOGEN
FREE

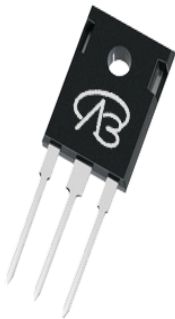
APPLICATIONS

- Telecommunications
 - Server and telecom power supplies
- Lighting
 - High-intensity discharge (HID)
 - Fluorescent ballast lighting
- Consumer and computing
 - ATX power supplies
- Industrial
 - Welding
 - Battery chargers
- Renewable energy
 - Solar (PV inverters)
- Switch mode power supplies (SMPS)

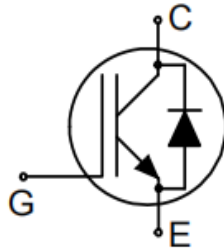
Package pin definition

- Pin1 G - Gate
- Pin2 C & backside - Collector
- Pin3 E - Emitter

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Top View



ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise noted)

PARAMETER				SYMBOL	LIMIT	UNIT
Collector-Emitter Voltage				V_{CE}	650	V
Gate-Emitter Voltage				V_{GE}	± 30	
Continuous Collector Current ($T_J = 150\text{ }^{\circ}\text{C}$)	V_{GE} at 15 V	$T_C = 25\text{ }^{\circ}\text{C}$	I_C	120	A	
		$T_C = 100\text{ }^{\circ}\text{C}$		60		
Pulsed Collector Current ^a				I_{CM}	180	
Diode Forward Current ^b				I_F	60	A
Maximum Power Dissipation		$T_C = 25\text{ }^{\circ}\text{C}$	P_D	450	W	
		$T_C = 100\text{ }^{\circ}\text{C}$		200	W	
Operating Junction and Storage Temperature Range				T_J, T_{stg}	-55 to +175	$^{\circ}\text{C}$
Short Circuit Withstand Time $T_C=150^{\circ}\text{C}$	$V_{GE}= 15\text{V}, V_{CE}\leq 400\text{V}$		tsc	3	μs	
Short Circuit Withstand Time $T_C=100^{\circ}\text{C}$	$V_{GE}= 15\text{V}, V_{CE}\leq 330\text{V}$			5		
Soldering Recommendations (Peak Temperature) ^c	for 10 s			260	$^{\circ}\text{C}$	

Notes

- Repetitive rating; pulse width limited by maximum junction temperature.
- Current limited by maximum junction temperature.
- 1.6 mm from case.

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R_{thJA}	-	40	°C/W
Maximum Junction-to-Case	R_{thJC}	-	0.5	

SPECIFICATIONS ($T_J = 25\text{ °C}$, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Collector-Emitter Breakdown Voltage	BV _{CE}	V _{GE} = 0 V, I _C = 250 μA V _{GE} = 0 V, I _C = 1 mA		650 650	- -	- -	V
Gate-Source Threshold Voltage (N)	V _{GE(th)}	V _{CE} = V _{GE} , I _D = 250 μA		4	5	6	V
Zero Gate Voltage Collector Current	I _{CES}	V _{CE} = 650 V,V _{GE} = 0 V,T _J = 25 °C		-	1	20	μA
		V _{CE} = 650 V,V _{GE} = 0 V,T _J = 150 °C		-	1000	-	μA
Gate-Emitter Leakage Current	I _{GES}	V _{CE} = 0 V, V _{GS} = ± 2 0 V		-	-	100	nA
Collector-Emitter Saturation Voltage	V _{CE(sat)}	V _{GE} = 15 V	I _C = 60 A	-	1.8	2.1	V
Forward Transconductance	g _{fs}	V _{CE} = 20 V, I _C = 60 A		-	40	-	S
Dynamic							
Input Capacitance	C _{ies}	V _{GE} = 0 V, V _{CE} = 25 V, f = 500 KHz		-	6210	-	pF
Output Capacitance	C _{oes}			-	228	-	
Reverse Transfer Capacitance	C _{res}			-	60	-	
Turn-on Energy	E _{on}	V _{CE} = 400 V , V _{GE} = 0 /15V, I _C = 60 A, R _g = 10Ω		-	0.76	-	nJ
Turn-off Energy	E _{off}			-	0.26	-	
Total Gate Charge	Q _g	V _{GE} = 15 V	I _C = 60 A, V _{CE} = 400 V	-	165	-	nC
Gate-Emitter Charge	Q _{ge}			-	18	-	
Gate to Collector Charge	Q _{gc}			-	2 3	-	
Turn-On Delay Time	t _{d(on)}	V _{CE} = 400 V , V _{GE} = 0 /15V, I _C = 60 A, R _g = 10Ω		-	72	-	ns
Rise Time	t _r			-	42	-	
Turn-Off Delay Time	t _{d(off)}			-	170	-	
Fall Time	t _f			-	26	-	
Internal emitter inductance measured 5 mm	L _E			-	13	-	nH
Diode Characteristics							
Diode Forward Current	I _F	IGBT symbol showing the integral reverse junction diode		-	-	60	A
Pulsed Diode Forward Current	I _{FM}			-	-	180	
Diode Forward Voltage	V _F	I _F = 60 A		-	1.50	2.0	V
Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = 60 A, dI _F /dt = 200 A/μs, V _R = 400 V		-	60	-	ns
Reverse Recovery Charge	Q _{rr}			-	0.3	-	μC
Reverse Recovery Current	I _R RM			-	11	-	A

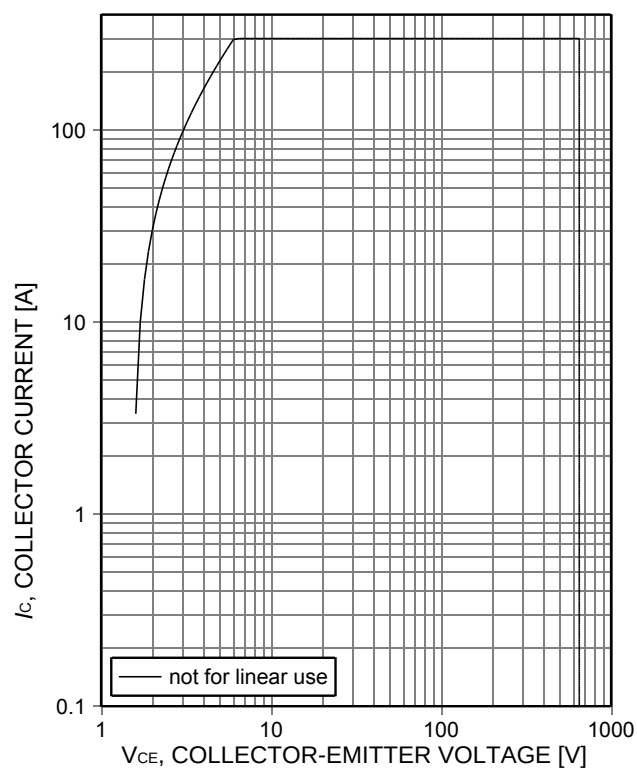


Figure 1. Forward bias safe operating area

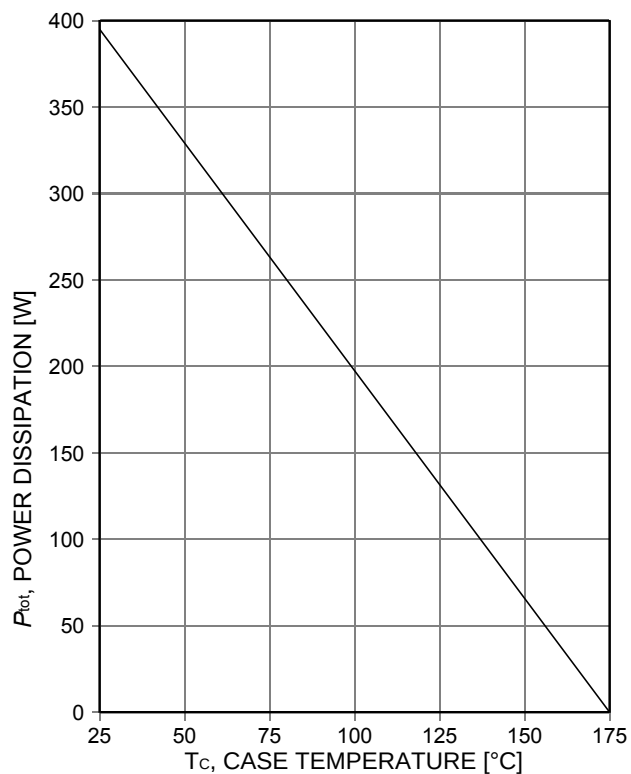


Figure 2. Power dissipation as a function of case

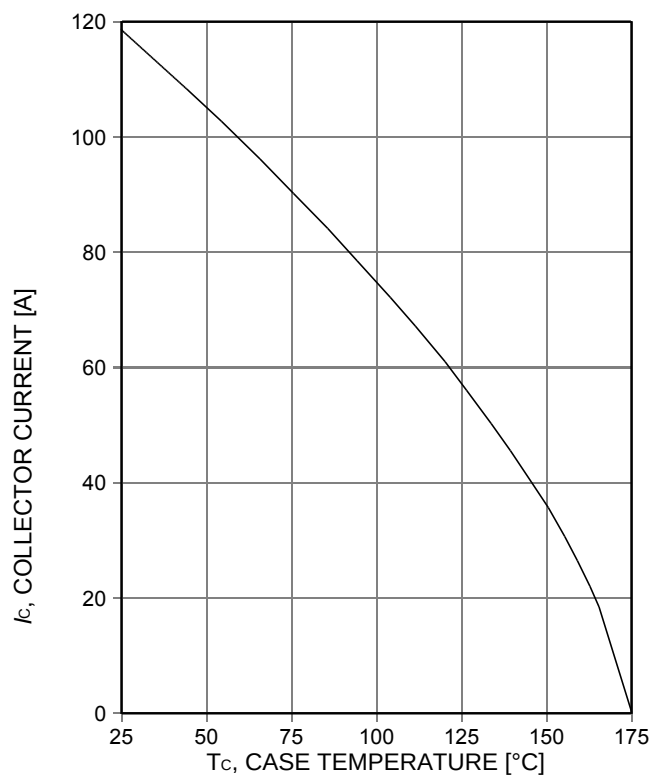


Figure 3. Collector current as a function of case temperature

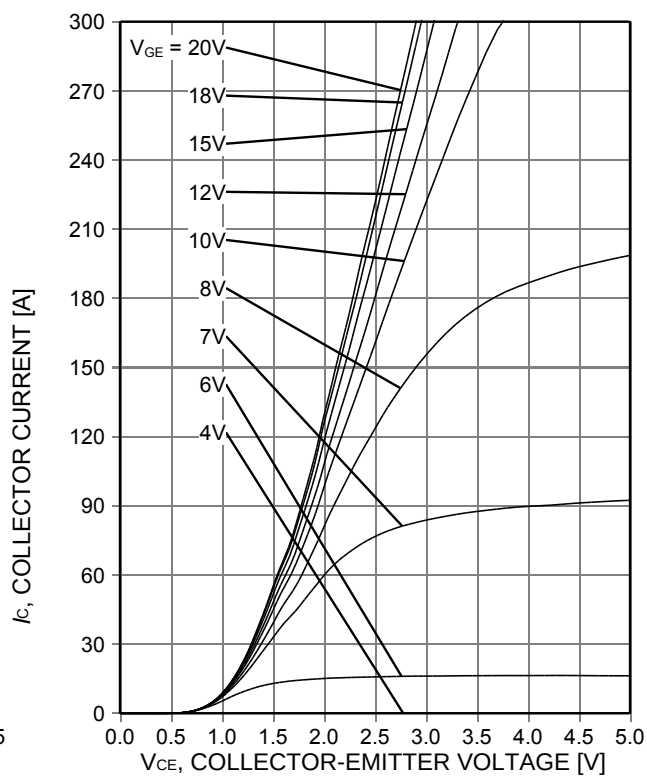


Figure 4. Typical output characteristic

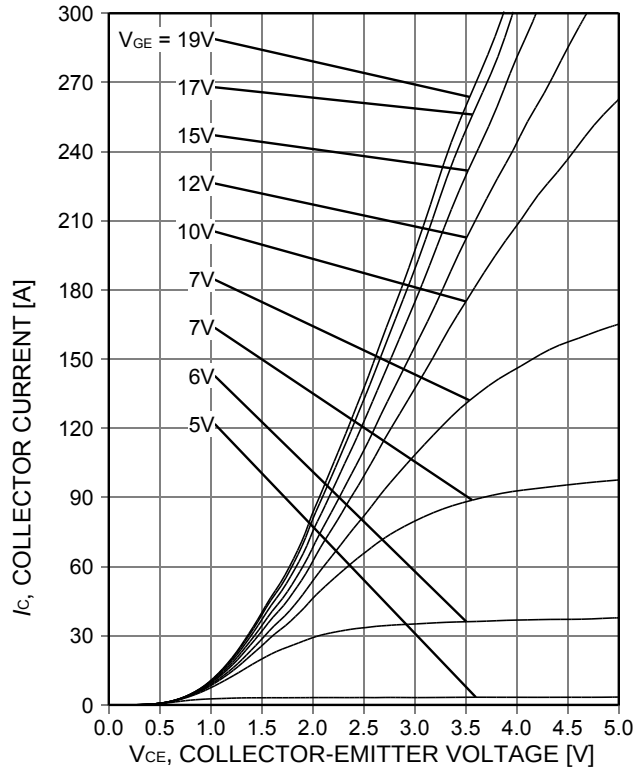


Figure 5. Typical output characteristic

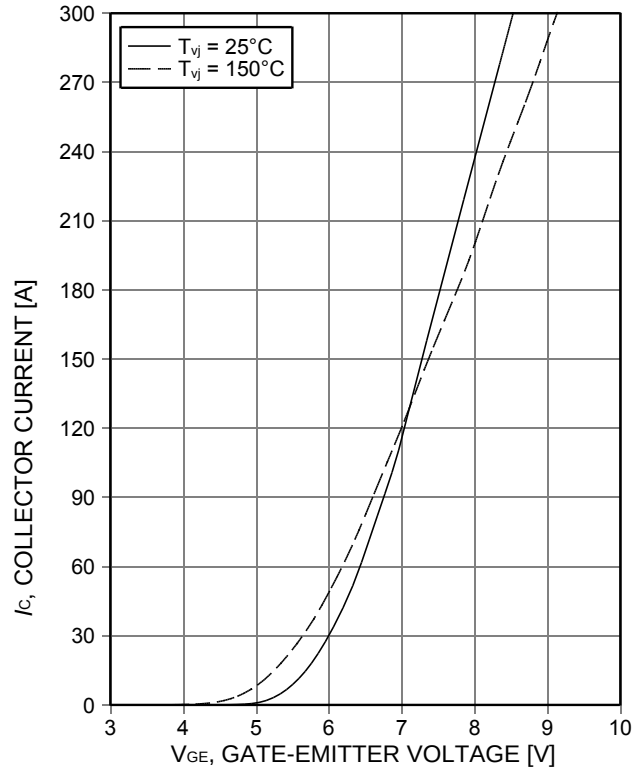


Figure 6. Typical transfer characteristic

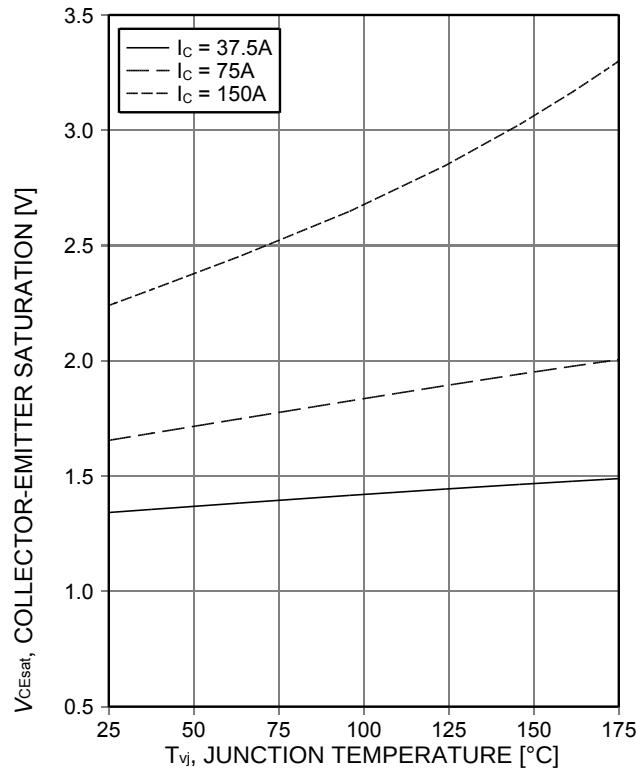


Figure 7. Typical collector-emitter saturation voltage as a function of junction temperature

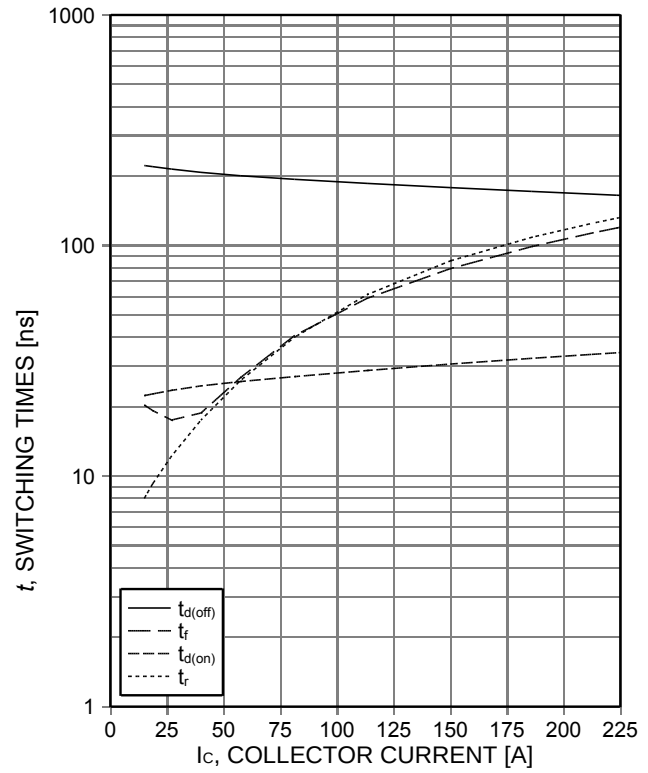


Figure 8. Typical switching times as a function of collector current

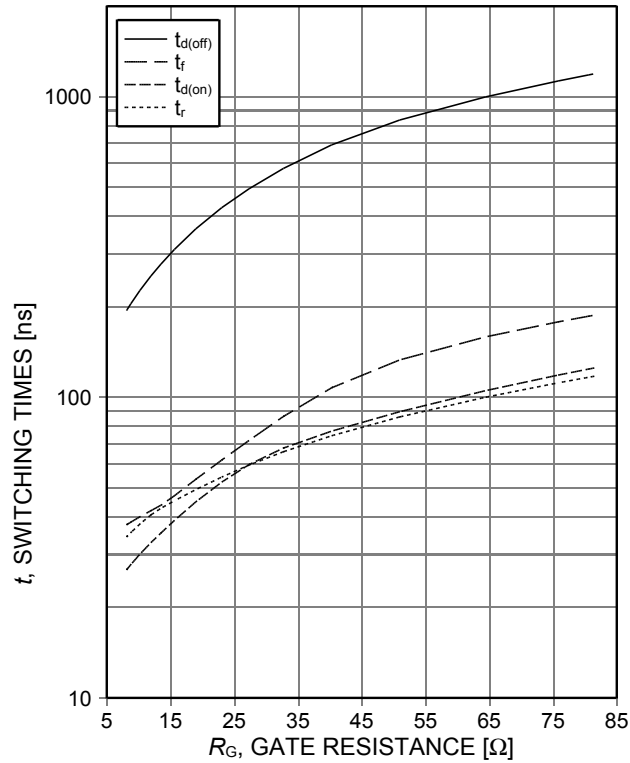


Figure 9. Typical switching times as a function of gate resistance

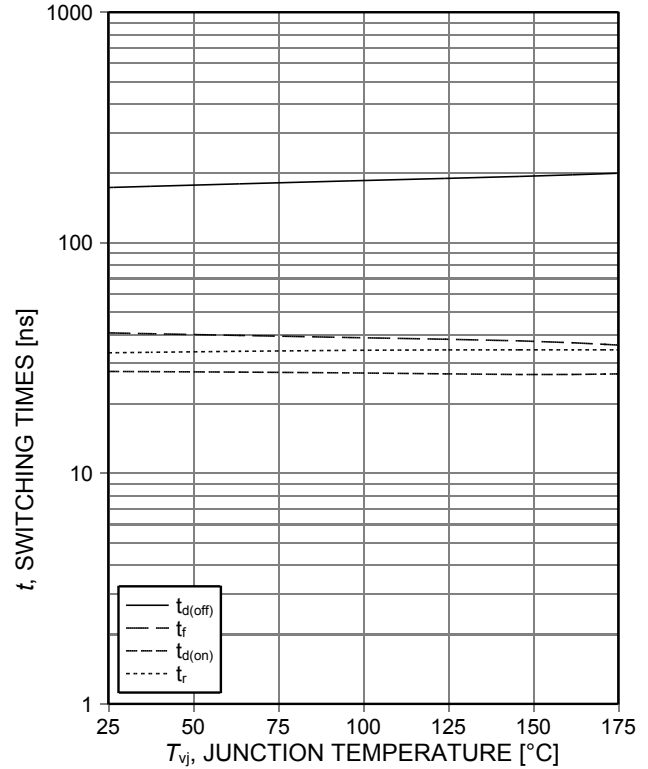


Figure 10. Typical switching times as a function of junction temperature

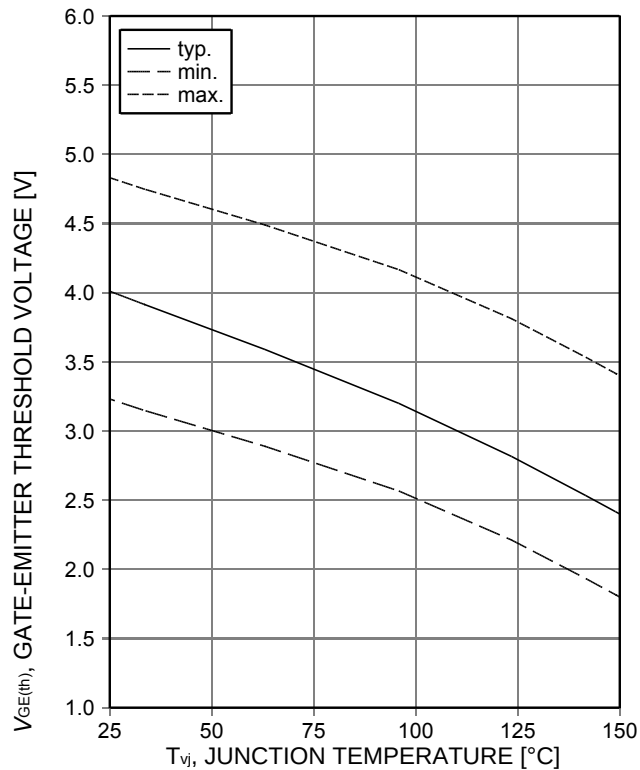


Figure 11. Gate-emitter threshold voltage as a function of junction temperature

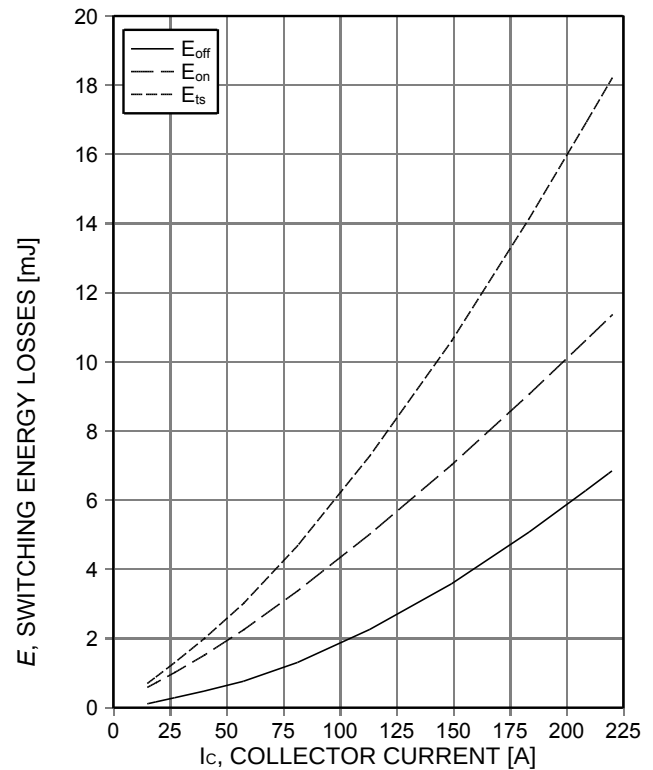


Figure 12. Typical switching energy losses as a function of collector current

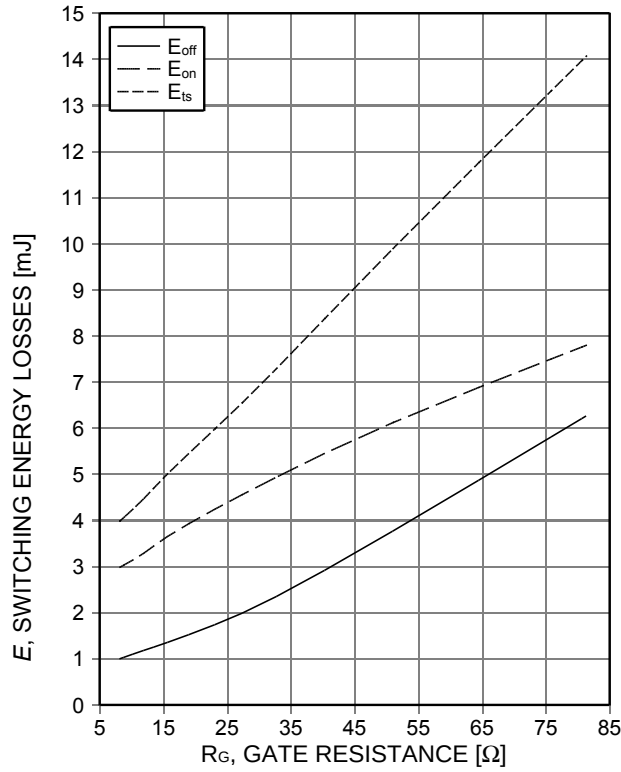


Figure 13. Typical switching energy losses as a function of gate resistance

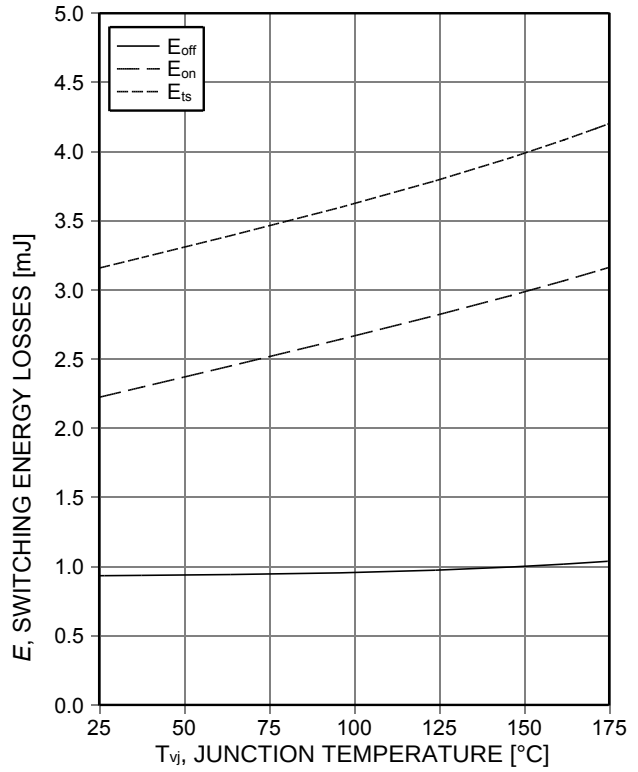


Figure 14. Typical switching energy losses as a function of junction temperature

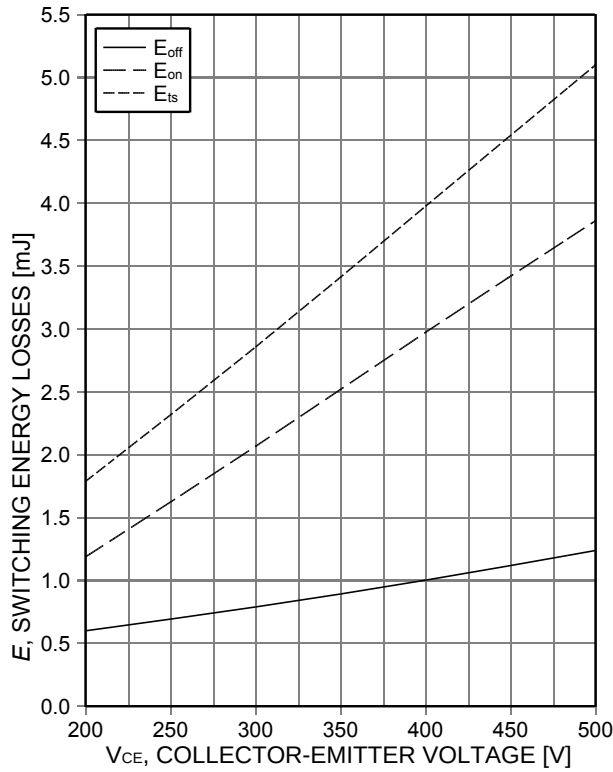


Figure 15. Typical switching energy losses as a function of collector emitter voltage

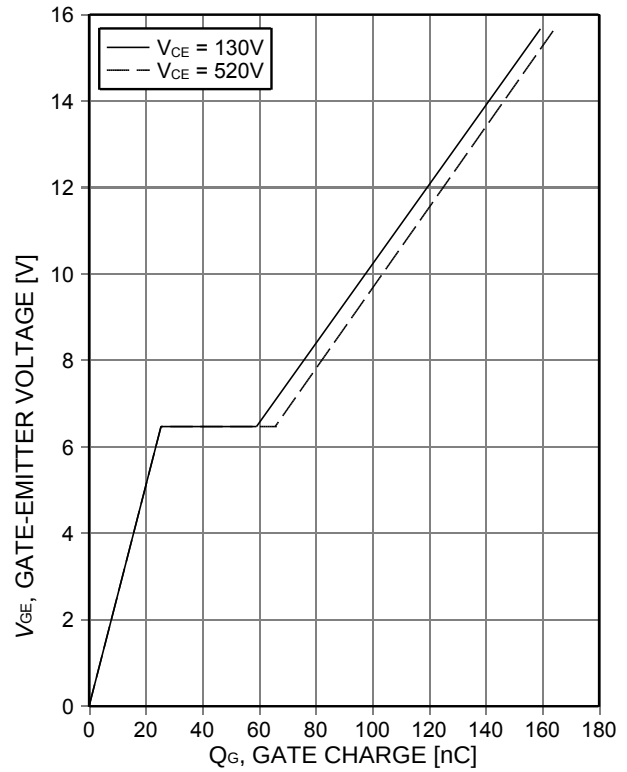
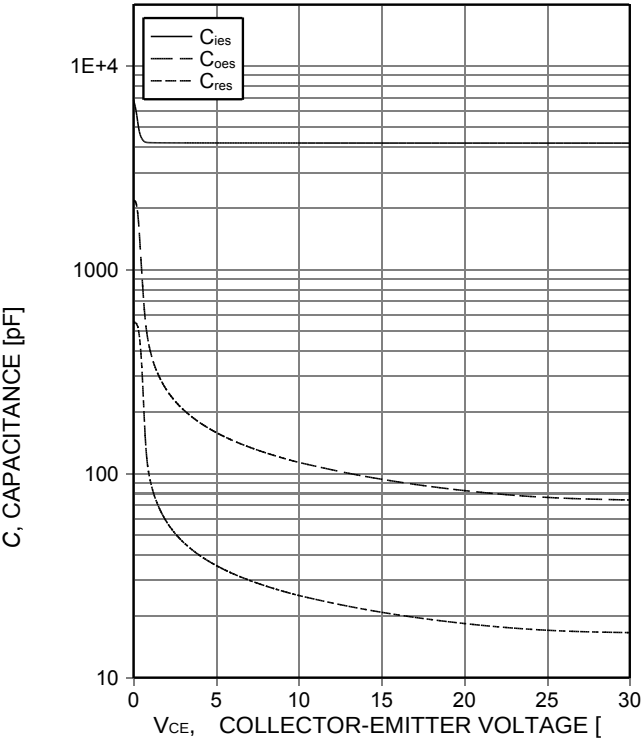


Figure 16. Typical gate charge



V]Figure 17. Typical capacitance as a function of collector-emitter voltage

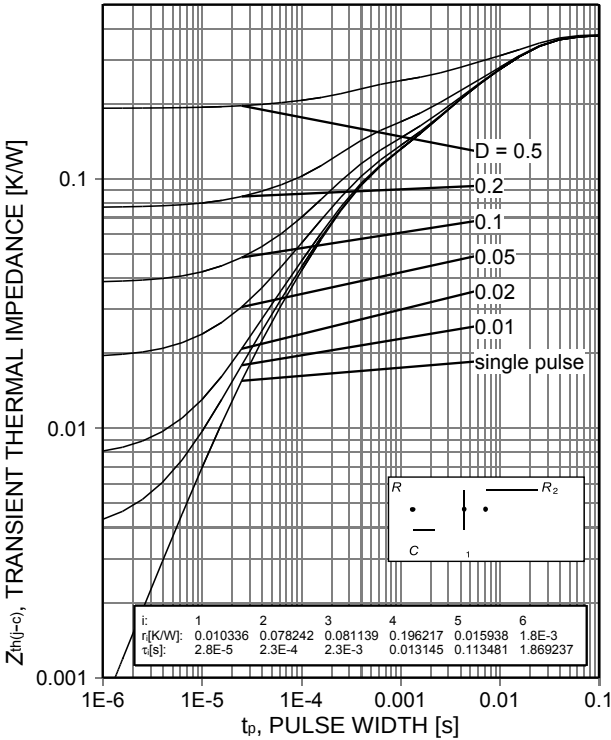
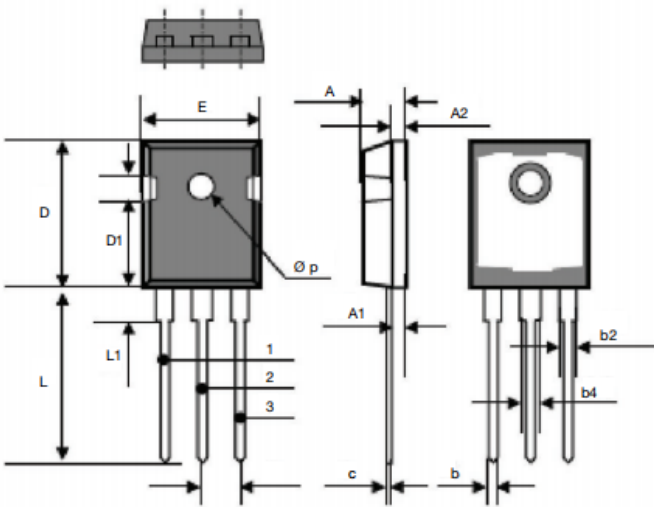


Figure 18.IGBT transient thermal impedance

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DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.70	5.31	0.185	0.209
A1	2.21	2.59	0.087	0.102
A2	1.50	2.49	0.059	0.098
b	0.99	1.40	0.039	0.055
b2	1.65	2.41	0.065	0.095
b4	2.59	3.43	0.102	0.135
c	0.61 BSC		0.024 BSC	
D	20.80	21.46	0.819	0.845
D1	3.68	5.49	0.145	0.216
(e)	5.46 BSC		0.215 BSC	
E	15.49	16.26	0.610	0.640
L	19.81	20.32	0.780	0.800
L1	4.06	4.50	0.160	0.177
$\varnothing p$	3.51	3.66	0.138	0.144

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