UMW LMV321AUIDBVR

Low-Power rail-to-rail Operational Amplifier

1.Description

UMW LMV321AUIDBVR (single channel) is a rail-to-rail input, output voltage feedback, low power consumption operational amplifier. It has wide input common mode voltage and output swing. The minimum working voltage can be up to 2.1V, and the maximum working voltage is recommended to be 5.5V. Used as power amplifier in all kinds of pocket or portable stereo radio recorders.

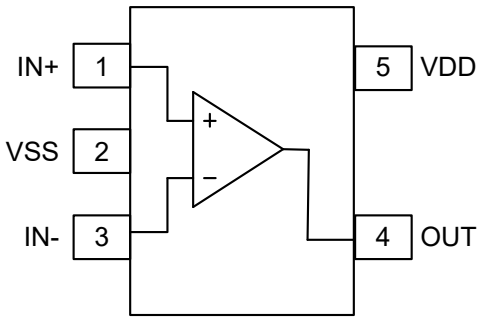
2.Features

- Rail to rail input and output, typical 0.5mv V_{OS}
- Gain bandwidth product 1MHz
- Low input bias current: 10nA (TYP)
- Low Power consumption
- 2.1V ~ 5.5V working voltage
- Low operating current:
  UMW LMV321AUIDBVR: 45uA

3.Application

- ASIC input and output amplifier
- Sensor interface
- Piezoelectric sensing amplifier
- Battery-powered equipment
- The mobile communication
- Audio output

4.Pinning information



SOT23-5

5.Absolute Maximum Ratings (T_A=25°C) Note1

Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	7.5	V
Common-mode input Voltage	V_{ICR}	(-VS)-0.5~(+VS)0.5	V
Junction Temperature	T_J	150	°C
Operating Temperature Range	T_{OPR}	-40 to 125	°C
Lead Temperature (Soldering, 10 sec)	T_L	250	°C
Storage Temperature Range	T_{STG}	-50 to 150	°C

Note1: Exceeding the above limits may damage to the chip. The reliability of the device will also be affected if the device works under the limit conditions. Electrostatic discharge can also cause damage to chips, so it is suggested to take some preventive measures for integrated circuits. Failure to follow proper handling and installation can also cause damage. Precision LMV321 and other devices are more vulnerable to damage than ordinary devices in the case of tiny electrostatic, and small parameter changes may make the whole circuit performance substandard.

6.Electrical Characteristics

At R_L=100kΩ connected to Vs/2,and V_{OUT}=Vs/2,Ta=25°C) , unless otherwise noted

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Input Offset Voltage	V_{OS}			0.5	1	mV
Input offset current	I_{OS}			10		nA
Input bias current	I_B			10		nA
Common-mode input voltage range	V_{CM}	V_S=5.5V	(V-)-0.1		(V+)-1	V
Open-loop Gain	AOL	V_O=0.1V to 4.9V, R_L=5kΩ	70	80		dB
		V_O=0.035V to 4.96V, R_L=100kΩ	80	84		dB
Common Mode Rejection	CMRR	V_{CM}=-0.1V~4V, V_S=5.5V	62	70		dB
		V_{CM}=-0.1V~5.6V, V_S=5.5V	56	68		dB
Power Supply Rejection	PSRR	V_{CM}=(-V_S)+0.5 V, V_S=2.5V~5.5V	60	80		dB
Input offset voltage drift	$\Delta V_{OS}/\Delta T$			2.7		μV/°C
Input voltage swing	V_I	R_L=100kΩ		8		mV
		R_L=10kΩ		80		mV
Operating voltage range	V_W		2.1		5.5	V
Output Current	I_O		20	25		mA
Quiescent Current	I_Q			45	100	μA
Slew Rate	SR	G=+1,2V Output Step		0.52		V/μs
Gain Bandwidth Product	GBP	C_L=100pF		1		MHz
Equivalent input Noise Voltage	eN	f=1KHz		27		nV/√Hz
		f=10KHz		20		

7.1Typical Characteristic

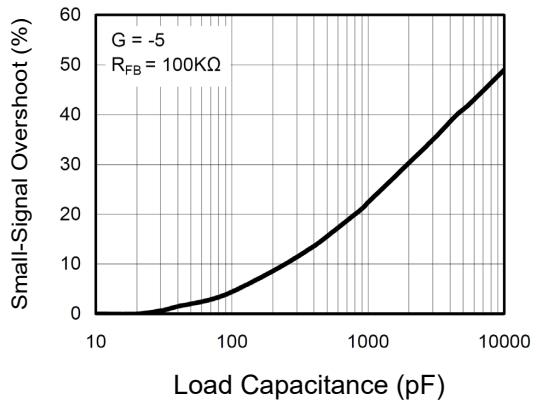


Figure 1: Small-Signal Overshoot vs. Load Capacitance

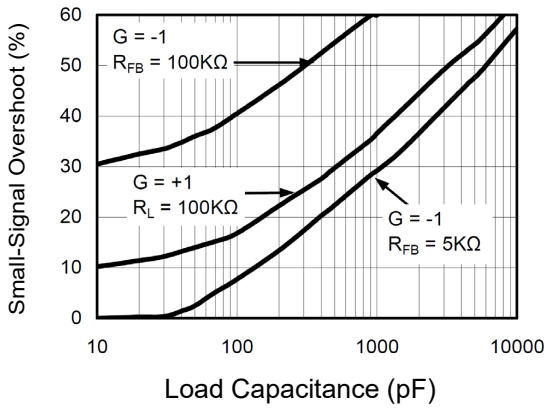


Figure 2: Small-Signal Overshoot vs. Load Capacitance

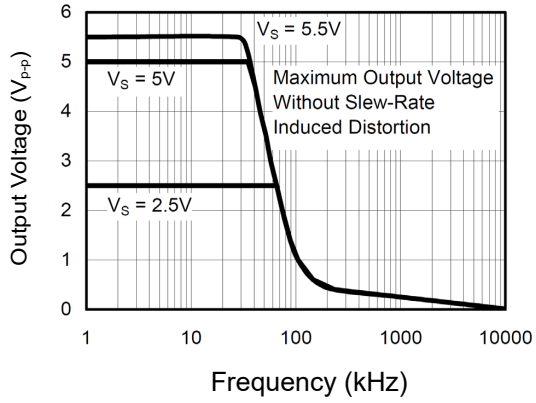


Figure 3: Maximum Output Voltage vs. Frequency

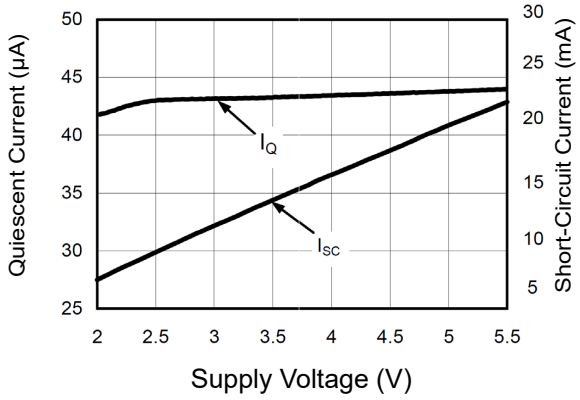


Figure 4: Quiescent and Short-Circuit Current vs. Supply Voltage

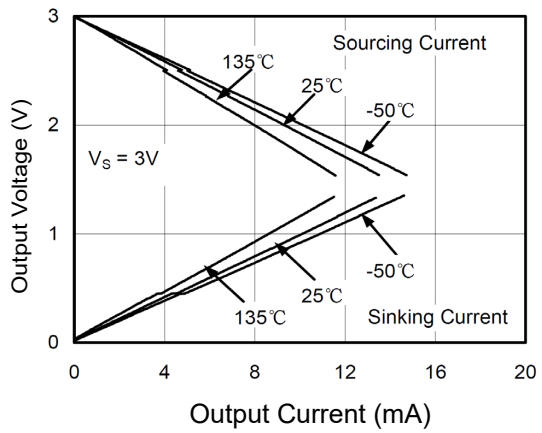


Figure 5: Output Voltage Swing vs. Output Current

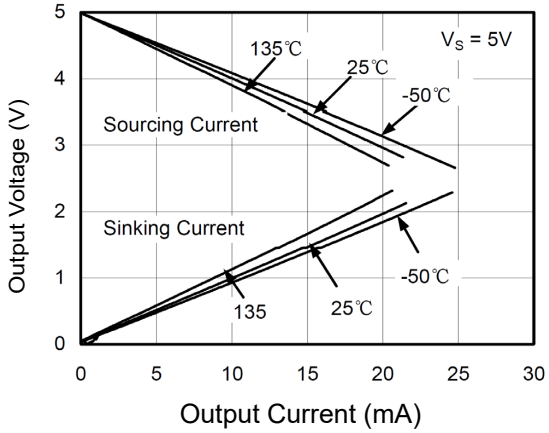


Figure 6: Output Voltage Swing vs. Output Current

7.2Typical Characteristic

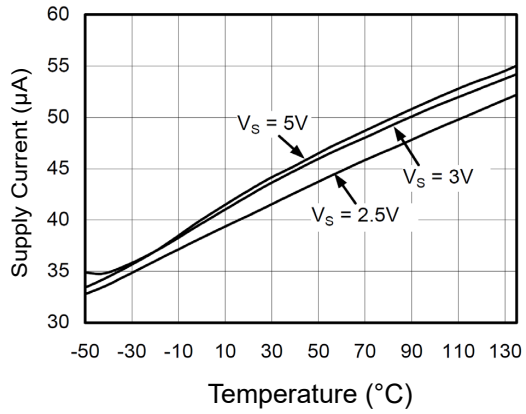


Figure 7: Supply Current vs. Temperature

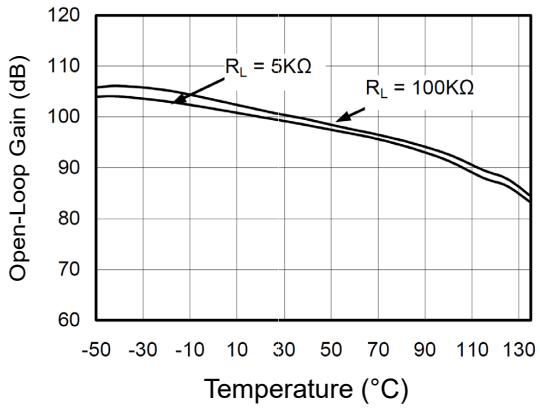


Figure 8: Open-Loop Gain vs. Temperature

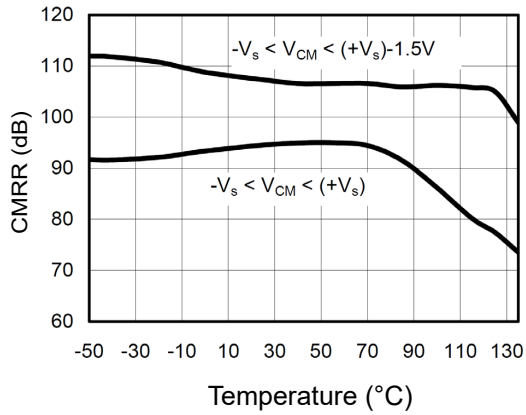


Figure 9: Common-Mode Rejection Ratio vs. Temperature

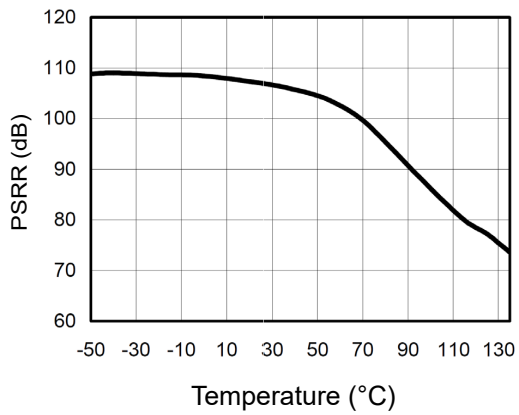
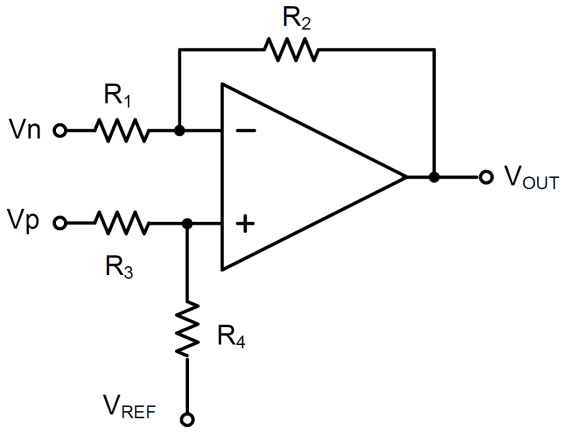


Figure 10: Power-Supply Rejection Ratio vs. Temperature

8.Typical Applications

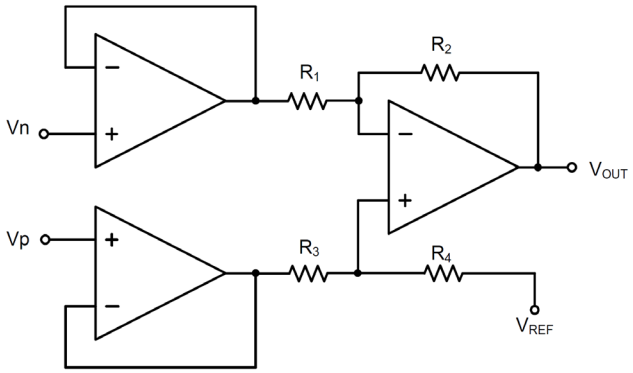
1.differential amplifier

As shown in the figure, if the resistance is equal, (R4 / R3 = R2 / R1), then the output $V_{OUT}=(V_p-V_n)\times R2/R1+V_{REF}$



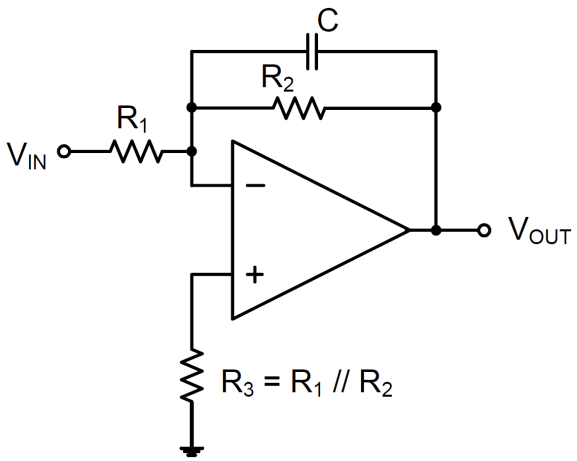
2.instrumentation amplifier

The circuit in the figure above performs the same function, but the input is high impedance.

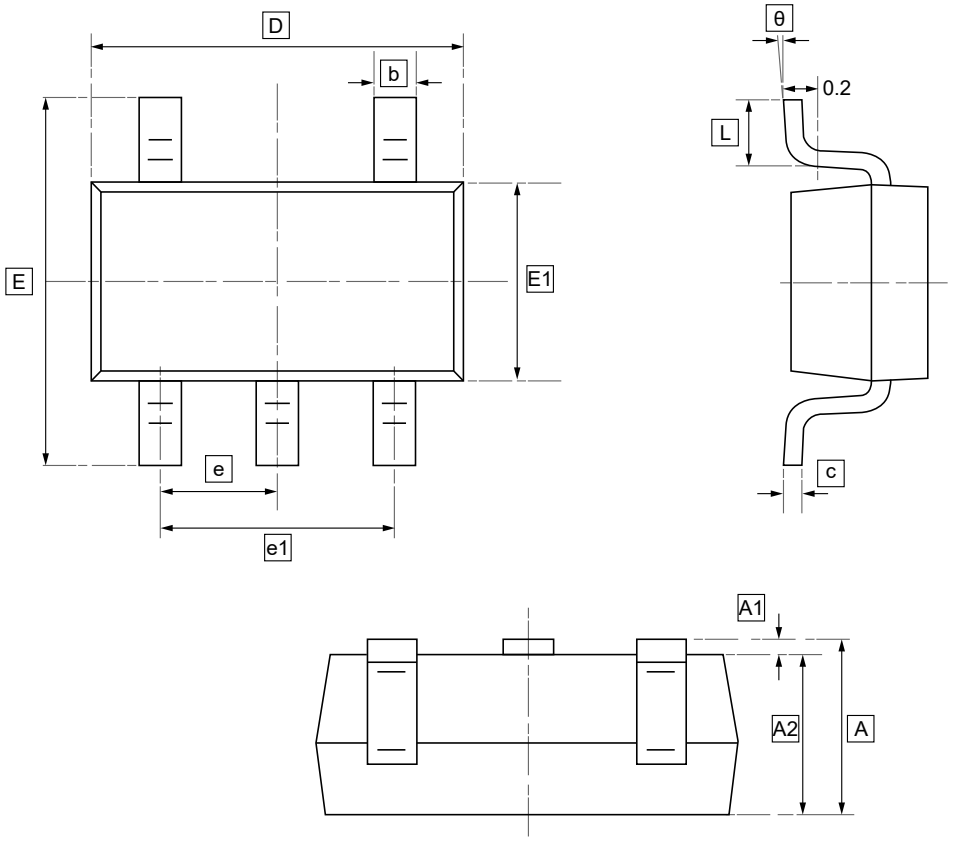


3.Low pass active filtering

The low-pass filter circuit shown here has a (-R2 / R1) DC gain and -3db at a frequency of 1/2 PI R2C corner. Make sure the filter is within the amplifier's bandwidth. Large feedback resistors are easily accompanied by parasitic capacitance at high speed, resulting in adverse effects such as oscillation. Keep the resistance value as low as possible and consider the appropriate output load.



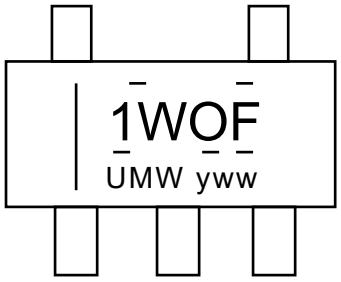
9.2 SOT23-5 Package Outline Dimensions



DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	b	c	D	E1	E	e	e1	L	θ
Min	1.050	0.000	1.050	0.300	0.100	2.820	1.500	2.650	0.950	1.800	0.300	0°
Max	1.250	0.100	1.150	0.500	0.200	3.020	1.700	2.950	BSC	2.000	0.600	8°

10.Ordering Information



yww: Batch Code

Order Code	Package	Base QTY	Delivery Mode
UMW LMV321AUIDBVR	SOT23-5	3000	Tape and reel

11.Disclaimer

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