

2A 150KHz 40V PWM Buck DC/DC Converter

Features

- Wide 4.5V to 40V Input Voltage Range
- 3.3V, 5V, 12V, and adjustable versions
- Output Adjustable from 1.23V to 37V
- Maximum Duty Cycle 100%
- Minimum Drop Out 1.0V
- Fixed 150KHz Switching Frequency
- 2A Constant Output Current Capability
- Internal Optimize Power Transistor
- High efficiency
- TTL shutdown capability
- Excellent line and load regulation
- Built in thermal shutdown function
- Built in current limit function
- Built in output short protection function
- Available in SOP8L packages

Applications

- Portable DVD
- LCD Monitor / TV
- Battery Charger
- ADSL Modem
- Telecom / Networking Equipment

Package Types

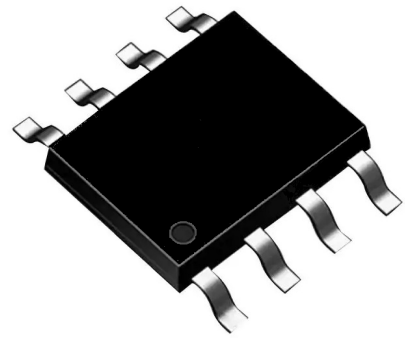


Figure 1. Package Types of CXL1509

Pin Configurations

VIN	1	8	GND
SW	2	7	GND
CXL1509			
FB	3	6	GND
EN	4	5	GND

Figure 2 Pin Configuration of CXL1509 (Top View)

Pin Description

Pin Number	Pin Name	Description
1	VIN	Supply Voltage Input Pin. CXL1509 operates from a 4.5V to 40V DC voltage. Bypass Vin to GND with a suitably large capacitor to eliminate noise on the input.
2	SW	Power Switch Output Pin (SW). Output is the switch node that supplies power to the output.
3	FB	Feedback Pin (FB). Through an external resistor divider network, Feedback senses the output voltage and regulates it. The feedback threshold voltage is 1.23V.
4	EN	Enable Pin. Drive EN pin low to turn on the device, drive it high to turn it off. Floating is default low.
5/6/7/8	GND	Ground Pin.

Function Block

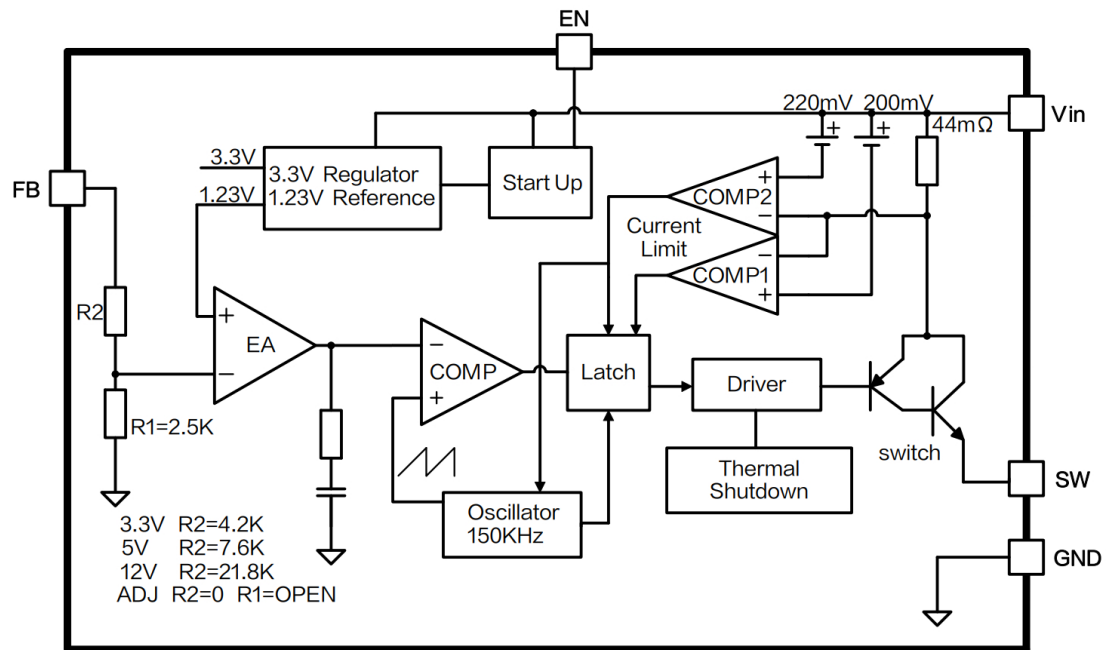


Figure 3 Function Block Diagram of CXL1509

Typical Application Circuit

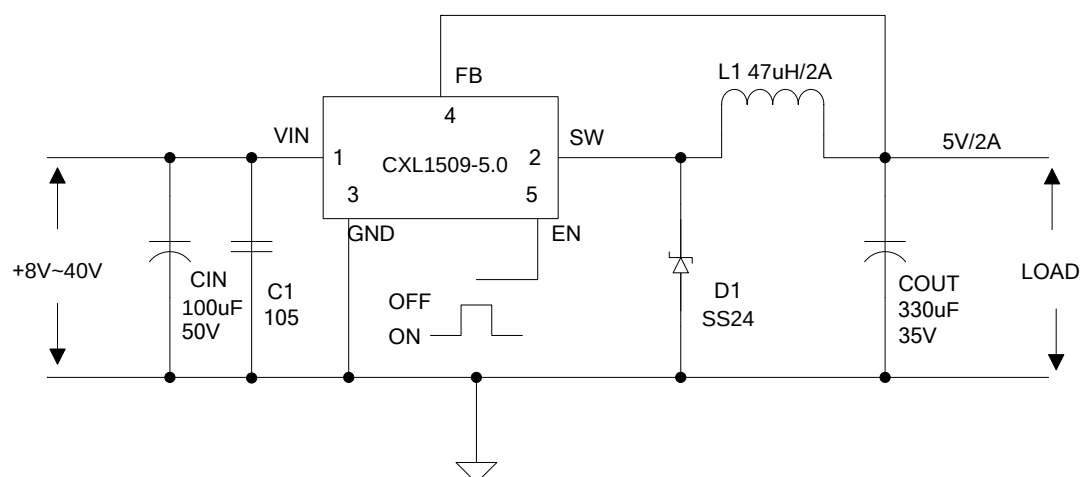


Figure4. CXL1509-5.0 Typical Application Circuit (VIN=8V~40V, VOUT=5V/2A)

Ordering Information

Order Information	Marking ID	Package Type	Eco Plan	Packing Type Supplied As
CXL1509-ADJ	LM1509-ADJ	SOP8L	RoHS & HF	2500 Units on Reel
CXL1509-3.3	LM1509-3	SOP8L	RoHS & HF	2500 Units on Reel
CXL1509-5.0	LM1509-5	SOP8L	RoHS & HF	2500 Units on Reel
CXL1509-12	LM1509-12	SOP8L	RoHS & HF	2500 Units on Reel

Absolute Maximum Ratings

Note1: Stresses greater than those listed under Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Parameter	Symbol	Value	Unit
Input Voltage	V_{IN}	-0.3 to 40	V
Feedback Pin Voltage	V_{FB}	-0.3 to V_{IN}	V
Enable Pin Voltage	V_{EN}	-0.3 to V_{IN}	V
Switch Pin Voltage	V_{SW}	-0.3 to V_{IN}	V
Power Dissipation	P_D	Internally limited	mW
Operating Junction Temperature	T_J	-40~125	°C
Storage Temperature	T_{STG}	-65 to 150	°C
Lead Temperature (Soldering, 10 sec)	T_{LEAD}	260	°C
ESD (HBM)		2000	V
MSL		Level3	
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	85	°C / W
Thermal Resistance-Junction to Case	$R_{\theta JC}$	45	°C / W

CXL1509-3.3 Electrical Characteristics

$T_a = 25^{\circ}\text{C}$; unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
System parameters test circuit figure6						
V _{OUT}	Output Voltage	V _{in} = 4.75V to 40V I _{load} =0.2A to 2A	3.168	3.3	3.432	V
η	Efficiency	V _{in} =12V, V _{out} =3.3V I _{out} =2A	–	75	–	%

CXL1509-5.0 Electrical Characteristics

$T_a = 25^{\circ}\text{C}$; unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
System parameters test circuit figure7						
V _{OUT}	Output Voltage	V _{in} = 7V to 40V, I _{load} =0.2A to 2A	4.8	5	5.2	V
η	Efficiency	V _{in} =12V, V _{out} =5V, I _{out} =2A	–	82	–	%

CXL1509-12 Electrical Characteristics

$T_a = 25^{\circ}\text{C}$; unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
System parameters test circuit figure8						
V _{OUT}	Output Voltage	V _{in} = 15V to 40V, I _{load} =0.2A to 2A	11.52	12	12.48	V
η	Efficiency	V _{in} =25V, V _{out} =12V, I _{out} =2A	–	90	–	%

CXL1509-ADJ Electrical Characteristics

$T_a = 25^{\circ}\text{C}$; unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
System parameters test circuit figure9						
V _{OUT}	Output Voltage	V _{in} = 4.5V to 40V, I _{load} =0.2A to 2A	1.193	1.23	1.267	V
η	Efficiency	V _{in} =12V, V _{out} =3V, I _{out} =2A	–	74	–	%

Electrical Characteristics (DC Parameters)

V_{in} = 12V for the 3.3V, 5V, and Adjustable versions and V_{in} =24V for the 12V version, $GND=0V$, V_{in} & GND parallel connect a 220uF/50V capacitor; I_{out} =500mA, T_a = 25°C; the others floating unless otherwise specified.

Parameters	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input operation voltage	V_{in}		4.5		40	V
Shutdown Supply Current	I_s	$V_{EN}=5V$		50	200	uA
Quiescent Supply Current	I_q	$V_{EN}=0V$, $V_{FB}=V_{in}$		4.5	10	mA
Oscillator Frequency	F_{osc}		127	150	173	Khz
Switch Current Limit	I_L	$V_{FB}=0V$		4.0		A
EN Pin Threshold	V_{EN}	High (Regulator OFF) Low (Regulator ON)		1.4 0.8		V
EN Pin Input Leakage Current	I_H	$V_{EN}=2.5V$ (OFF)		8	15	uA
	I_L	$V_{EN}=0.5V$ (ON)		0.5	5	uA
Output Saturation Voltage	V_{CE}	$V_{FB}=0V$ $I_{out}=2A$		1.2	1.5	V
Max. Duty Cycle	D_{MAX}	$V_{FB}=0V$		100		%

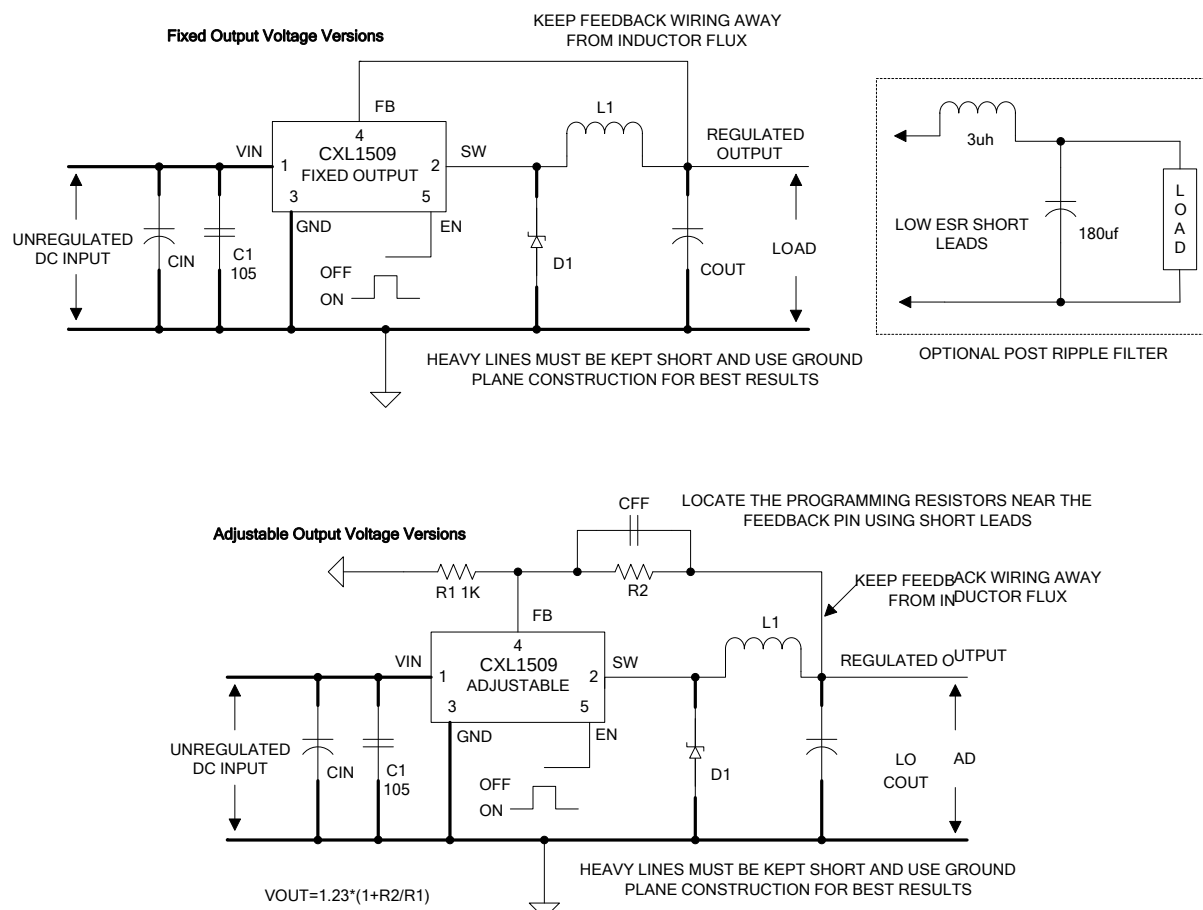


Figure4. Standard Test Circuits and Layout Guides

Select R1 to be approximately 1K, use a 1% resistor for best stability.

C1 and CFF are optional; in order to increase stability and reduce the input power line noise, C1 must be placed near to PIN1 and PIN3;

For output voltages greater than approximately 10V, an additional capacitor CFF is required. The compensation capacitor is typically between 100 pf and 33 nf, and is wired in parallel with the output voltage setting resistor, R2. It provides additional stability for high output voltage, low input-output voltages, and/or very low ESR output capacitors, such as solid tantalum capacitors.

$CFF = 1/(31 \cdot 1000 \cdot R_2)$; This capacitor type can be ceramic, plastic, silver mica, etc. (Because of the unstable characteristics of ceramic capacitors made with Z5U material, they are not recommended.)

CXL1509 Series Buck Regulator Design Procedure(Fixed Output)

Conditions			Inductor (L1)	Output Capacitor (COUT)			
				Through Hole Electrolytic		Surface Mount Tantalum	
Output Voltage (V)	Load Current (A)	Max Input Voltage (V)	Inductance (uH)	Panasonic HFQ Series (uF/V)	Nichicon PL Series (uF/V)	AVX TPS Series (uF/V)	Sprague 595D Series (uF/V)
3.3	2	6	22	470/25	470/35	330/6.3	390/6.3
		10	33	330/35	330/35	330/6.3	390/6.3
		40	47	330/35	270/50	220/10	330/10
5	2	9	22	470/25	560/16	220/10	330/10
		20	68	180/35	180/35	100/10	270/10
		40	68	180/35	180/35	100/10	270/10
12	2	15	33	330/25	330/25	100/16	180/16
		20	68	180/25	180/25	100/16	120/20
		40	150	82/25	82/25	68/20	68/25

CXL1509 Series Buck Regulator Design Procedure(Adjustable Output)

Output Voltage (V)	Through Hole Output Electrolytic			Surface Mount Output Capacitor		
	Panasonic HFQ Series (uF/V)	Nichicon PL Series (uF/V)	Feedforward Capacitor	AVX TPS Series (uF/V)	Sprague 595D Series (uF/V)	Feedforward Capacitor
2	820/35	820/35	33nF	330/6.3	470/4	33nF
4	560/35	470/35	10nF	330/6.3	390/6.3	10nF
6	470/25	470/35	3.3nF	220/10	330/10	3.3nF
9	330/25	330/25	1.5nF	100/16	180/16	1.5nF
12	330/25	330/25	1nF	100/16	180/16	1nF
15	220/25	220/35	680pF	68/20	120/20	680pF
24	220/35	150/35	560pF	33/25	33/25	220pF
28	100/50	100/50	390pF	10/35	15/50	220pF

Schottky Diode Selection Table

Current	Surface Mount	Through Hole	VR (The same as system maximum input voltage)				
			20V	30V	40V	50V	60V
1A		✓	1N5817	1N5818	1N5819		
3A		✓	1N5820	1N5821	1N5822		
		✓	MBR320	MBR330	MBR340	MBR350	MBR360
	✓		SK32	SK33	SK34	SK35	SK36
	✓			30WQ03	30WQ04	30WQ05	
		✓		31DQ03	31DQ04	31DQ05	
		✓	SR302	SR303	SR304	SR305	SR306

Typical System Application for 3.3V Version

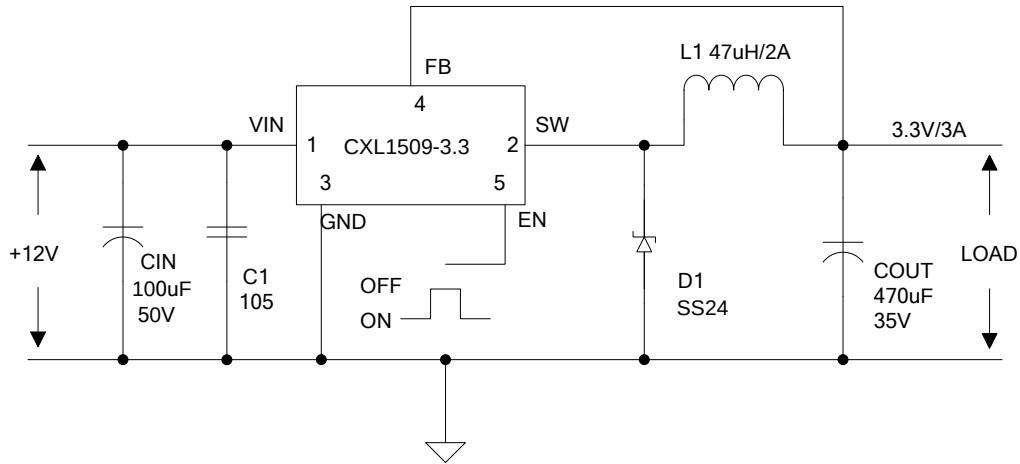


Figure5. CXL1509-3.3 System Parameters Test Circuit

Typical System Application for 5V Version

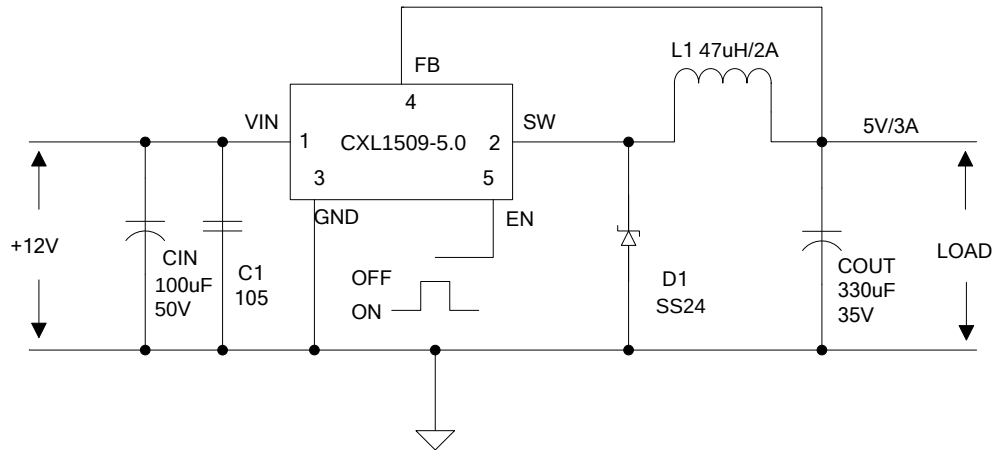


Figure6. CXL1509-5.0 System Parameters Test Circuit

Typical System Application for 12V Version

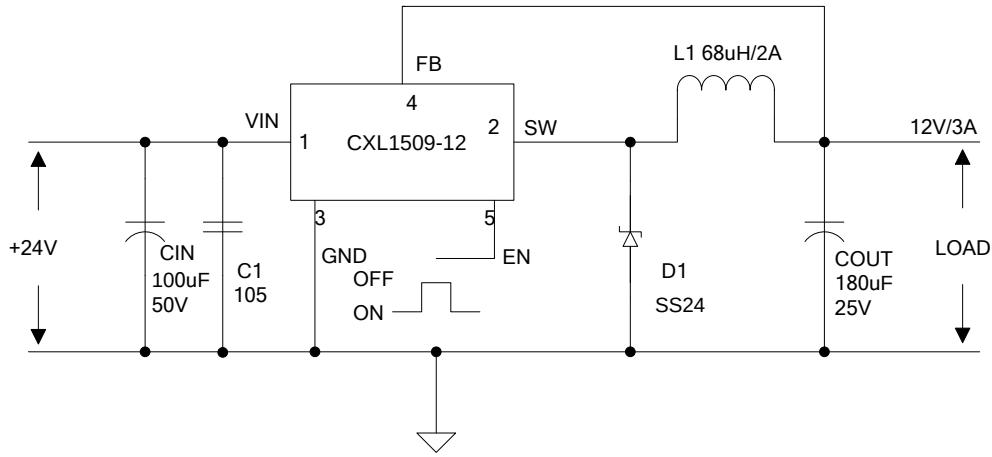


Figure7. CXL1509-12 System Parameters Test Circuit

Typical System Application for ADJ Version

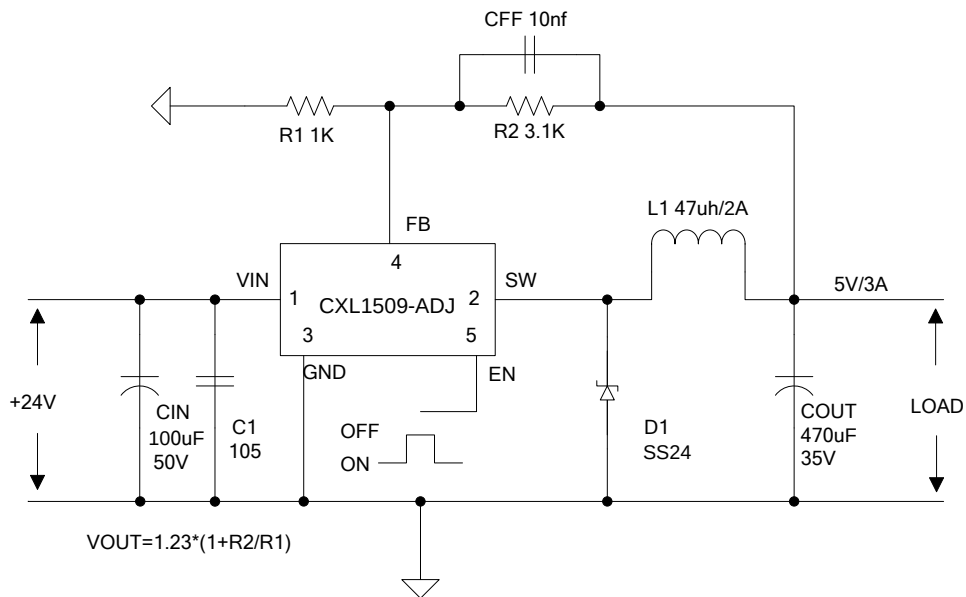
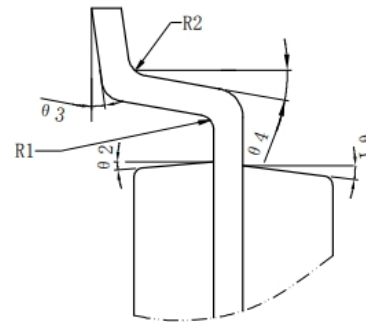
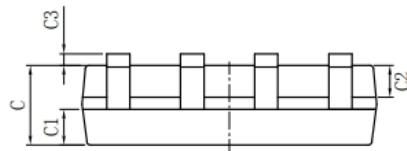
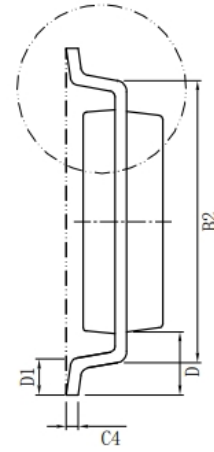
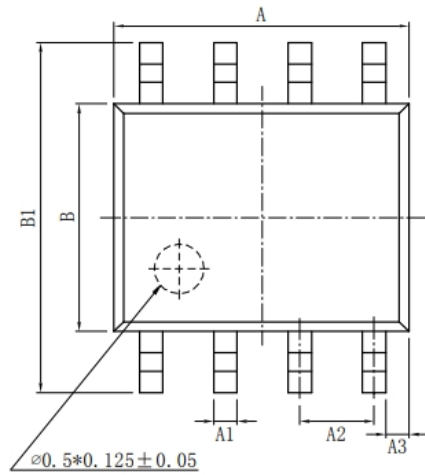


Figure8. CXL1509-ADJ System Parameters Test Circuit

SOP8L Package Outline Dimensions



标注	尺寸	MIN(mm)	MAX(mm)	标注	尺寸	MIN(mm)	MAX(mm)
A		4.80	5.00	C3		0.05	0.20
A1		0.356	0.456	C4		0.203	0.233
A2		1.27TYP		D		1.05TYP	
A3		0.345TYP		D1		0.40	0.80
B		3.80	4.00	R1		0.20TYP	
B1		5.80	6.20	R2		0.20TYP	
B2		5.00TYP		$\theta 1$		17° TYP4	
C		1.30	1.60	$\theta 2$		13° TYP4	
C1		0.55	0.65	$\theta 3$		0° ~ 8°	
C2		0.55	0.65	$\theta 4$		4° ~ 12°	