



Features

- Wide bandgap SiC MOSFET technology
- Low On-Resistance with High Blocking Voltage
- Low Capacitances with High-Speed switching
- Low reverse recovery(Qrr)
- Halogen free, RoHs compliant

Benefits

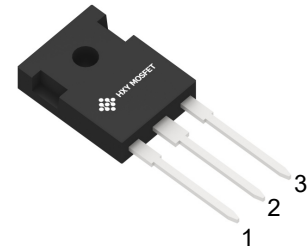
- Reduce switching losses
- Increased system Switching Frequency
- Increased power density
- Reduction of heat sink requirements

Applications

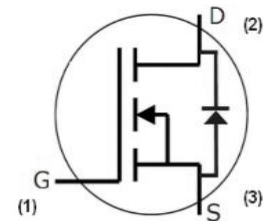
- Switch mode power supplies
- Renewable energy
- On Board Charger
- High Voltage DC/DC Converters



Ordering Part Number	Package	Brand
STW40N65M2	TO-247	HXY MOSFET



TO-247



Maximum Ratings (T_C = 25 °C unless otherwise specified)

Symbol	Parameter	Test conditions	Value	Unit	Note
V _{DSmax}	Drain-Source Voltage	V _{GS} = 0V, I _D = 100μA	650	V	
V _{GS}	Gate-Source voltage (transient)	t _p ≤ 500ns, duty cycle ≤ 1%	-8/+20	V	
V _{GSop}	Recommend Gate-Source Voltage	Static	-4/+18	V	
EAS	Single pulse avalanche energy	V _{DS} =650V, V _{DD} =50V, V _{GS} =10V, L=10mH, T _C =25°C	198	mJ	
I _D	Continuous Drain current	V _{GS} = 18V, T _C = 25°C	38	A	Fig. 14
		V _{GS} = 18V, T _C = 100°C	27		
I _{D,pulse}	Pulsed Drain Current	Pulse with t _p limited by T _{jmax}	48	A	
P _D	Power Dissipation	T _C = 25°C, T _j = 175°C	150	W	Fig.16
T _j	Operating junction temperature		-55~175	°C	
T _{stg}	Storage temperature		-55~175	°C	



Thermal Characteristics

Symbol	Parameter	Value			Unit	Note
		Min.	Typ.	Max.		
$R_{th(jc)}$	Thermal resistance from Junction to Case		1.0		K/W	Fig. 15
$R_{th(ja)}$	Thermal resistance from Junction to Ambient		40		K/W	

Electrical Characteristics (T_c = 25°C unless other wise specified)

Static Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
$V_{(BR)DSS}$	Drain-Source Breakdown voltage	$V_{GS} = 0V, I_D = 100\mu A$	650			V	
$V_{GS(th)}$	Gate Threshold voltage	$V_{GS} = V_{DS}, I_D = 4mA$		3.0		V	Fig. 9
		$V_{GS} = V_{DS}, I_D = 4mA, T_j = 175^\circ C$		2.0			
I_{GSS}	Gate-Source Leakage current	$V_{GS} = 18V, V_{DS} = 0V$			250	nA	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650V, V_{GS} = 0V, T_j = 25^\circ C$		1	50	μA	
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS} = 18V, I_D = 10A$		95	125	m Ω	Fig. 3, 4, 5
		$V_{GS} = 18V, I_D = 10A, T_j = 175^\circ C$		135			
g_{fs}	Transconductance	$V_{DS} = 18V, I_D = 10A$		7		S	Fig. 6
		$V_{DS} = 18V, I_D = 10A, T_j = 175^\circ C$		6			



Gate Charge Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
Q_{GS}	Gate to Source Charge	$V_{DS} = 400V$ $I_D = 10A$ $V_{GS} = -4V/18V$		7.8		nC	Fig. 10
Q_{GD}	Gate to Drain Charge			9			
Q_G	Total Gate Charge			34			

AC Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
C_{iss}	Input Capacitance	$V_{GS} = 0V, V_{DS} = 600V$ $f = 1\text{ MHz}$ $V_{AC} = 25mV$		803		pF	Fig. 13
C_{oss}	Output Capacitance			77		pF	
C_{rss}	Reverse Transfer Capacitance			5		pF	
$R_{G(int)}$	Internal Gate Resistance	$f = 1\text{ MHz}, V_{AC} = 25mV$		3.8		Ω	



Reverse Diode Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
V_{SD}	Diode Forward Voltage	$V_{GS} = -5V, I_{SD} = 5A$		1.4	4.0	V	Fig. 7,8
		$V_{GS} = -5V, I_{SD} = 5A, T_J = 175^{\circ}C$		1.9	4.5		
I_S	Continuous Diode Forward Current	$V_{GS} = -5V, T_C = 25^{\circ}C$		37		A	
$I_{S, pulse}$	Diode pulse Current	$V_{GS} = -5V$, pulse width t_p limited by T_{jmax}		48		A	
t_{rr}	Reverse Recovery Time	$V_{GS} = -4V, I_{SD} = 10A, V_R = 400V$ $diff/dt = 1000A/us$		29		nS	
Q_{rr}	Reverse Recovery Charge			63		nC	
I_{rrm}	Peak Reverse Recovery Current			5		A	

Switching Characteristics

Symbol	Parameter	Test conditions	Value			Unit	Note
			Min.	Typ.	Max.		
$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 400V, V_{GS} = -4/+18V$ $I_D = 10A, R_{G(ext)} = 5\Omega$ $L = 294\mu H$		5		nS	Fig.21
t_r	Rise Time			18		nS	
$t_{d(off)}$	Turn-Off Delay Time			17		nS	
t_f	Fall Time			10		nS	
E_{on}	Turn-On Energy			43		μJ	Fig.19
E_{off}	Turn-Off Energy			17		μJ	
E_{tot}	Total switching energy			60		μJ	



Typical Performance

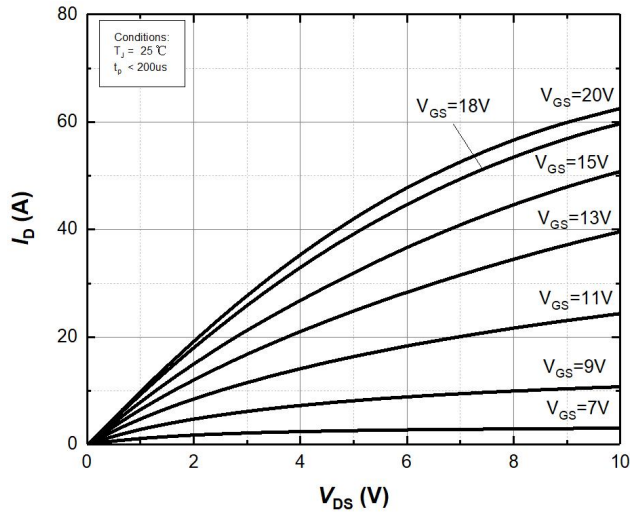


Figure 1. Output characteristics at $T_j=25^\circ\text{C}$

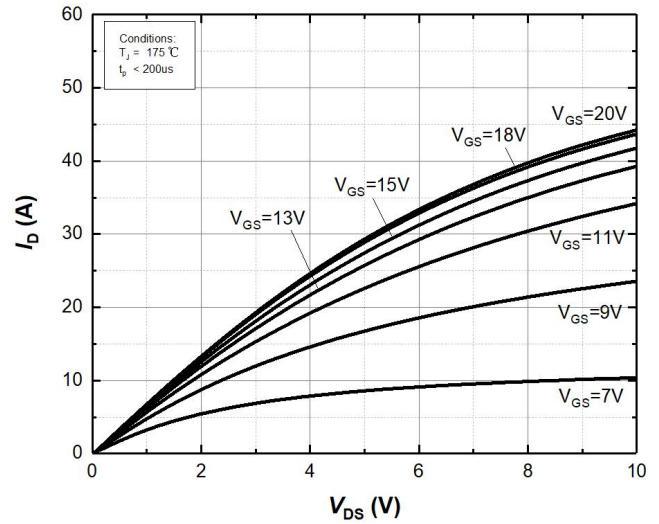


Figure 2. Output characteristics at $T_j=175^\circ\text{C}$

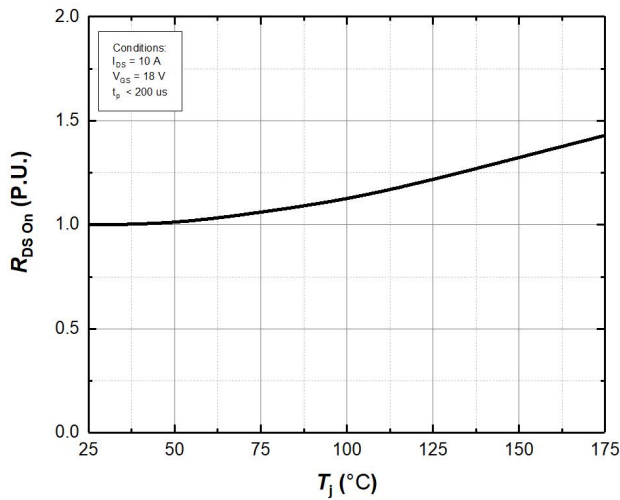


Figure 3. Normalized On-Resistance vs. Temperature

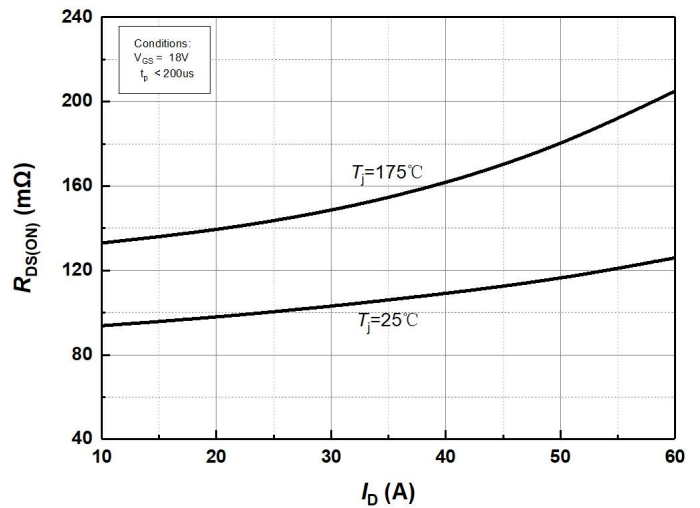


Figure 4. On-Resistance vs. Drain current for Various Temperature

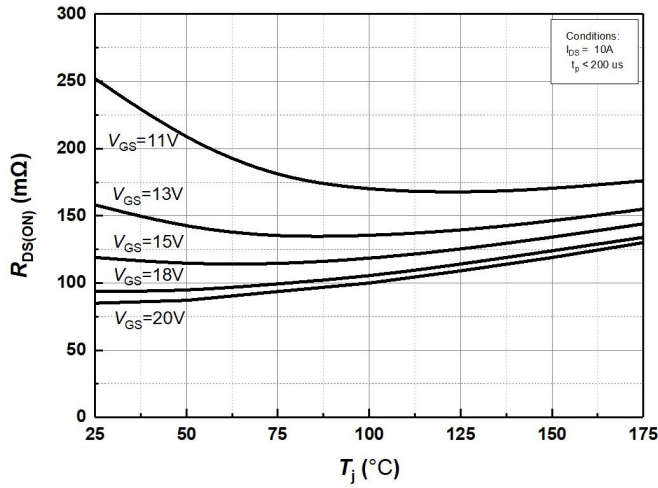


Figure 5. On-Resistance vs. Temperature for Various Gate Voltage

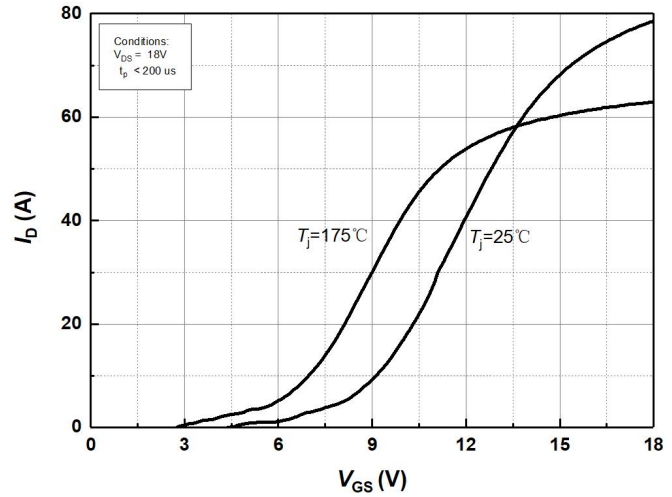


Figure 6. Transfer Characteristics for Various Junction Temperatures

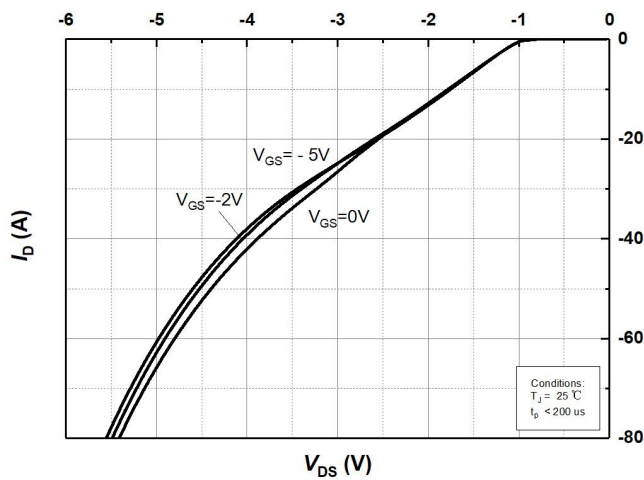


Figure 7. Body Diode Characteristics at $T_j=25^\circ C$

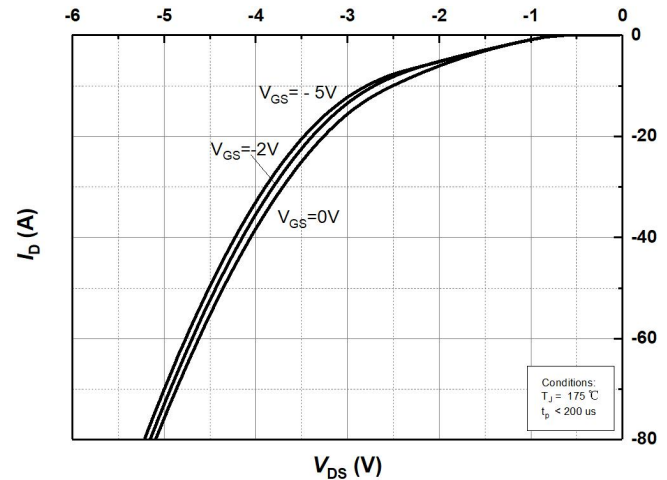


Figure 8. Body Diode Characteristics at $T_j=175^\circ C$

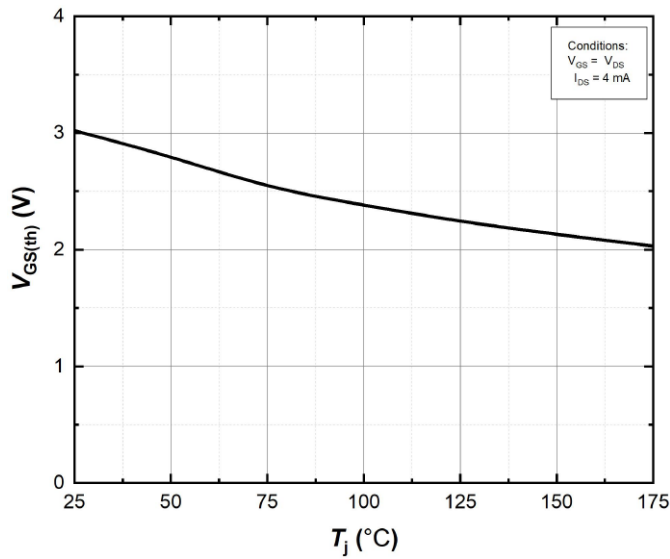


Figure 9. Threshold Voltage vs. Temperature

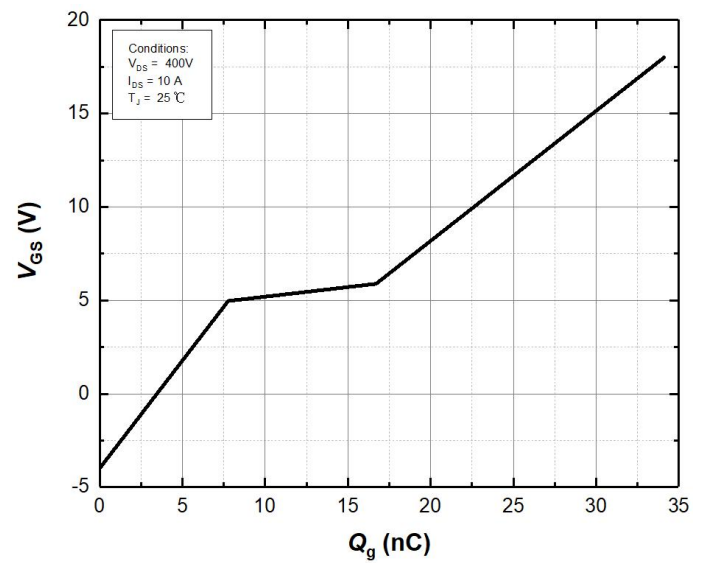


Figure 10 Gate Charge Characteristics

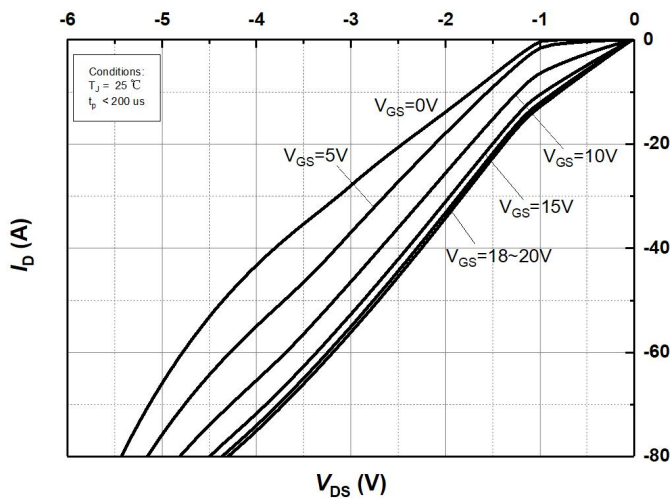


Figure 11. 3rd Quadrant Characteristic at $T_J=25\text{ °C}$

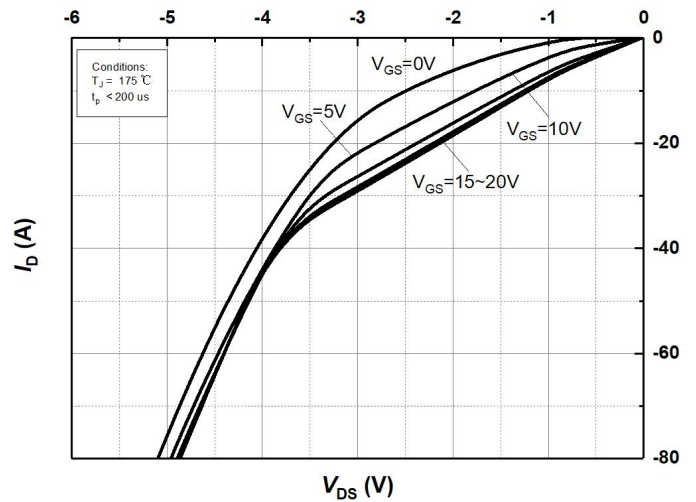


Figure 12. 3rd Quadrant Characteristic at $T_J=175\text{ °C}$

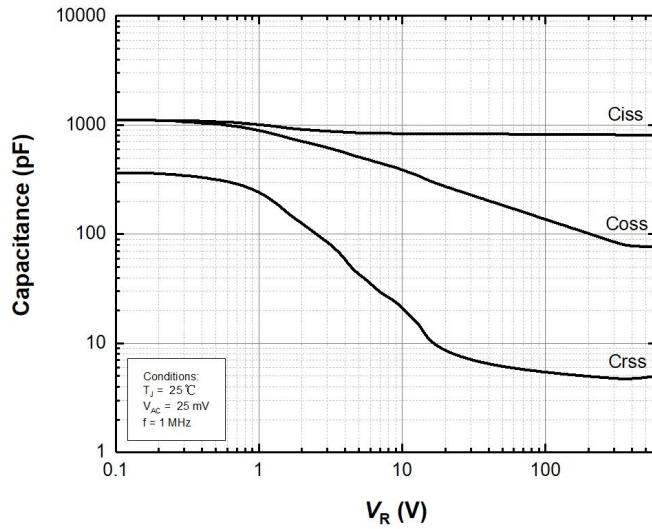


Figure 13. Capacitances vs. Drain-Source Voltage (0 – 600V)

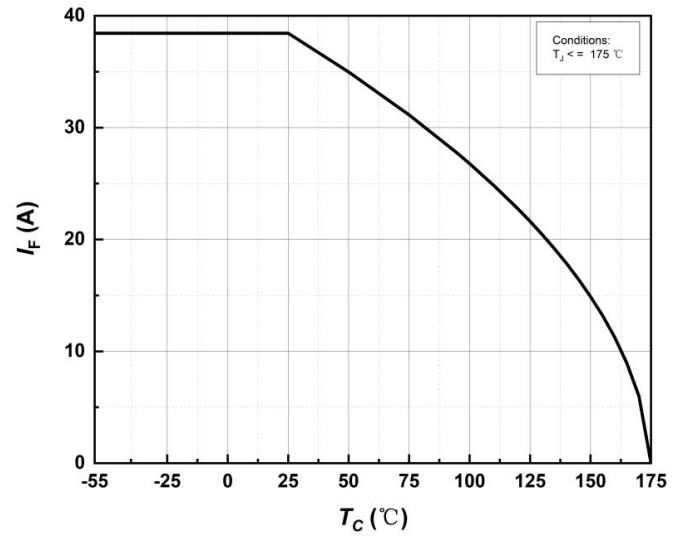


Figure 14. Continuous Drain Current Derating vs Case Temperature

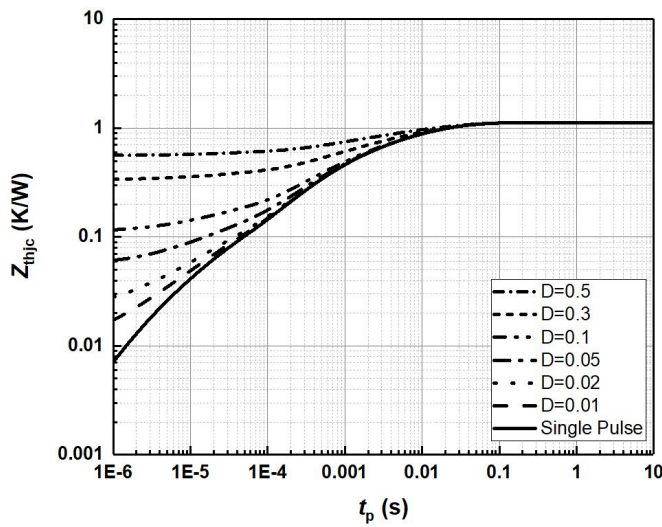


Figure 15. Transient Thermal Impedance (Junction – Case)

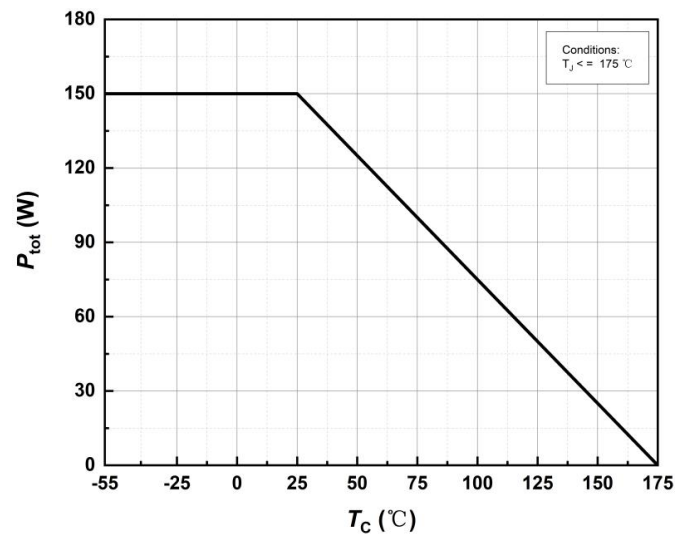


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

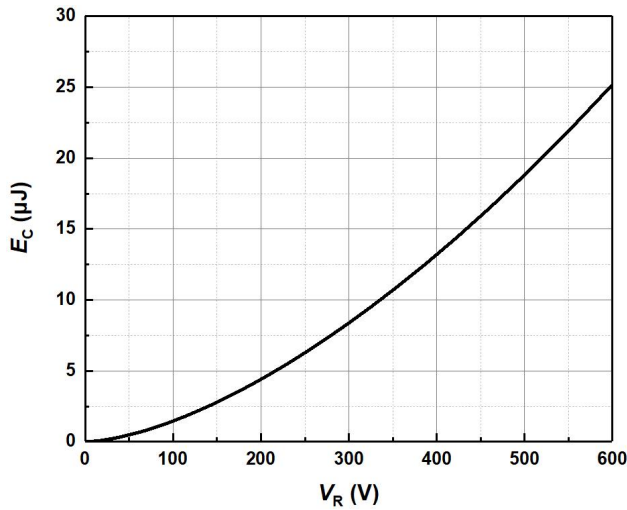


Figure 17. Output Capacitor Stored Energy

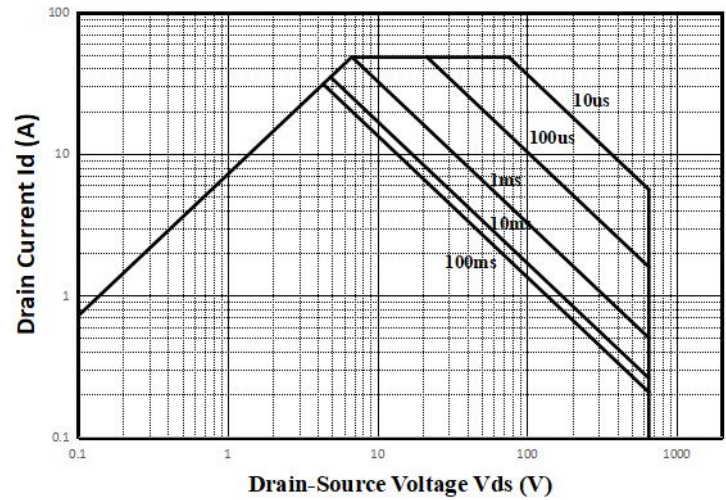


Figure 18. Safe Operating Area

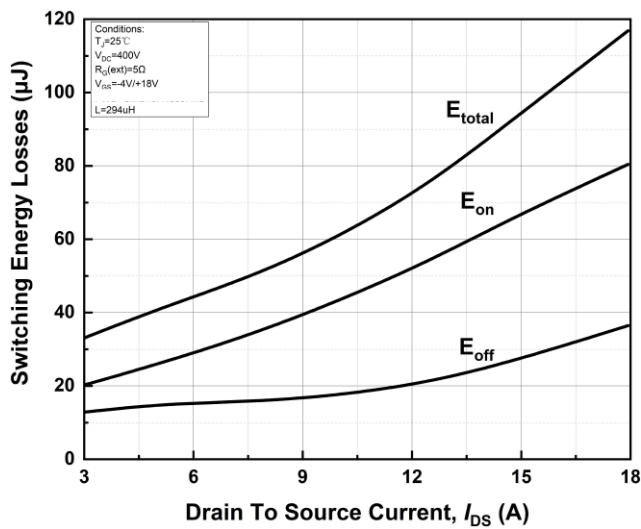


Figure 19. Clamped Inductive Switching Energy vs. Drain Current($V_{DD} = 400\text{V}$)

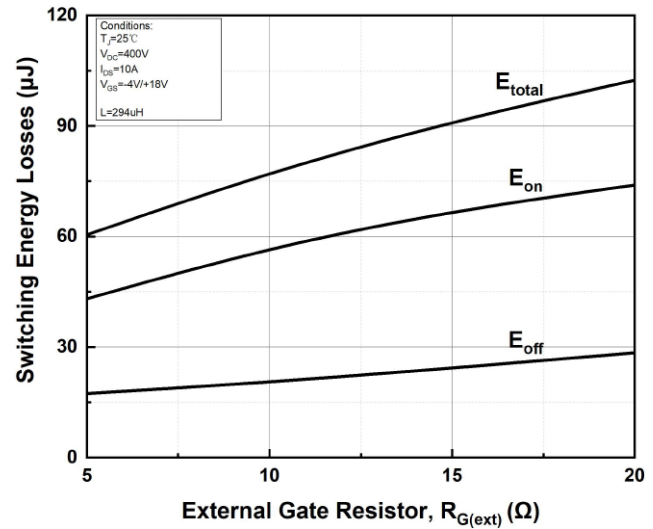


Figure 20. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

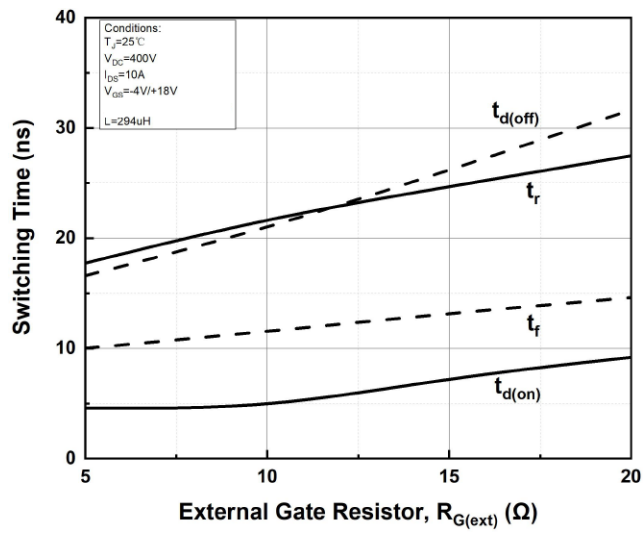
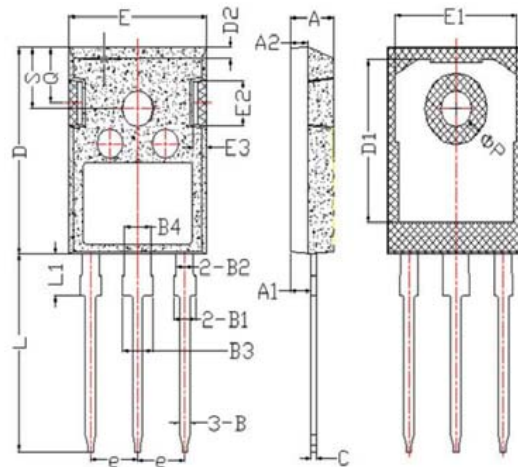


Figure 21. Switching Times vs. $R_{G(ext)}$



Package Dimensions

Package TO-247



Items	Values(mm)	
	MIN	MAX
A	4.6	5.2
A1	2.2	2.6
B	0.9	1.4
B1	1.75	2.35
B2	1.75	2.15
B3	2.8	3.35
B4	2.8	3.15
C	0.5	0.7
D	20.6	21.3
D1	16	18
E	15.5	16.1
E1	13	14.7
E2	3.8	5.3
E3	0.8	2.6
e	5.2	5.2
L	19	20.5
L1	3.9	4.6
Φp	3.3	3.7
Q	5.2	6
S	5.8	6.6



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