

## SCT4065DEHR-VB Datasheet

### N-Channel 650V (D-S) SiC Power MOSFET

PRODUCT SUMMARY		
V <sub>DS</sub> (V)	650	
R <sub>DS(on)</sub> at 25 °C (Ω)	V <sub>GS</sub> = 18 V	0.05
Q <sub>g</sub> (nC)	90	

#### FEATURES

- Low figure-of-merit (FOM) R<sub>on</sub> x Q<sub>g</sub>
- Low input capacitance (C<sub>iss</sub>)
- Reduced switching and conduction losses
- Ultra low gate charge (Q<sub>g</sub>)
- Avalanche energy rated (UIS)



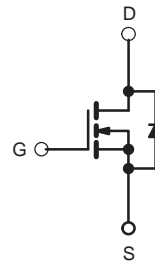
#### APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- DC/DC converter

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Top View



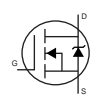
N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DS</sub>	650	V
Gate-Source Voltage	V <sub>GS</sub>	-10 / +22	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	V <sub>GS</sub> at 18 V	T <sub>C</sub> = 25 °C	40
		T <sub>C</sub> = 100 °C	32
Pulsed Drain Current <sup>a</sup>	I <sub>DM</sub>	120	A
Linear Derating Factor		2.1	W/°C
Single Pulse Avalanche Energy <sup>b</sup>	E <sub>AS</sub>	225	mJ
Maximum Power Dissipation	P <sub>D</sub>	187	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Drain-Source Voltage Slope	dV/dt	T <sub>J</sub> = 125 °C	50
Reverse Diode dV/dt <sup>d</sup>		15	
Soldering Recommendations (Peak Temperature) <sup>c</sup>	for 10 s	260	°C

#### Notes

- Repetitive rating; pulse width limited by maximum junction temperature.
- V<sub>DD</sub> = 100 V, starting T<sub>J</sub> = 25 °C, L = 0.5mH, R<sub>g</sub> = 25 Ω, I<sub>AS</sub> = 30A.
- 1.6 mm from case.
- I<sub>SD</sub> ≤ I<sub>D</sub>, dI/dt = 100 A/μs, starting T<sub>J</sub> = 25 °C.

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	$R_{thJA}$	-	40	°C/W
Maximum Junction-to-Case (Drain)	$R_{thJC}$	-	0.8	

SPECIFICATIONS ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
<b>Static</b>							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$		650	-	-	V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	Reference to $25\text{ }^\circ\text{C}$ , $I_D = 1\text{ mA}$		-	0.70	-	V/°C
Gate-Source Threshold Voltage (N)	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 10\text{ mA}$		2	-	5	V
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = +22\text{ V}$		-	-	100	nA
		$V_{GS} = -10\text{ V}$		-	-	100	$\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$		-	10	-	$\mu\text{A}$
		$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$		-	-	100	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 18\text{ V}$	$I_D = 20\text{ A}$	-	0.05	-	$\Omega$
Forward Transconductance	$g_{fs}$	$V_{DS} = 0\text{ V}, I_D = 30\text{ A}$		-	12	-	S
<b>Dynamic</b>							
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V},$ $V_{DS} = 600\text{ V},$ $f = 1\text{ MHz}$		-	2000	-	pF
Output Capacitance	$C_{oss}$			-	175	-	
Reverse Transfer Capacitance	$C_{rss}$			-	9	-	
Effective Output Capacitance, Energy Related <sup>a</sup>	$C_{o(er)}$	$V_{DS} = 0\text{ V to } 400\text{ V}, V_{GS} = 0\text{ V}$		-	156	-	
Effective Output Capacitance, Time Related <sup>b</sup>	$C_{o(tr)}$			-	268	-	
Total Gate Charge	$Q_g$	$V_{GS} = -5/18\text{ V}$	$I_D = 20\text{ A}, V_{DS} = 400\text{ V}$	-	70	-	nC
Gate-Source Charge	$Q_{gs}$			-	20	-	
Gate-Drain Charge	$Q_{gd}$			-	23	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 400\text{ V}, I_D = 20\text{ A},$ $V_{GS} = -5/18\text{ V}, R_g = 2\text{ }\Omega$		-	12	15	ns
Rise Time	$t_r$			-	10	13	
Turn-Off Delay Time	$t_{d(off)}$			-	20	-	
Fall Time	$t_f$			-	10	-	
Gate Input Resistance	$R_g$	$f = 1\text{ MHz}, \text{ open drain}$		-	2.2	-	$\Omega$
<b>Drain-Source Body Diode Characteristics</b>							
Continuous Source-Drain Diode Current	$I_S$	MOSFET symbol showing the integral reverse p - n junction diode 		-	-	40	A
Pulsed Diode Forward Current	$I_{SM}$			-	-	120	
Diode Forward Voltage	$V_{SD}$	$T_J = 25\text{ }^\circ\text{C}, I_S = 20\text{ A}, V_{GS} = 0$		-	-	4.1	V
Reverse Recovery Time	$t_{rr}$	$T_J = 25\text{ }^\circ\text{C}, I_F = I_S = 20\text{ A},$ $di/dt = 1000\text{ A}/\mu\text{s}, V_R = 400\text{ V}$		-	20	-	ns
Reverse Recovery Charge	$Q_{rr}$			-	60	-	$\mu\text{C}$
Reverse Recovery Current	$I_{RRM}$			-	10	-	A

**Notes**

- a.  $C_{oss(er)}$  is a fixed capacitance that gives the same energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 60 %  $V_{DSS}$ .
- b.  $C_{oss(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 60 %  $V_{DSS}$ .

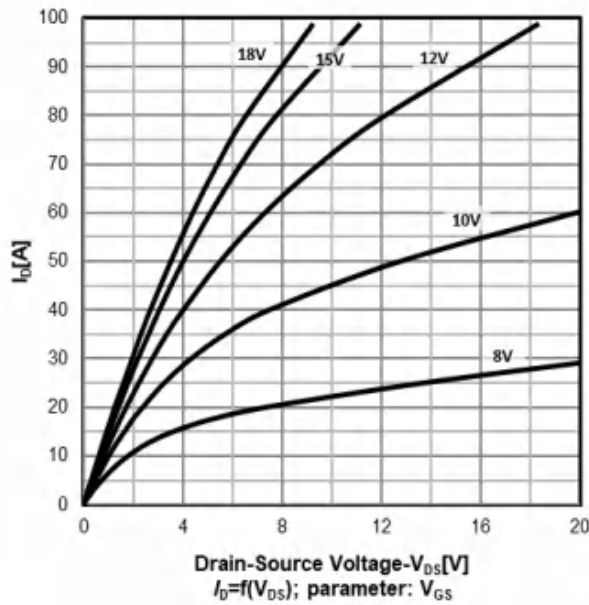
Safe operating area Tc=25 °C  
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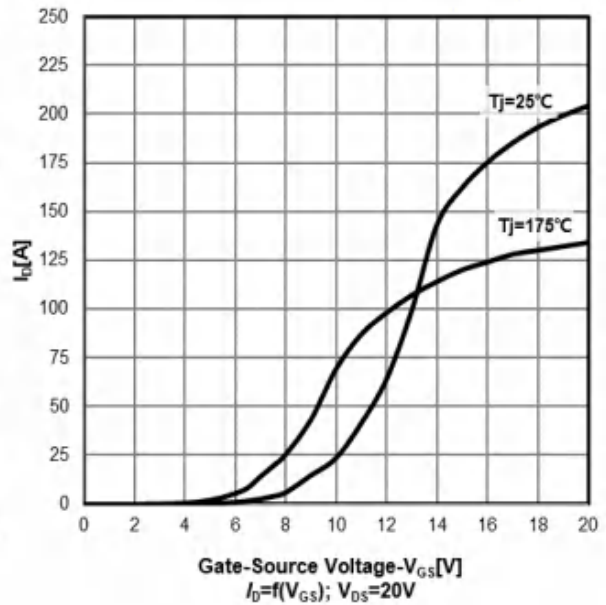
On-Region characteristics Tj=25 °C



On-Region characteristics Tj=175 °C



Transfer characteristics



Drain current vs temperature



Power dissipation



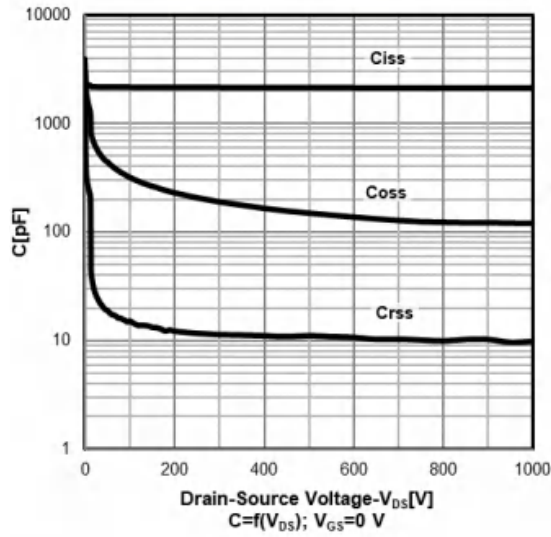
Threshold voltage vs temperature



Normalized On-resistance vs temperature



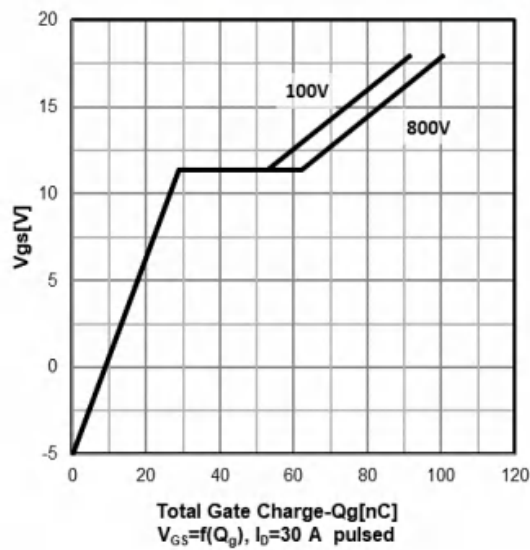
Typ. capacitances



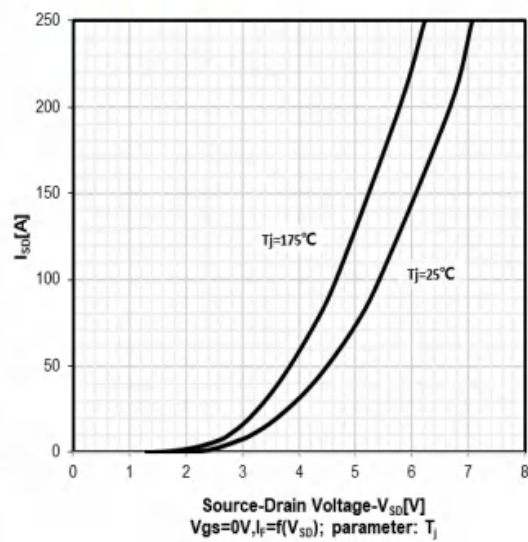
$C_{oss}$  stored energy



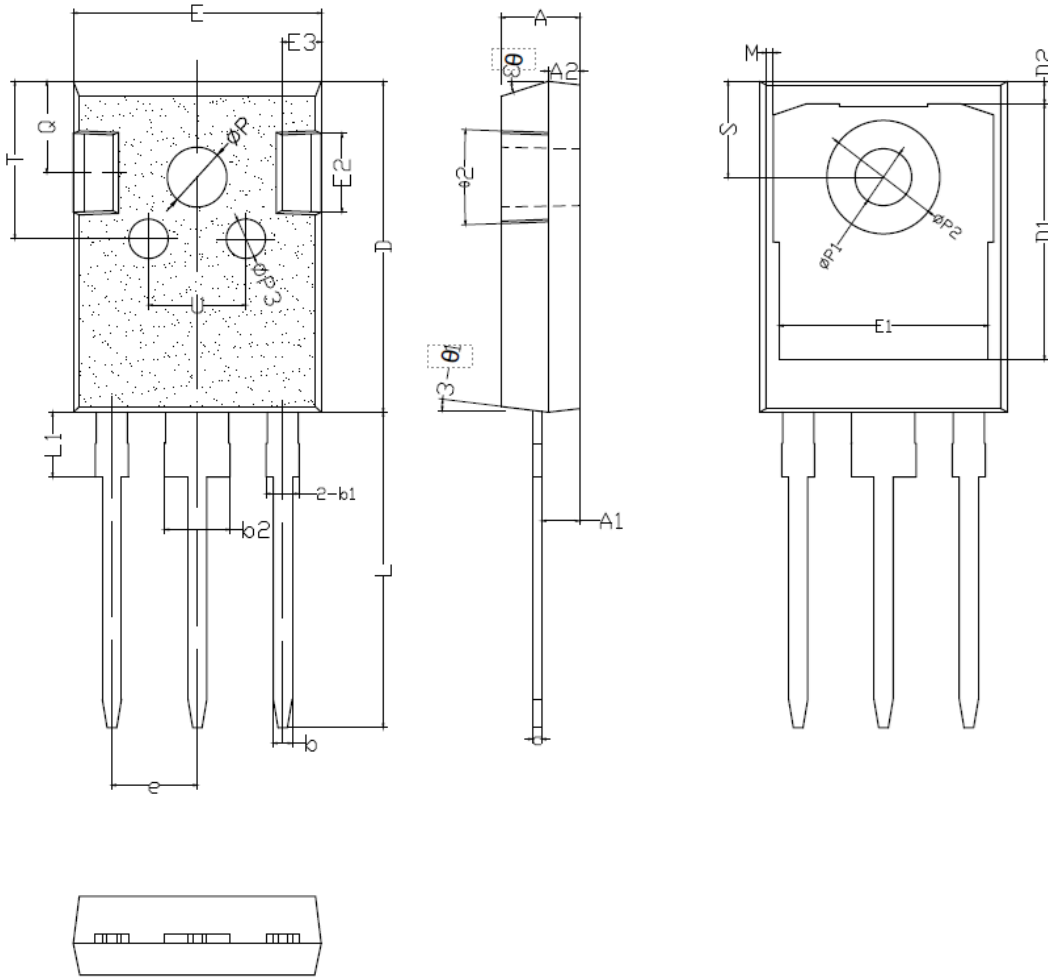
Typ. gate charge characteristics



Diode forward voltage characteristics  
 $T_j=25^\circ C/175^\circ C$



TO-247 PACKAGE OUTLINE DIMENSIONS



SYMBOL	mm		
	MIN	NOM	MAX
*A	4.90	5.00	5.10
*A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
*b	1.15	1.20	1.25
*b1	1.95	2.10	2.25
*b2	2.95	3.10	3.25
*c	0.55	0.60	0.65
*D	20.90	21.00	21.10
D1	16.35	16.55	16.75
D2	1.05	1.20	1.35

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