

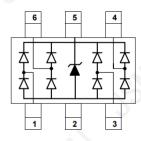
Description

The IP4220CZ6,125-JSM is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution forprotecting voltage sensitive high-speed data lines.

The IP4220CZ6,125-JSM has an ultra-low capacitance with a typical value at 0. 6 pF, and complies with the IEC 61000-4-2 (ESD) standard with ±15kV air and ±8kV conta ct discharge. It is assembled into a 6-pin lead-free SOT23 -6 package. The combination of small size, ultra low capacitance, and high ESD surge capability make it ideal for use in applications such as multimedia, and other high speed ports.



SOT23-6



Circuit Diagram

Features

◆ Ultra low capacitance: 0. 6pF typical (I/O-GND)

Ultra low leakage: nA level

Low operating voltage: 5.0V

◆ Up to 4 data lines and one power line protects

Low clamping voltage

Complies with following standards:

- IEC 61000-4-2 (ESD) immunity test

Air: ±20kV; discharge: ±15kV

- IEC61000-4-5 (Lightning) 4.5A (8/20μs)

♦ SOT23-6 Package

RoHS Compliant

Applications

- Monitors and flat panel displays
- Set-top box and Digital TV
- Video graphics cards
- Digital Video Interface (DVI)
- Notebook Computers
- PCI Express and Serial SATA Ports



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

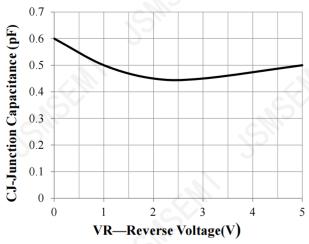
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs,I/O-GND)	Ppk	60	W
Peak Pulse Power (8/20µs,Vcc-GND)	Ppk	300	W
Peak Pulse Current (8/20µs,I/O-GND)	IPP	4.5	А
Peak Pulse Current (8/20µs,Vcc-GND)	IPP	17	Α
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	VESD,VDD VESD,I/O	±20±15	kV
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

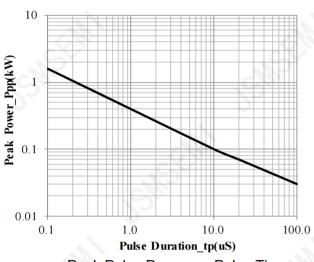
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	VRWM	Pin 5 to GND,I/O-GND			5. 0	V
Breakdown Voltage	VBR	IT= 1mA(Pin 5 to GND,I/O_GND)	6.0	7.5	8.5	V
Reverse Leakage Current	lR	VRWM= 5. 0V			0.5	μА
Forward Breakdown Voltage	VF	IF = 15mA,GND to Pin 5/IO		0.8	1.0	V
Clamping Voltage	VC	IPP = 4.5A (8 x 20µs pulse, I/O to GND)		14.0	15.0	V
Clamping Voltage	VC	IPP = 17A (8 x 20µs pulse, Pin 5 to GND)		16.0	18. 0	V
Junction Capacitance	Cl	Vpin5= 5V, I/O=0V, f = 1MHz,I/O-GND	12,	0.6	0.7	pF
Junction Capacitance	CJ	Vpin5= 5V, I/O=0V, f = 1MHz,I/O-I/O pins		0.3	0.4	pF



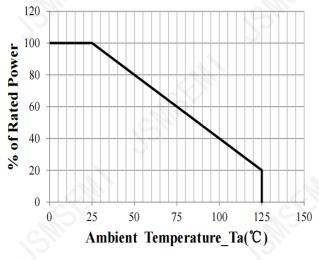
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



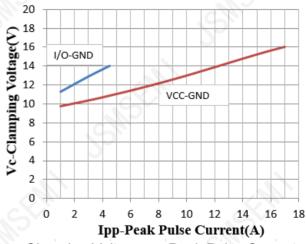
Junction Capacitance vs. Reverse Voltage



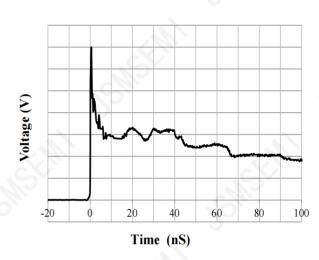
Peak Pulse Power vs. Pulse Time



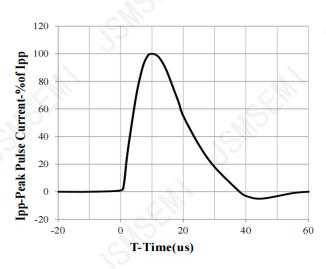
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



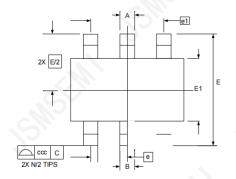
IEC61000-4-2 Pulse Waveform

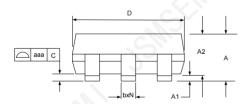


8 X 20us Pulse Waveform



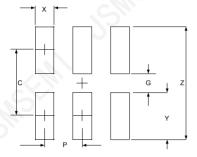
SOT23-6 Package Outline Drawing





	DIMENSIONS						
	MILLIMETERS			INCHES			
SYM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.90		1.45	0.035		0.057	
A1	0.00		0.15	0.000		0.006	
A2	0.90	1.15	1.30	0.035	0.045	0.051	
b	0.25		0.50	0.010		0.020	
С	0.08		0.22	0.003		0.009	
D	2.80	2.90	3.10	0.110	0.114	0.122	
E1	1.50	1.60	1.75	0.060	0.063	0.069	
Е	2.80 BSC			0.110 BSC			
е	0.95 BSC			0.037 BSC			
e1	1.90 BSC			0.075 BSC			
N	6			6			
aaa	0.10			0.004			
CCC	0.20				0.008		

Suggested Land Pattern



CVM	DIMENSIONS			
SYM	MILLIMETERS	INCHES		
С	2.50	0.098		
G	1.40	0.055		
Р	0.95	0.037		
X	0.60	0.024		
Υ	1.10	0.043		
Z	3.60	0.141		



Revision History

Rev.	Change	Date
V1.0	Initial version	2/23/2024

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